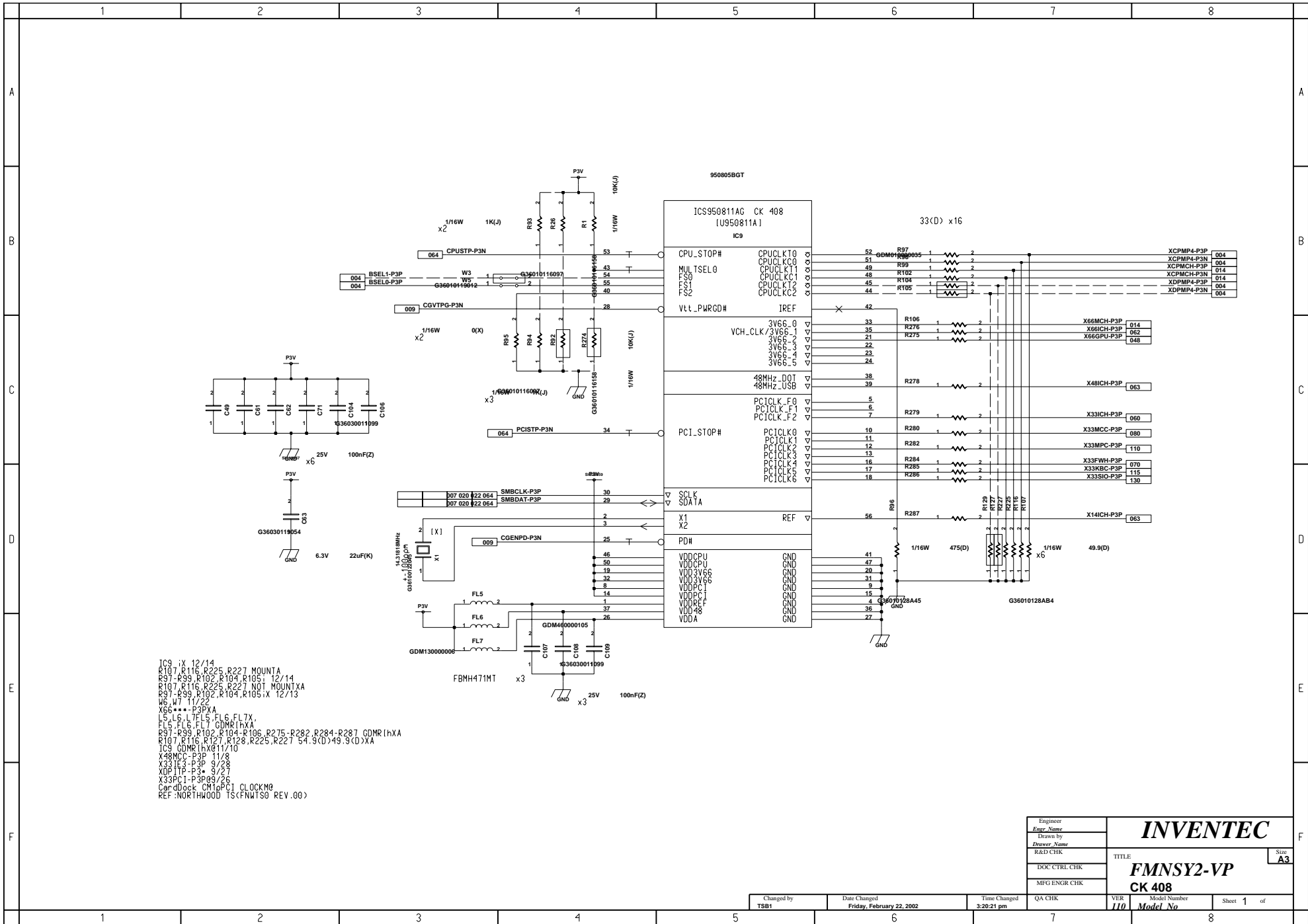


Engineer	
Engr. Name	
Drawn by	
Drawer Name	
R&D CHK	
DOC CTRL CHK	
MFG ENGR CHK	
TITLE	<b>FMNSY2-VP</b>
	<b>BLOCK DIAGRAM</b>
Model Number	Model No
Sheet	of

Changed by	Date Changed	Time Changed	QA CHK	VER	Model Number	Sheet	of
TSB1	Friday, February 22, 2002	3:26:25 pm		110	Model No		

**INVENTEC**

Size **A3**



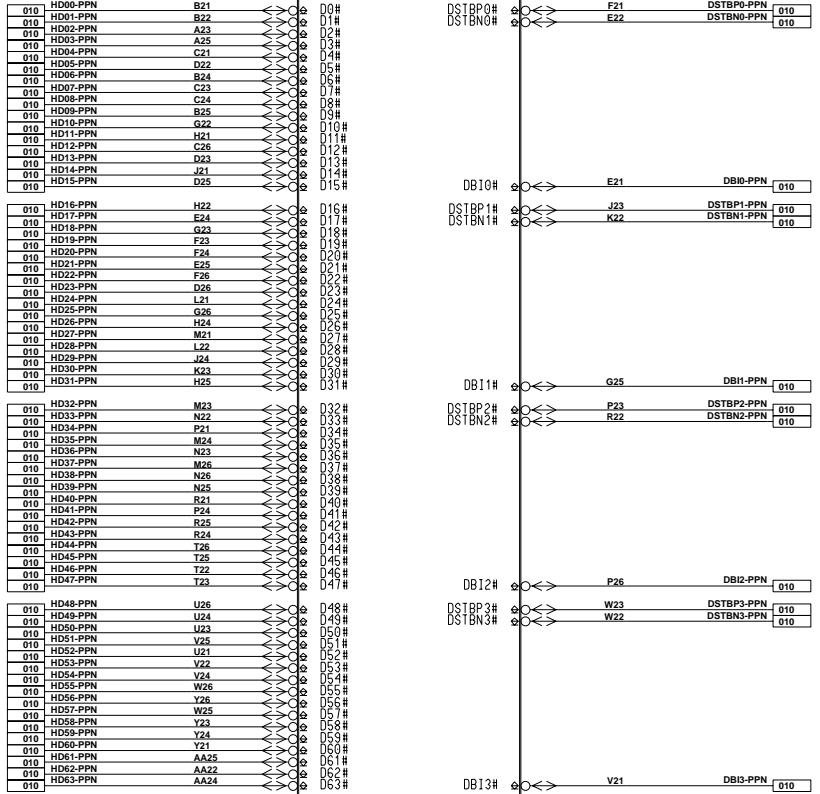
IC9: iX 12/14  
 R107, R116, R225, R227 MOUNTA  
 R97, R99, R102, R104, R105, 12/14  
 R107, R116, R225, R227, N01 MOUNTXA  
 R97, R99, R102, R104, R105, X 12/13  
 W6, W7, 11/22  
 X66\*\*\*-P3PX  
 FL5, FL6, FL9, FLX  
 FL5, FL6, FL9, GDMRHX  
 R97, R99, R102, R104, R106, R275, R282, R284, R287 GDMRHX  
 R107, R116, R127, R128, R225, R227, 54.9(D), 49.9(D) XA  
 IC9 GDMRHX11/10  
 X66MCH-P3P 11/2  
 X33ICH-P3P 9/28  
 XDP1TP-P3P 9/27  
 X33PC1-P3P89/26  
 GdmDock GM10PC1 CLOCKM0  
 REF: NORTHWOOD TS(FNMTS0 REV.00)

Engineer Eng Name	<b>INVENTEC</b> TITLE: <b>FMNSY2-VP</b> <b>CK 408</b> Model Number Model No	Size <b>A3</b>
Drawn by Drawer Name		
R&D CHK		
DOC CTRL CHK		
MFG ENGR CHK		

Changed by TSB1	Date Changed Friday, February 22, 2002	Time Changed 3:20:21 pm	QA CHK	VER 110	Sheet 1 of
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Mobile Pentium 4 1/6  
[ISMPGA479M]  
IS3



[PSB Data 4x]  
[Source Synchronous]

GDM250000012

IS3 AMPRLN\*X 11/20  
REF:NORTHWOOD TS\FNWT50\SH.2 REV.00

Engineer <i>Engr Name</i>	<b>INVENTEC</b>
Drawn by <i>Drawer Name</i>	
R&D CHK	
DOC CTRL CHK	
MFG ENGR CHK	
TITLE	FMNSY2-VP mPGA479M 1
Model Number	Size <b>A3</b>

Changed by <b>TSB1</b>	Date Changed <b>Friday, February 22, 2002</b>	Time Changed <b>3:21:11 pm</b>	QA CHK	VER <b>110</b>	Model Number <i>Model No</i>	Sheet <b>2</b> of
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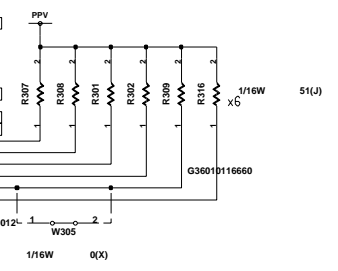
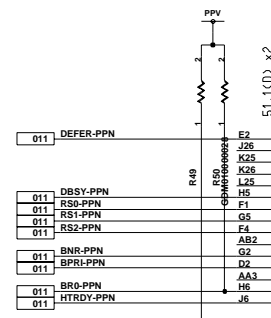
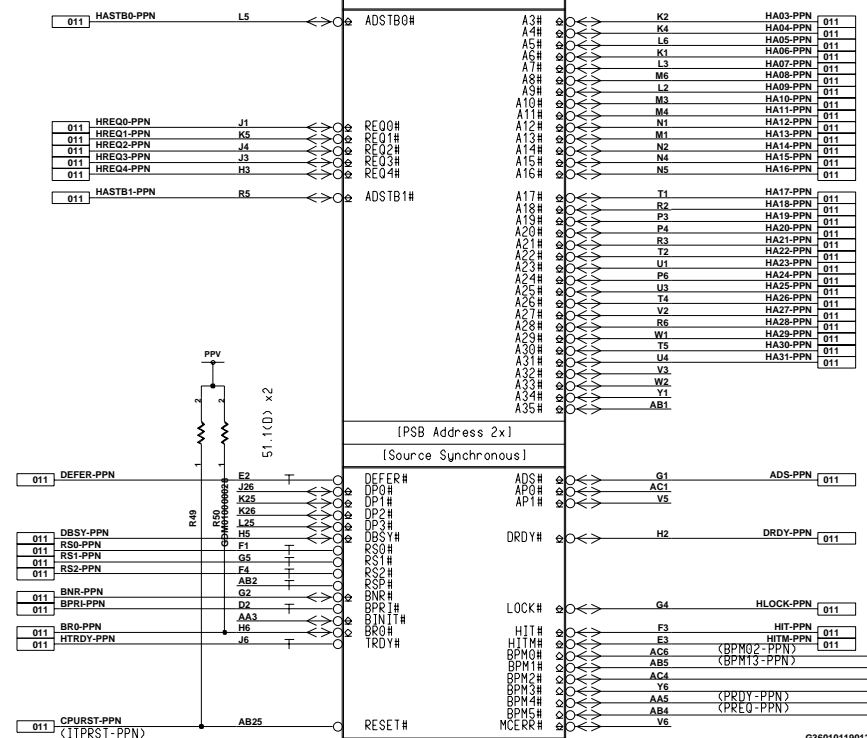
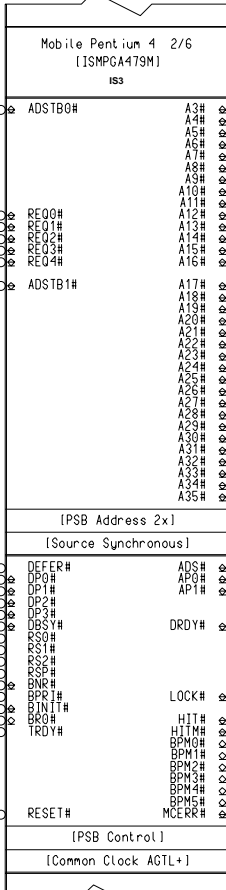
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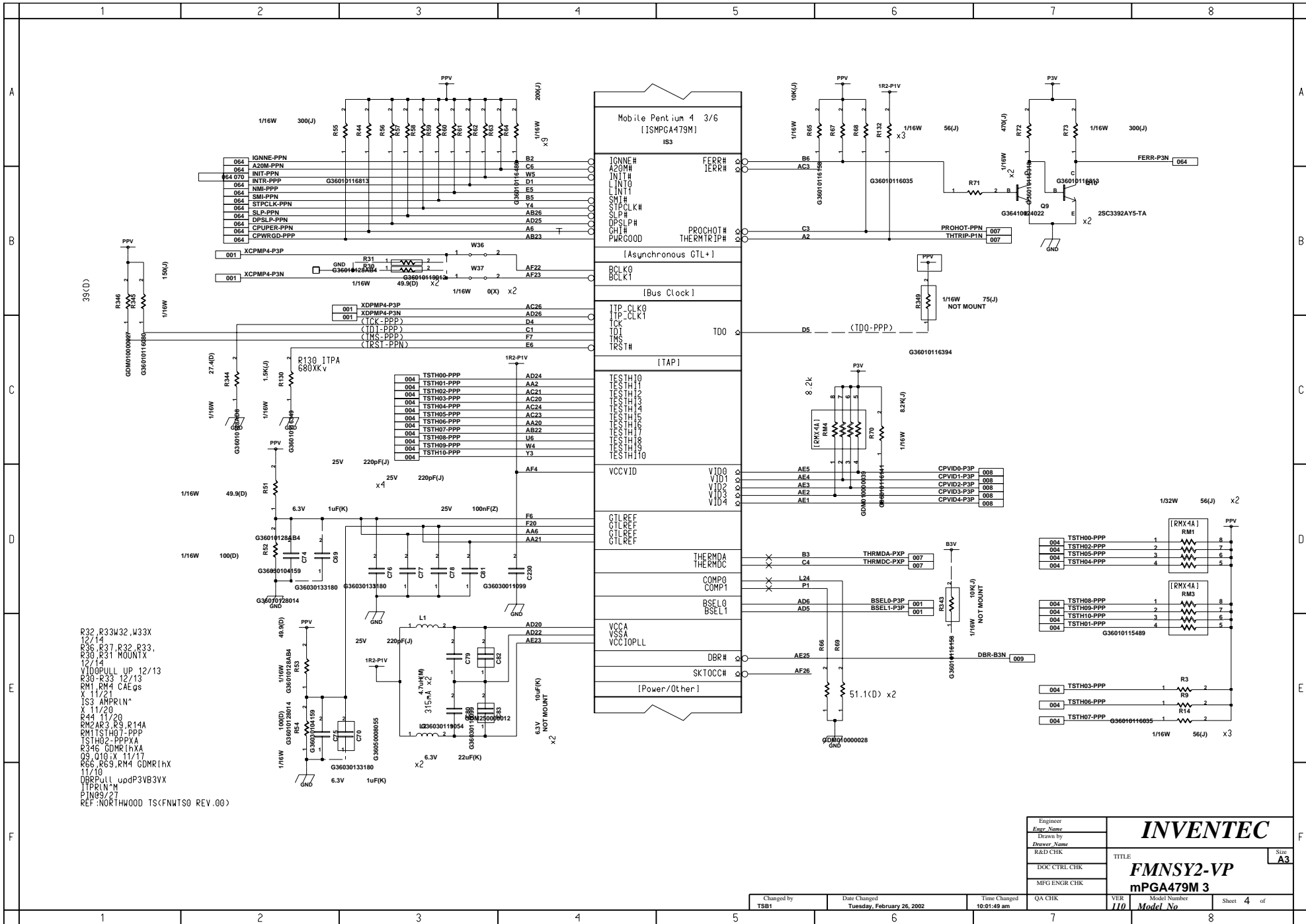


IS3 AMPRLN\*X 11/20  
R301 R302 W305A  
R40 550 DDMRLHX 11/17  
ITPRN M69/27  
REF:NORTHWOOD TS(FNWT50 REV.00)

GDM250000012

Engineer Engr Name	<p align="center"><b>INVENTEC</b></p> <p align="center">TITLE: <b>FMNSY2-VP</b></p> <p align="center"><b>mPGA479M 2</b></p>
Drawn by Drawer Name	
R&D CHK	
DOC CTRL CHK	
MFG ENGR CHK	
Model Number	Model No
Sheet 3 of	Sheet 3 of

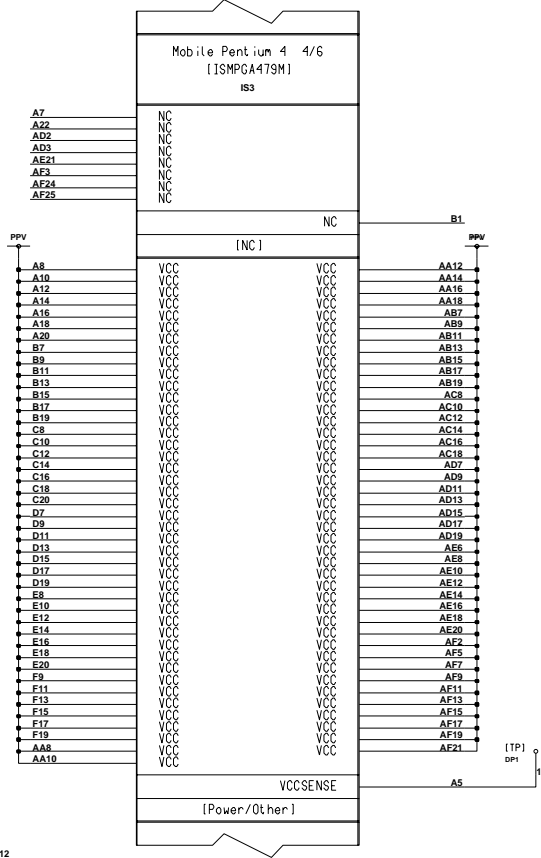
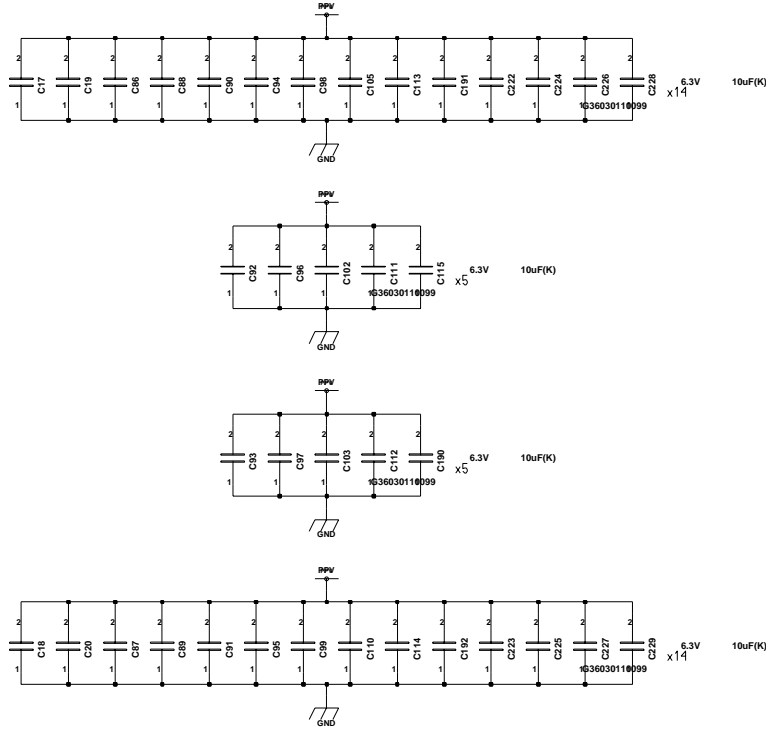
Changed by TSB1	Date Changed Friday, February 22, 2002	Time Changed 3:21:57 pm
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R32 R33W32,W33X  
 12/14  
 R36 R37 R32 R33,  
 R30 R31 MOUNTX  
 12/14  
 VIDOPULL UP 12/13  
 R30 R33 12/13  
 RM1 RM4 CAEGS  
 X 1/12/1  
 IS3 AMPRLN\*  
 X 1/1/20  
 R44 1/1/20  
 RM2AR3,R9 R14A  
 RM1TSTH07-PPP  
 TSTH02-PPPXA  
 R346 GDMR1XA  
 R9,016,X 1/1/17  
 R66,R69,RM4 GDMR1X  
 1/1/10  
 DRPULL UPDP3VB3VX  
 ITPRLN\*  
 PIN09/27  
 REF: NORTHWOOD TS(FNWT50 REV.00)

Engineer		<b>INVENTEC</b> TITLE: <b>FMNSY2-VP</b> <b>mPGA479M 3</b>	Model Number <b>Mod1 No</b>	Sheet 4 of
Engr Name				
Drawn by				
Drawn Name				
R&D CHK				
DOC CTRL CHK				
MFG ENGR CHK				

Changed by	Date Changed	Time Changed	QA CHK	VER
TSB1	Tuesday, February 26, 2002	10:01:49 am		110

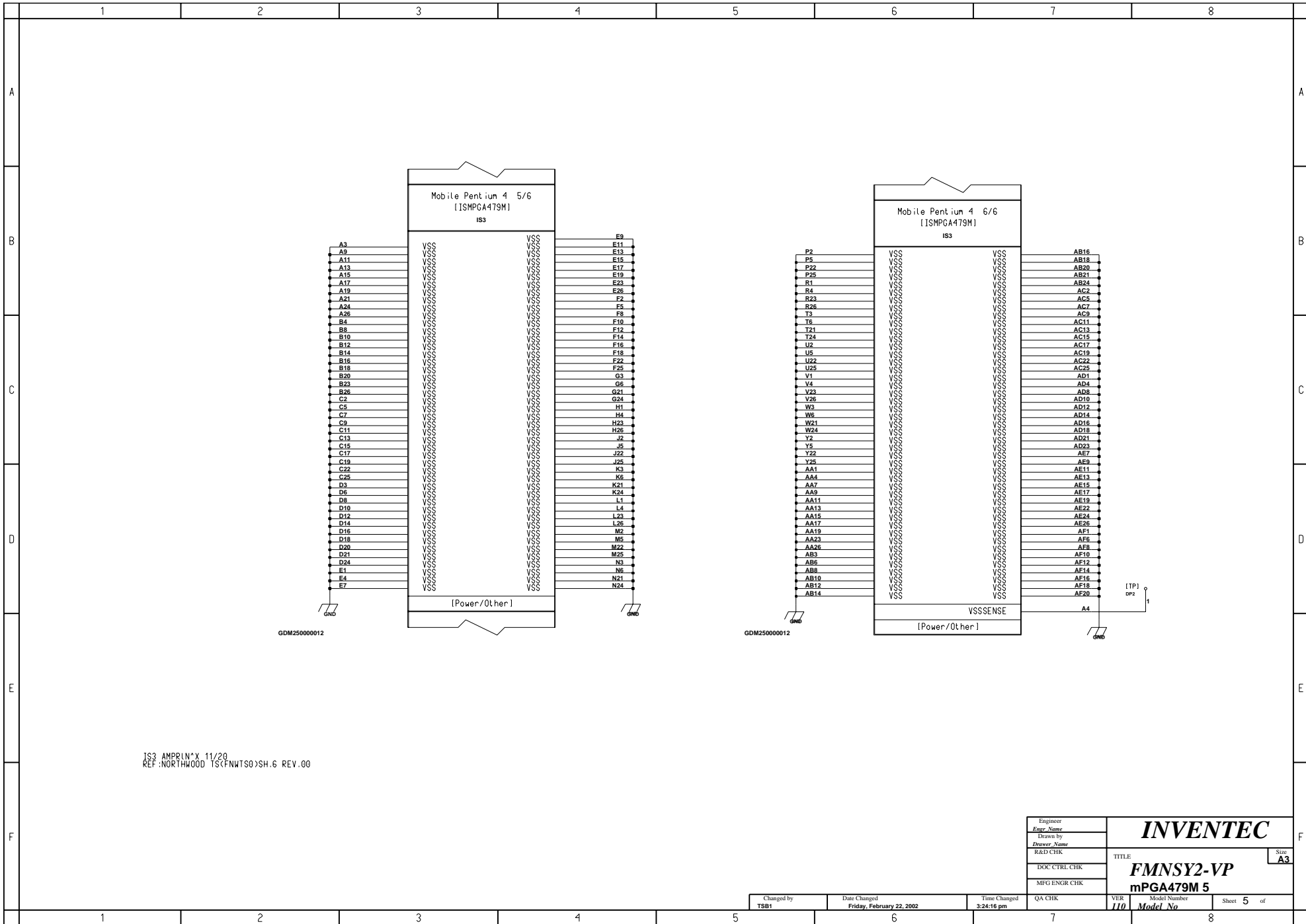


GDM250000012

IS3 AMPPLN\*X 11/20  
REF:NORTHWOOD TS\FNWTSG\SH.5 REV.00

Engineer <i>Engy Name</i>	<b>INVENTEC</b>  <b>FMNSY2-VP</b> <b>mPGA479M 4</b>	Size <b>A3</b>
Drawn by <i>Drawer Name</i>		TITLE
R&D CHK		
DOC CTRL CHK		
MFG ENGR CHK		

Changed by <b>TSB1</b>	Date Changed <b>Friday, February 22, 2002</b>	Time Changed <b>3:23:30 pm</b>	QA CHK	VER <b>110</b>	Model Number <i>Model No</i>	Sheet <b>4</b> of
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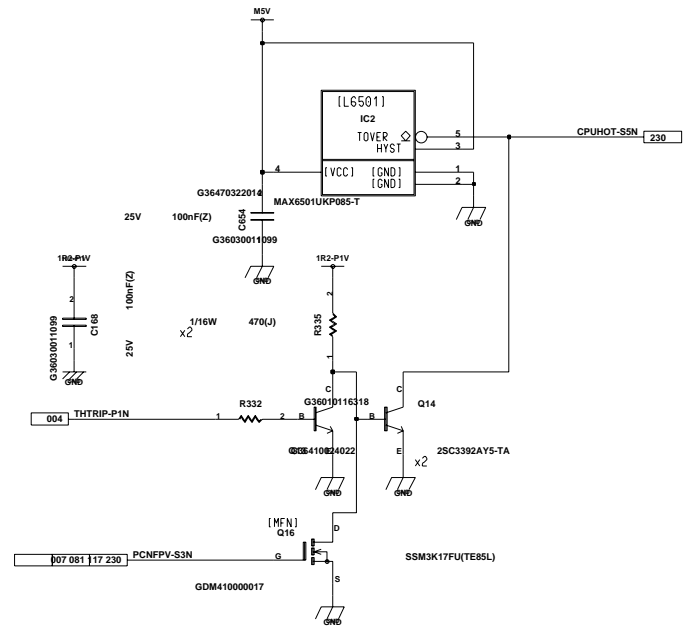
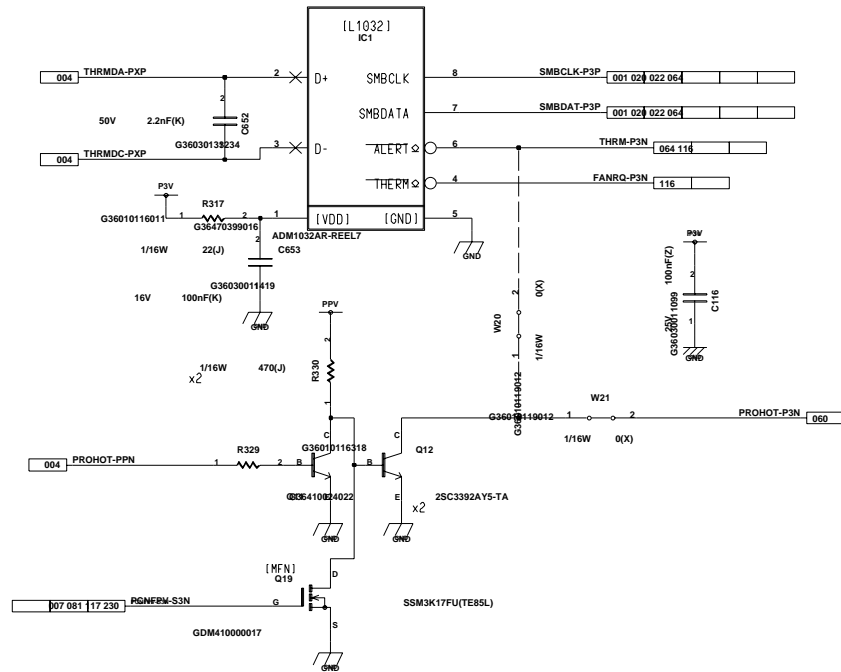


IS3 AMPRLIN X 11/20  
 REF: NORTHWOOD TS<FNWTSO>SH.6 REV.00

Engineer <i>Engr Name</i>	<b>INVENTEC</b>
Drawn by <i>Drawer Name</i>	
R&D CHK	
DOC CTRL CHK	
MFG ENGR CHK	
TITLE	FMNSY2-VP mPGA479M 5
Model Number	<i>Model No</i>

Changed by TSB1	Date Changed Friday, February 22, 2002	Time Changed 3:24:16 pm	QA CHK	VER 110	Model Number	Sheet 5 of
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Size A3



016, 019Q: X 12/6  
 W20, W21, R329, R330, 011, 012, 019 MOUNTX 11/22  
 011-014Q: XAR331011/17  
 019 11/10  
 W12, R330 (THTRIPgo) 11/10  
 R329, R330, 011 MOUNTNOT MOUNTX 10/29  
 014 NOT MOUNTMOUNTX  
 REF: FNWTS0 (NORTHWOOD TS) SH.7 REV.00

Engineer Engr Name	<b>INVENTEC</b> TITLE: <b>FMNSY2-VP</b> <b>THERMAL SENSOR</b>	Size <b>A3</b>
Drawn by Drawer Name		
R&D CHK		
DOC CTRL CHK		
MFG ENGR CHK		

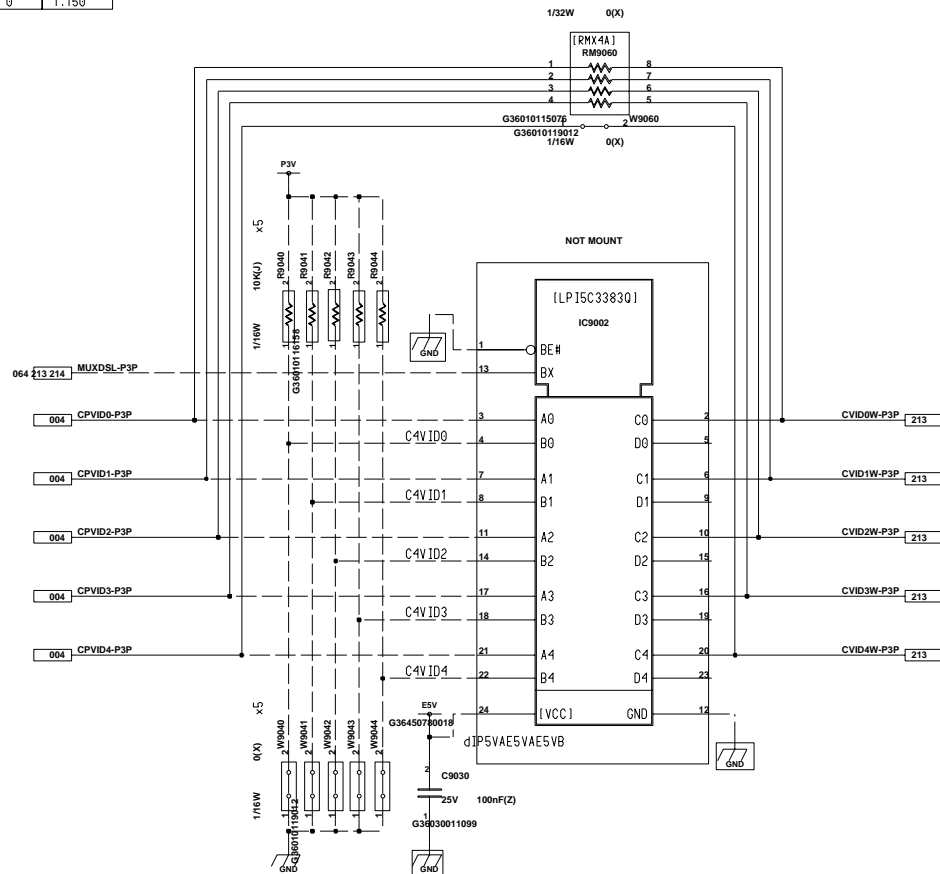
Changed by TSB1	Date Changed Friday, February 22, 2002	Time Changed 3:24:56 pm	QA CHK	VER 110	Model Number Model No	Sheet 7	of 7
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C4VID4	C4VID3	C4VID2	C4VID1	C4VID0	C4_VCC
0	1	1	1	1	1.000
0	1	1	1	1	1.050
0	1	1	0	1	1.100
0	1	0	0	0	1.150

Spec in ENTS Rev.07

NorthwoodDeeper SleepdXubN1.000VXA4HB



Engineer Engr Name	<b>INVENTEC</b> TITLE: <b>FMNSY2-VP</b> <b>C4 VOLTAGE CON.</b>	Size <b>A3</b>
Drawn by Drawer Name		
R&D CHK		
DOC CTRL CHK		
MFG ENGR CHK		

Changed by TSB1	Date Changed Friday, February 22, 2002	Time Changed 3:25:48 pm	QA CHK	VER 110	Model Number Model No	Sheet 8	of 8
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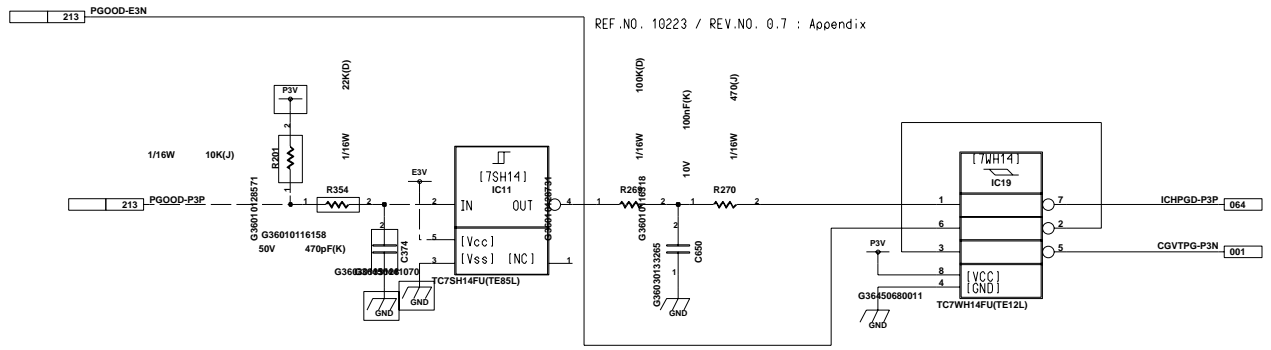
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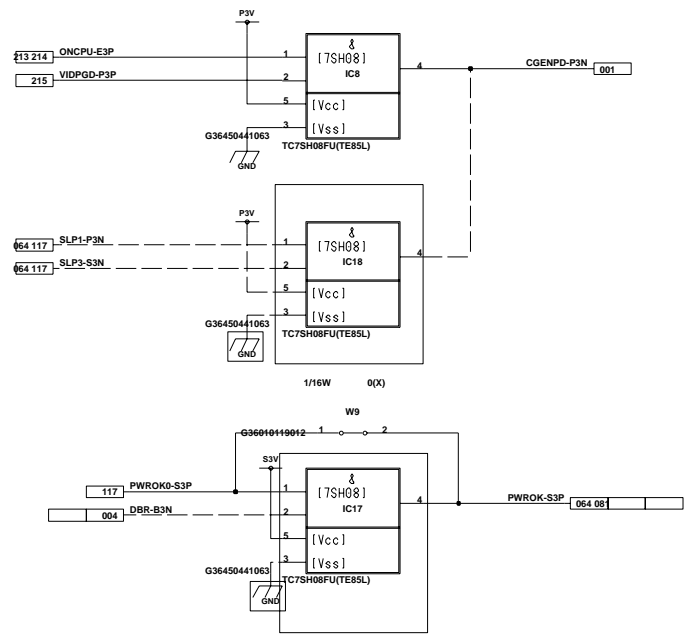
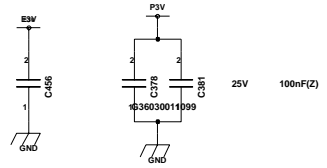
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REF.NO. 10223 / REV.NO. 0.7 : Appendix



IC10 IC12 IC13IC19X 11/21  
 IC13AIC10:XA  
 IC18 11/20  
 IC13 IC17 49AIC10:XA  
 R271 C338 11/11  
 C181010/29  
 IC13(PWROK0BRAND010 J09/26  
 REF:NORTHWOOD TS(FNW130 REV.00)

Engineer	
Engr Name	
Drawn by	
Drawer Name	
R&D CHK	
DOC CTRL CHK	
MFG ENGR CHK	
TITLE	Size
<b>FMNSY2-VP</b>	<b>A3</b>
<b>TIMING CONTROL</b>	

Changed by	Date Changed	Time Changed	QA CHK	VER	Model Number	Sheet	of
TSB1	Friday, February 22, 2002	3:26:32 pm		110	Model No	9	

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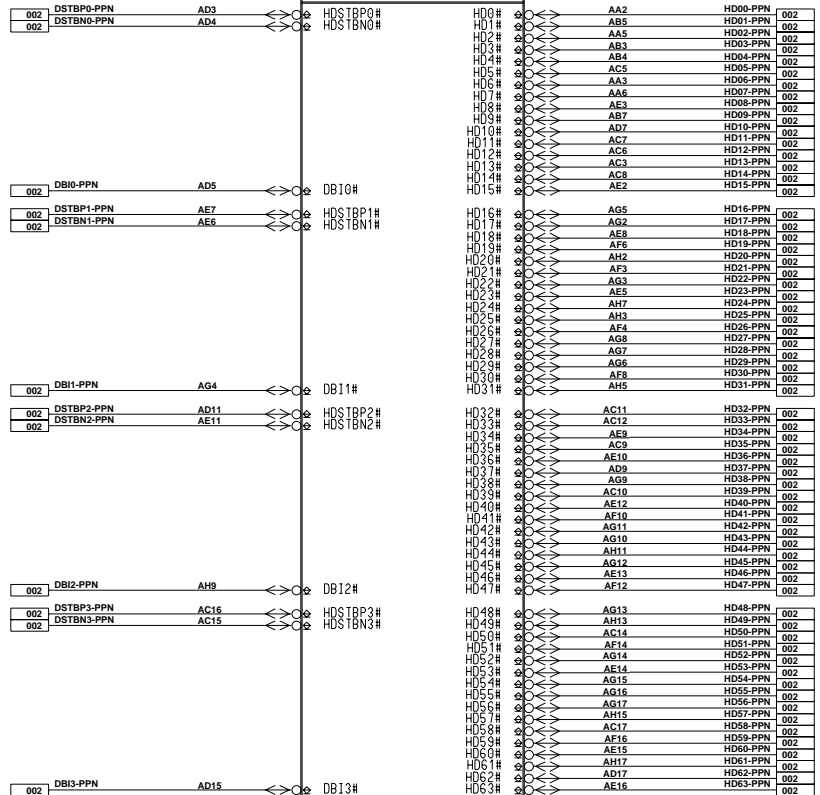
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82845MP MCH-M 1/7  
[U82845MP]  
IC14



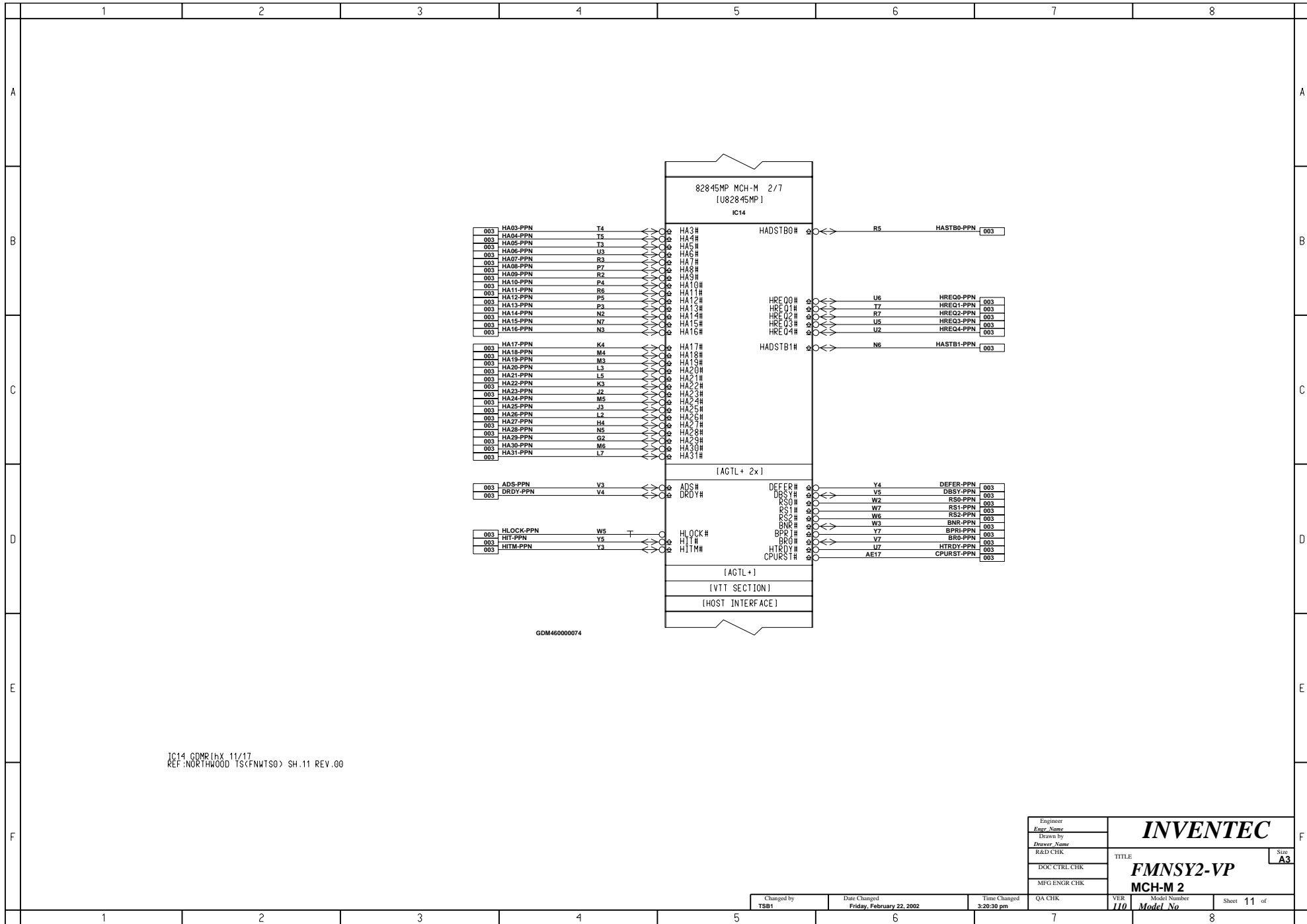
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[VTT SECTION]  
[HOST INTERFACE]

GDM46000074

IC14 GDMR1x 11/17  
REF: NORTHWOOD TS<FNWTSO> SH.10 REV.00

Engineer <i>Engr Name</i>	<b>INVENTEC</b>
Drawn by <i>Drawer Name</i>	
R&D CHK	
DOC CTRL CHK	
MFG ENGR CHK	
TITLE	Size <b>A3</b>
<b>FMNSY2-VP</b>	
<b>MCH-M 1</b>	

Changed by <b>TSB1</b>	Date Changed <b>Friday, February 22, 2002</b>	Time Changed <b>3:20:26 pm</b>	QA CHK	VER <b>110</b>	Model Number <b>Model No</b>	Sheet <b>10</b> of
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IC14 GDMR.IHX 11/17  
REF: NORTHWOOD TSKFNWTS0 SH.11 REV.00

Engineer <i>Engr Name</i>	<b>INVENTEC</b>
Drawn by <i>Drawer Name</i>	
R&D CHK	
DOC CTRL CHK	
MFG ENGR CHK	
TITLE	Size <b>A3</b>
<b>FMNSY2-VP</b>	
<b>MCH-M 2</b>	

Changed by TSB1	Date Changed Friday, February 22, 2002	Time Changed 3:20:30 pm	QA CHK	VER 110	Model Number Model No	Sheet 11 of
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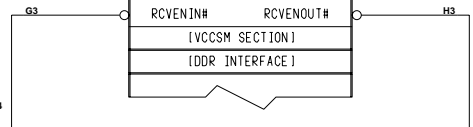
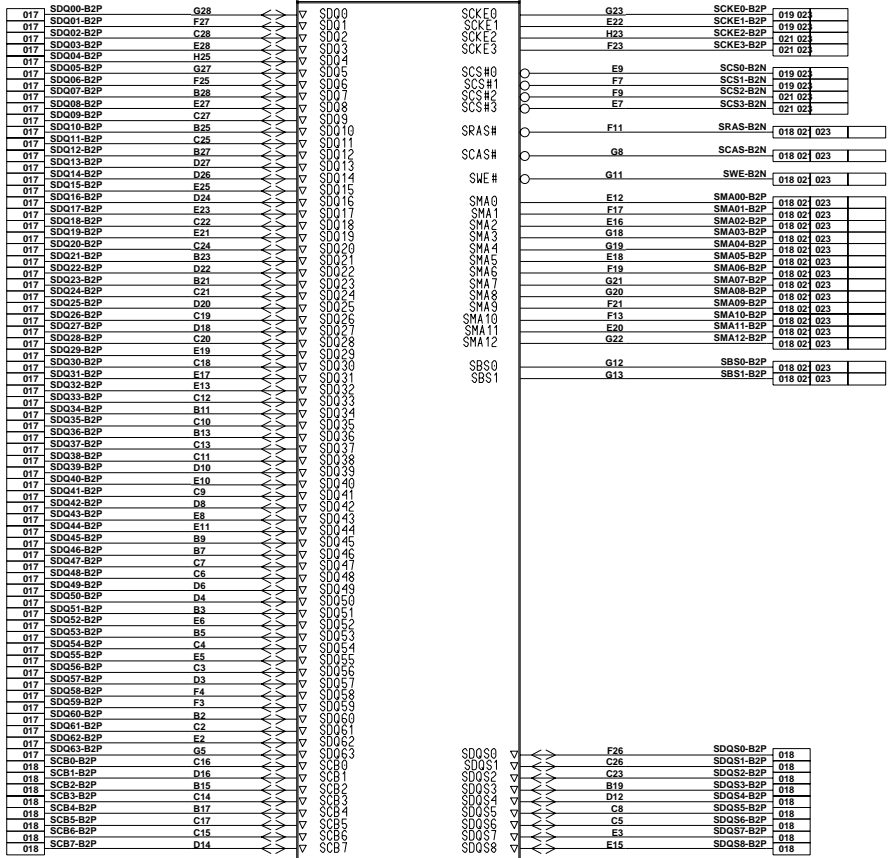
D

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82845MP MCH-M 3/7  
(U82845MP)

IC14



IC14 COMR (HX 11/17)  
REF: NORTHWOOD TS<FNNWTS0> SH.12 REV.00

GDM460000074

Engineer		<b>INVENTEC</b>
Eng. Name		
Drawn by		
Drawer Name		
R&D CHK		TITLE: <b>FMNSY2-VP</b>
DOC CTRL CHK		<b>MCH-M 3</b>
MFG ENGR CHK		Size <b>A3</b>

Changed by	Date Changed	Time Changed	QA CHK	VER	Model Number	Sheet
TSB1	Friday, February 22, 2002	3:20:34 pm		110	Model No	12 of

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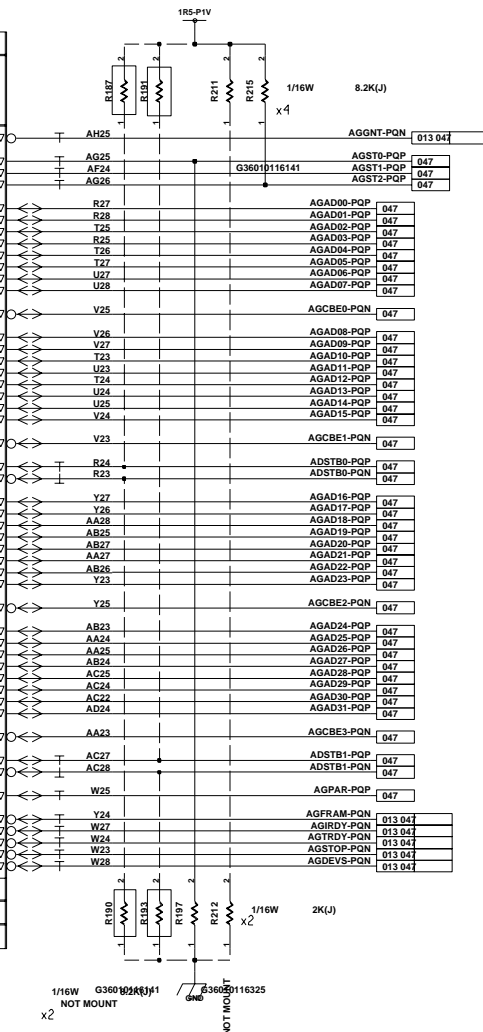
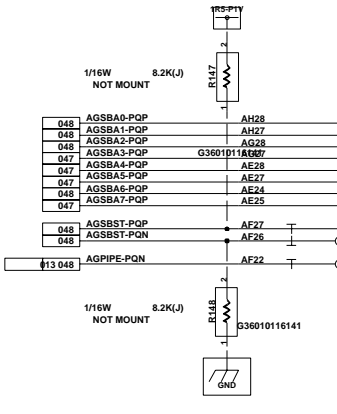
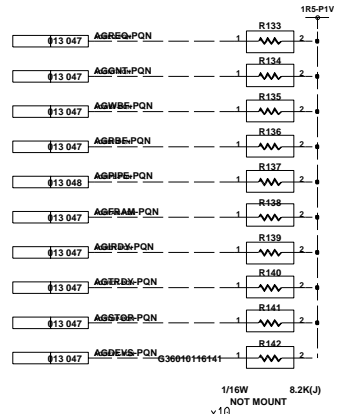
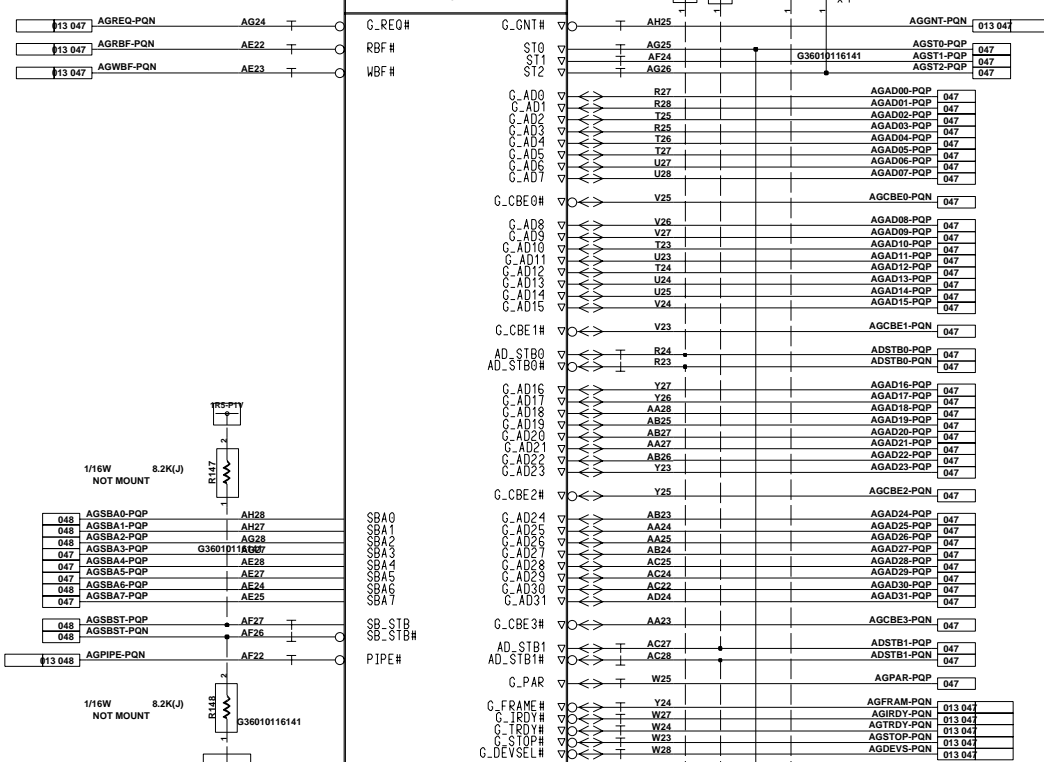
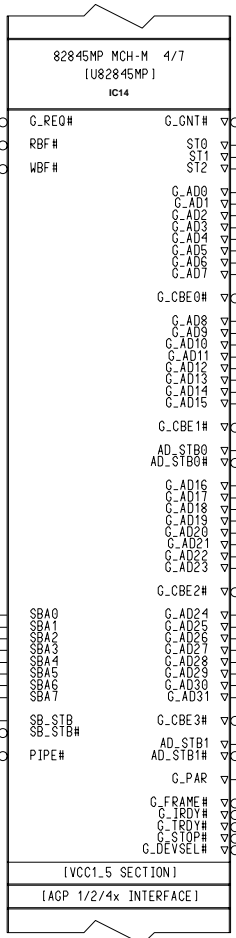
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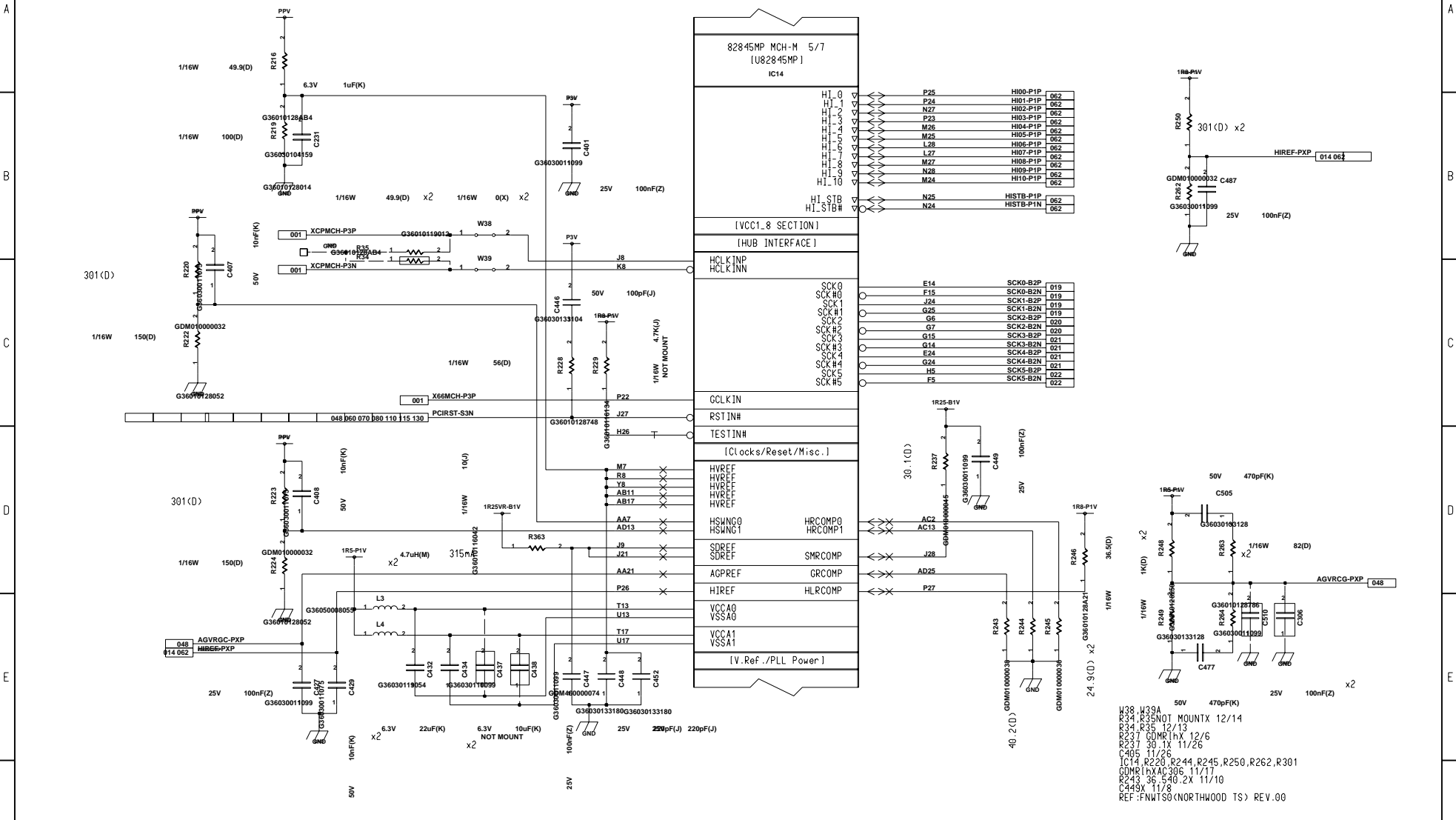
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M\*\*\*-POPX 11/17  
REF:NORTHWOOD TS(FNWT50) SH.13 REV.00

GDM460000074

Engineer Engy Name	<b>INVENTEC</b> TITLE: <b>FMNSY2-VP</b> <b>MCH-M 4</b> Model Number Model No	Size <b>A3</b>
Drawn by Drawer Name		
R&D CHK		
DOC CTRL CHK		
MFG ENGR CHK		

Changed by TSB1	Date Changed Friday, February 22, 2002	Time Changed 3:20:38 pm	QA CHK	VER 110	Sheet 13 of
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1 2 3 4 5 6 7 8



Engineer	
Engy Name	
Drawn by	
Drawer Name	
R&D CHK	TITLE
DOC CTRL CHK	
MFG ENGR CHK	

# INVENTEC

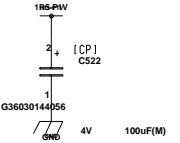
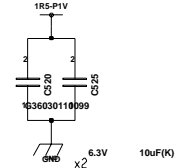
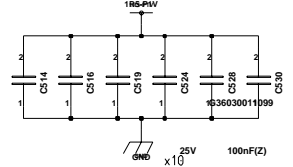
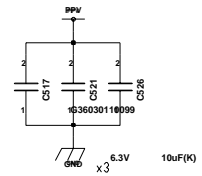
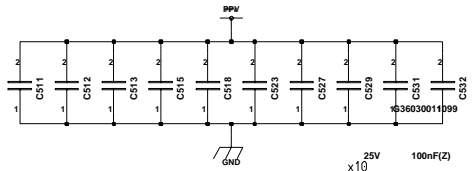
## FMNSY2-VP

### MCH-M 5

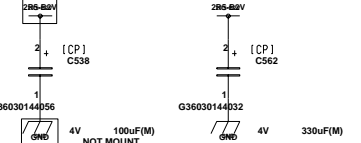
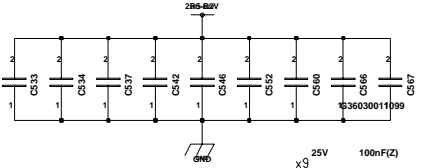
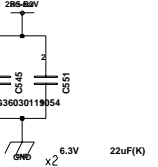
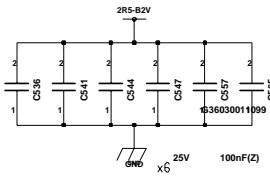
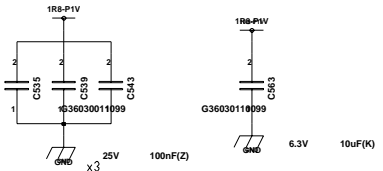
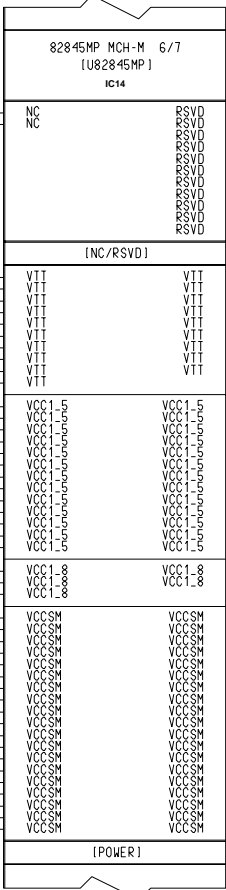
Model Number **MCH-M 5**

Sheet **14** of **14**

1 2 3 4 5 6 7 8



GDM46000074



IC14 GDMR (HX 11/17)  
REF: NORTHWOOD TS(FNWTSG) SH.15 REV.00

Engineer Engy Name	<p><b>INVENTEC</b></p> <p>TITLE: <b>FMNSY2-VP</b></p> <p><b>MCH-M 6</b></p> <p>Model Number</p>
Drawn by Drawer Name	
R&D CHK	
DOC CTRL CHK	
MFG ENGR CHK	
QA CHK	Model No
VER 110	Sheet 15 of

Changed by TSB1	Date Changed Friday, February 22, 2002	Time Changed 3:20:49 pm
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1 2 3 4 5 6 7 8

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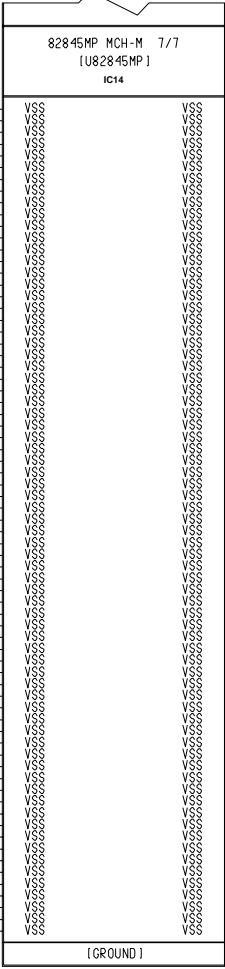
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GDM460000074 GND

GND

IC14 GDM(hX 11/17  
REF: NORTHWOOD TS(FNWT50) SH.16 REV.00

Engineer <i>Engr Name</i>	<b>INVENTEC</b>
Drawn by <i>Drawer Name</i>	
R&D CHK	
DOC CTRL CHK	
MFG ENGR CHK	
TITLE	<b>FMNSY2-VP</b>
	<b>MCH-M 7</b>
Model Number	<i>Model No</i>

Size  
**A3**

Changed by <b>TSB1</b>	Date Changed <b>Friday, February 22, 2002</b>	Time Changed <b>3:20:54 pm</b>	QA CHK	VER <b>110</b>	Sheet <b>16</b> of
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1 2 3 4 5 6 7 8

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		[RMX 4A] RM5		1/32W 22(J)			
012	SDQ04-B2P	1	8	SDQ04R-B2P	019 021 023		
012	SDQ05-B2P	2	7	SDQ05R-B2P	019 021 023		
012	SDQ06-B2P	3	6	SDQ06R-B2P	019 021 023		
012	SDQ06-B2P	4	5	SDQ06R-B2P	019 021 023		
G36010115083							

		[RMX 4A] RM13		1/32W 22(J)			
012	SDQ34-B2P	1	8	SDQ34R-B2P	020 022 023		
012	SDQ38-B2P	2	7	SDQ38R-B2P	020 022 023		
012	SDQ39-B2P	3	6	SDQ39R-B2P	020 022 023		
012	SDQ35-B2P	4	5	SDQ35R-B2P	020 022 023		
G36010115083							

B

		[RMX 4A] RM6		1/32W 22(J)			
012	SDQ07-B2P	1	8	SDQ07R-B2P	019 021 023		
012	SDQ08-B2P	2	7	SDQ08R-B2P	019 021 023		
012	SDQ09-B2P	3	6	SDQ09R-B2P	019 021 023		
012	SDQ01-B2P	4	5	SDQ01R-B2P	019 021 023		
G36010115083							

		[RMX 4A] RM14		1/32W 22(J)			
012	SDQ33-B2P	1	8	SDQ33R-B2P	020 022 023		
012	SDQ32-B2P	2	7	SDQ32R-B2P	020 022 023		
012	SDQ37-B2P	3	6	SDQ37R-B2P	020 022 023		
012	SDQ36-B2P	4	5	SDQ36R-B2P	020 022 023		
G36010115083							

C

		[RMX 4A] RM7		1/32W 22(J)			
012	SDQ08-B2P	1	8	SDQ08R-B2P	019 021 023		
012	SDQ13-B2P	2	7	SDQ13R-B2P	019 021 023		
012	SDQ09-B2P	3	6	SDQ09R-B2P	019 021 023		
012	SDQ12-B2P	4	5	SDQ12R-B2P	019 021 023		
G36010115083							

		[RMX 4A] RM15		1/32W 22(J)			
012	SDQ45-B2P	1	8	SDQ45R-B2P	020 022 023		
012	SDQ44-B2P	2	7	SDQ44R-B2P	020 022 023		
012	SDQ41-B2P	3	6	SDQ41R-B2P	020 022 023		
012	SDQ40-B2P	4	5	SDQ40R-B2P	020 022 023		
G36010115083							

		[RMX 4A] RM8		1/32W 22(J)			
012	SDQ10-B2P	1	8	SDQ10R-B2P	019 021 023		
012	SDQ11-B2P	2	7	SDQ11R-B2P	019 021 023		
012	SDQ15-B2P	3	6	SDQ15R-B2P	019 021 023		
012	SDQ14-B2P	4	5	SDQ14R-B2P	019 021 023		
G36010115083							

		[RMX 4A] RM16		1/32W 22(J)			
012	SDQ43-B2P	1	8	SDQ43R-B2P	020 022 023		
012	SDQ47-B2P	2	7	SDQ47R-B2P	020 022 023		
012	SDQ42-B2P	3	6	SDQ42R-B2P	020 022 023		
012	SDQ46-B2P	4	5	SDQ46R-B2P	020 022 023		
G36010115083							

		[RMX 4A] RM9		1/32W 22(J)			
012	SDQ17-B2P	1	8	SDQ17R-B2P	019 021 023		
012	SDQ21-B2P	2	7	SDQ21R-B2P	019 021 023		
012	SDQ16-B2P	3	6	SDQ16R-B2P	019 021 023		
012	SDQ20-B2P	4	5	SDQ20R-B2P	019 021 023		
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		[RMX 4A] RM21		1/32W 22(J)			
012	SDQ53-B2P	1	8	SDQ53R-B2P	020 022 023		
012	SDQ48-B2P	2	7	SDQ48R-B2P	020 022 023		
012	SDQ49-B2P	3	6	SDQ49R-B2P	020 022 023		
012	SDQ54-B2P	4	5	SDQ54R-B2P	020 022 023		
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		[RMX 4A] RM10		1/32W 22(J)			
012	SDQ23-B2P	1	8	SDQ23R-B2P	019 021 023		
012	SDQ18-B2P	2	7	SDQ18R-B2P	019 021 023		
012	SDQ19-B2P	3	6	SDQ19R-B2P	019 021 023		
012	SDQ23-B2P	4	5	SDQ23R-B2P	019 021 023		
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		[RMX 4A] RM20		1/32W 22(J)			
012	SDQ55-B2P	1	8	SDQ55R-B2P	020 022 023		
012	SDQ50-B2P	2	7	SDQ50R-B2P	020 022 023		
012	SDQ52-B2P	3	6	SDQ52R-B2P	020 022 023		
012	SDQ51-B2P	4	5	SDQ51R-B2P	020 022 023		
G36010115083							

		[RMX 4A] RM11		1/32W 22(J)			
012	SDQ29-B2P	1	8	SDQ29R-B2P	019 021 023		
012	SDQ28-B2P	2	7	SDQ28R-B2P	019 021 023		
012	SDQ25-B2P	3	6	SDQ25R-B2P	019 021 023		
012	SDQ24-B2P	4	5	SDQ24R-B2P	019 021 023		
G36010115083							

		[RMX 4A] RM23		1/32W 22(J)			
012	SDQ61-B2P	1	8	SDQ61R-B2P	020 022 023		
012	SDQ57-B2P	2	7	SDQ57R-B2P	020 022 023		
012	SDQ60-B2P	3	6	SDQ60R-B2P	020 022 023		
012	SDQ56-B2P	4	5	SDQ56R-B2P	020 022 023		
G36010115083							

		[RMX 4A] RM12		1/32W 22(J)			
012	SDQ26-B2P	1	8	SDQ26R-B2P	019 021 023		
012	SDQ27-B2P	2	7	SDQ27R-B2P	019 021 023		
012	SDQ30-B2P	3	6	SDQ30R-B2P	019 021 023		
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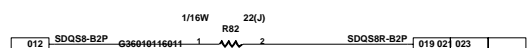
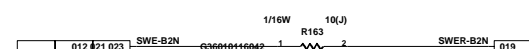
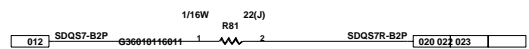
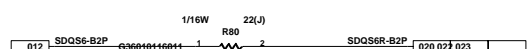
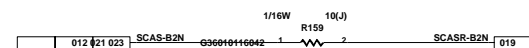
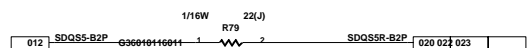
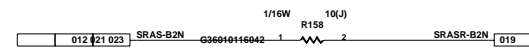
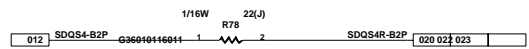
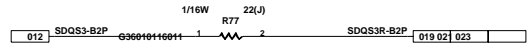
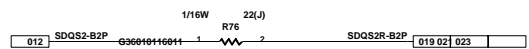
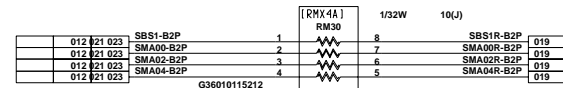
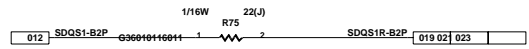
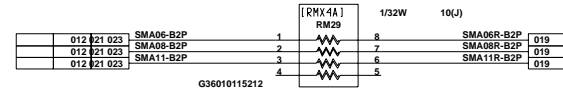
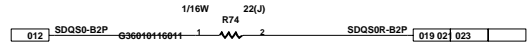
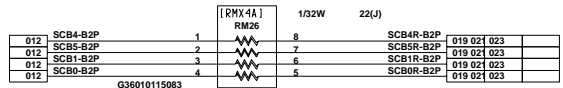
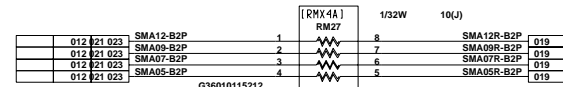
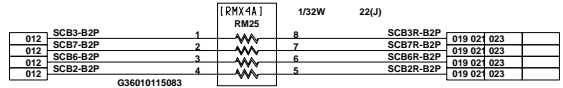
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012	SDQ63-B2P	3	6	SDQ63R-B2P	020 022 023		
012	SDQ62-B2P	4	5	SDQ62R-B2P	020 022 023		
G36010115083							

CAEGsATCX 19/19  
REF.NORTHWOOD TSK(FNWT50) SH.17 REV.00

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Engineer <i>Engr Name</i>	<b>INVENTEC</b>	Model Number	<b>FMNSY2-VP</b>	Sheet 17 of
Drawn by <i>Drawn Name</i>		TITLE		
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MFG ENGR CHK				
Changed by TSB1	Date Changed Friday, February 22, 2002	Time Changed 3:20:59 pm	QA CHK	VER 110

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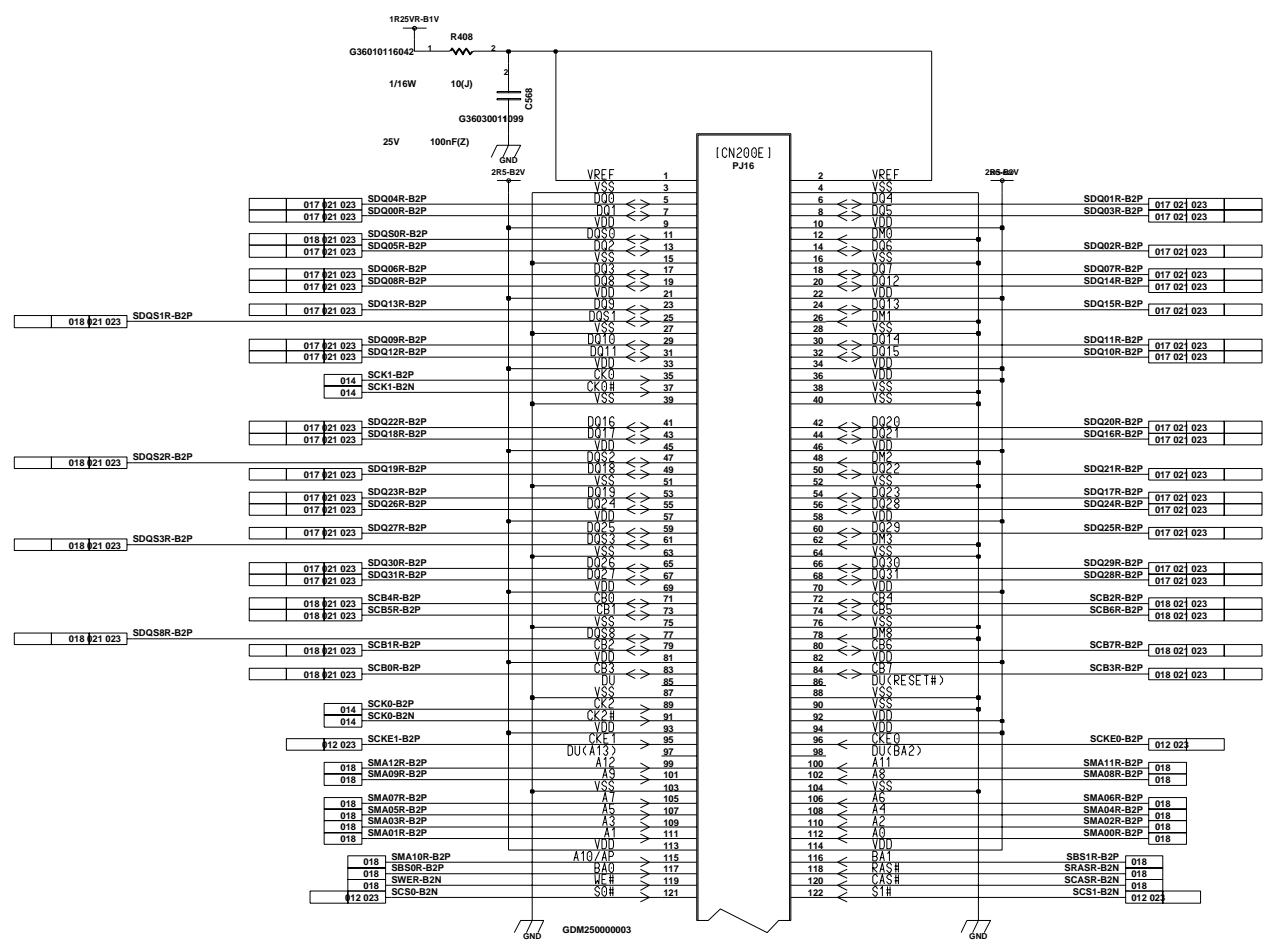
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CAEqsATCX 10/19  
REF:NORTHWOOD TS(FNWTSG) SH.18 REV.00

Engineer <i>Engr Name</i>	<b>INVENTEC</b>
Drawn by <i>Drawer Name</i>	
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MFG ENGR CHK	
TITLE	<b>FMNSY2-VP</b> <b>DDR I/F RS 2</b>
Model Number	Size <b>A3</b>

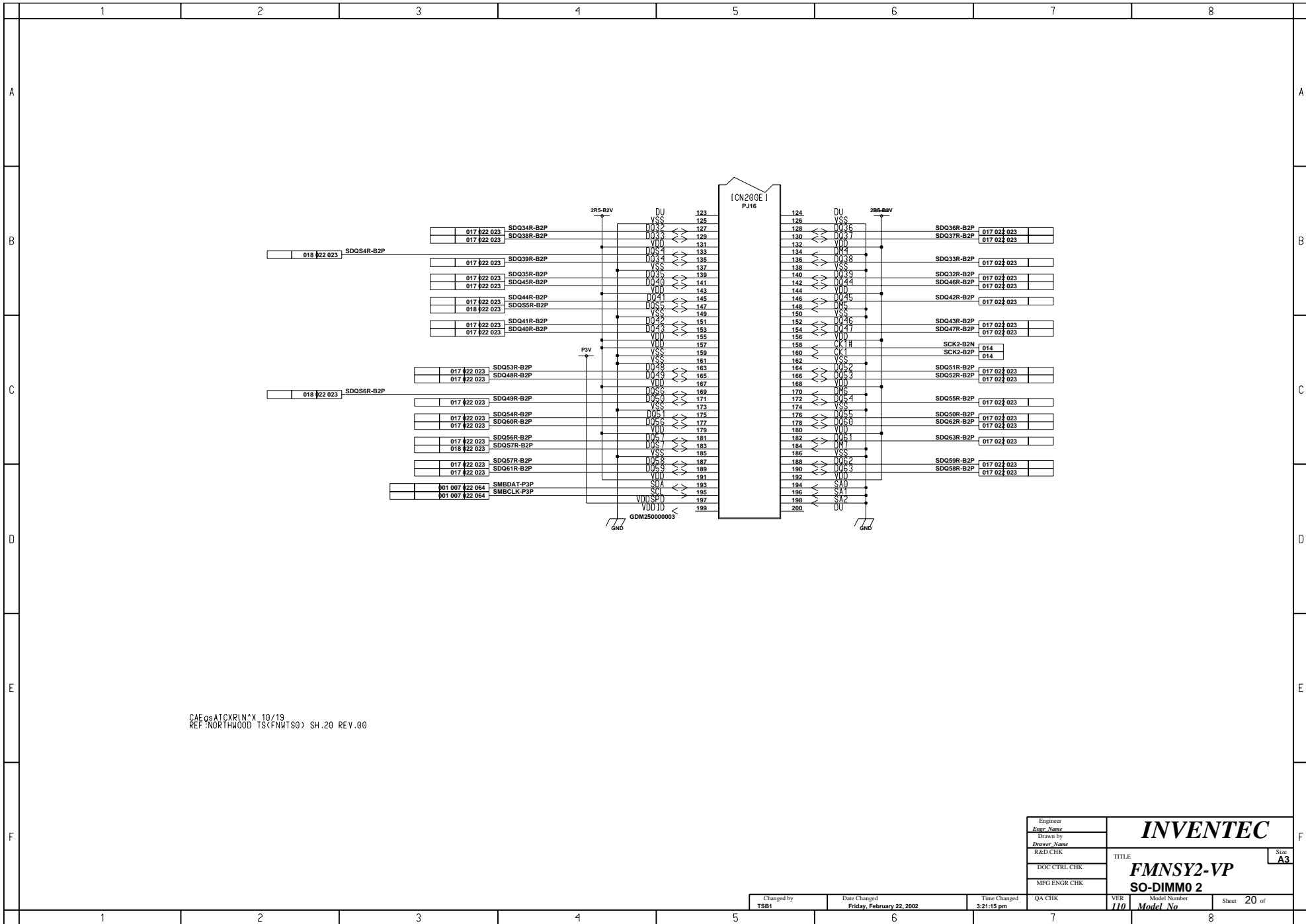
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CAFqsATCXRLN\*X 10/19  
REF: NORTHWOOD TS(FNWTISO) SH.19 REV.00

Engineer Engy Name	<b>INVENTEC</b> TITLE: <b>FMNSY2-VP</b> <b>SO-DIMMO 1</b> Size <b>A3</b>
Drawn by Drawer Name	
R&D CHK	
DOC CTRL CHK	
MFG ENGR CHK	

Changed by TSB1	Date Changed Friday, February 22, 2002	Time Changed 3:21:07 pm	QA CHK	VER 110	Model Number Model No	Sheet 19 of
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CAE qstcrln\*x\_10/19  
 REF: NORTHWOOD TSCFNWTSO) SH.20 REV.00

Engineer <i>Engr Name</i>	<b>INVENTEC</b>  <b>FMNSY2-VP</b> <b>SO-DIMM0 2</b>	Size <b>A3</b>
Drawn by <i>Drawer Name</i>		TITLE
R&D CHK		
DOC CTRL CHK		
MFG ENGR CHK		
Changed by <b>TSB1</b>	Date Changed <b>Friday, February 22, 2002</b>	Time Changed <b>3:21:15 pm</b>
QA CHK	Model Number <b>Model No</b>	Sheet <b>20</b> of

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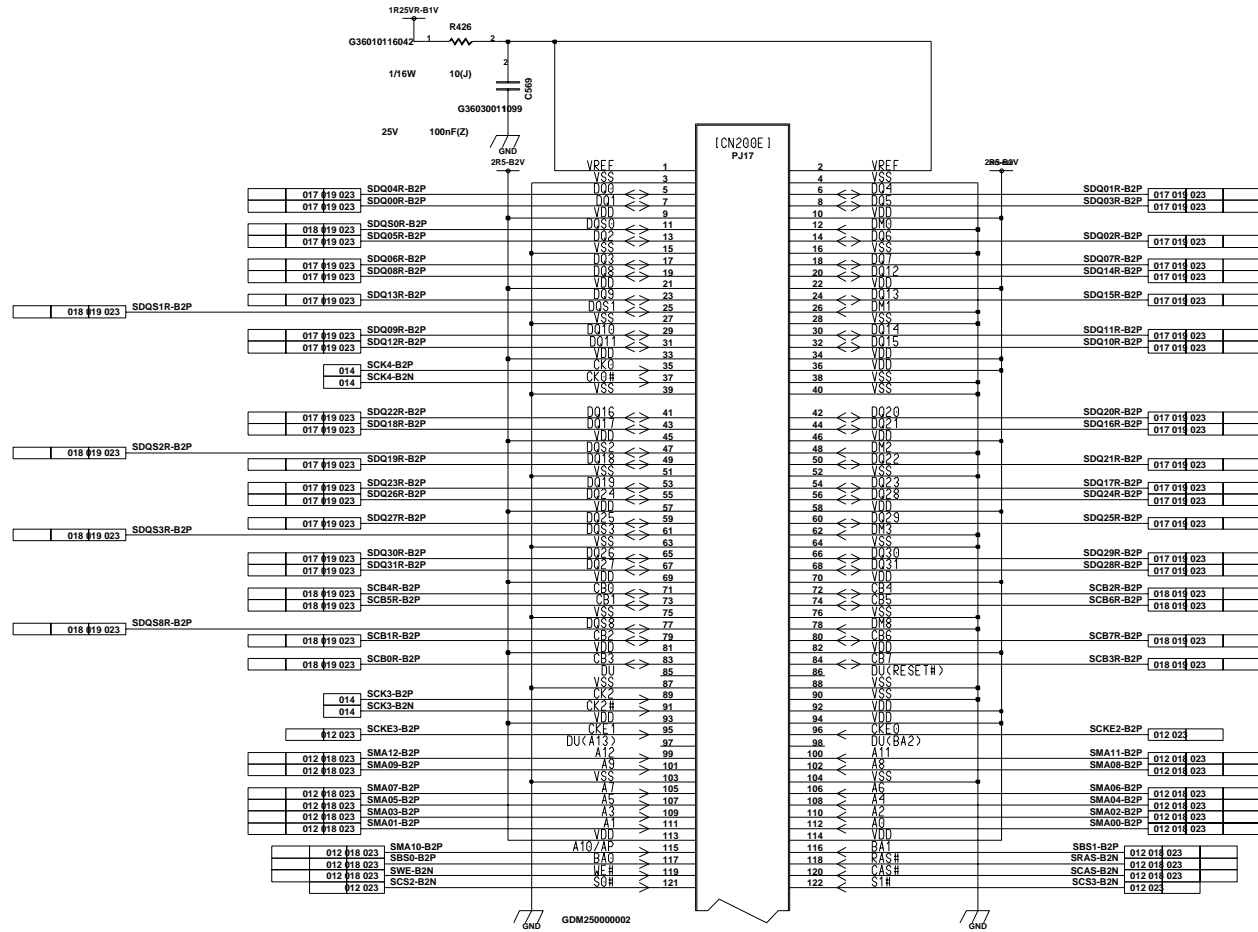
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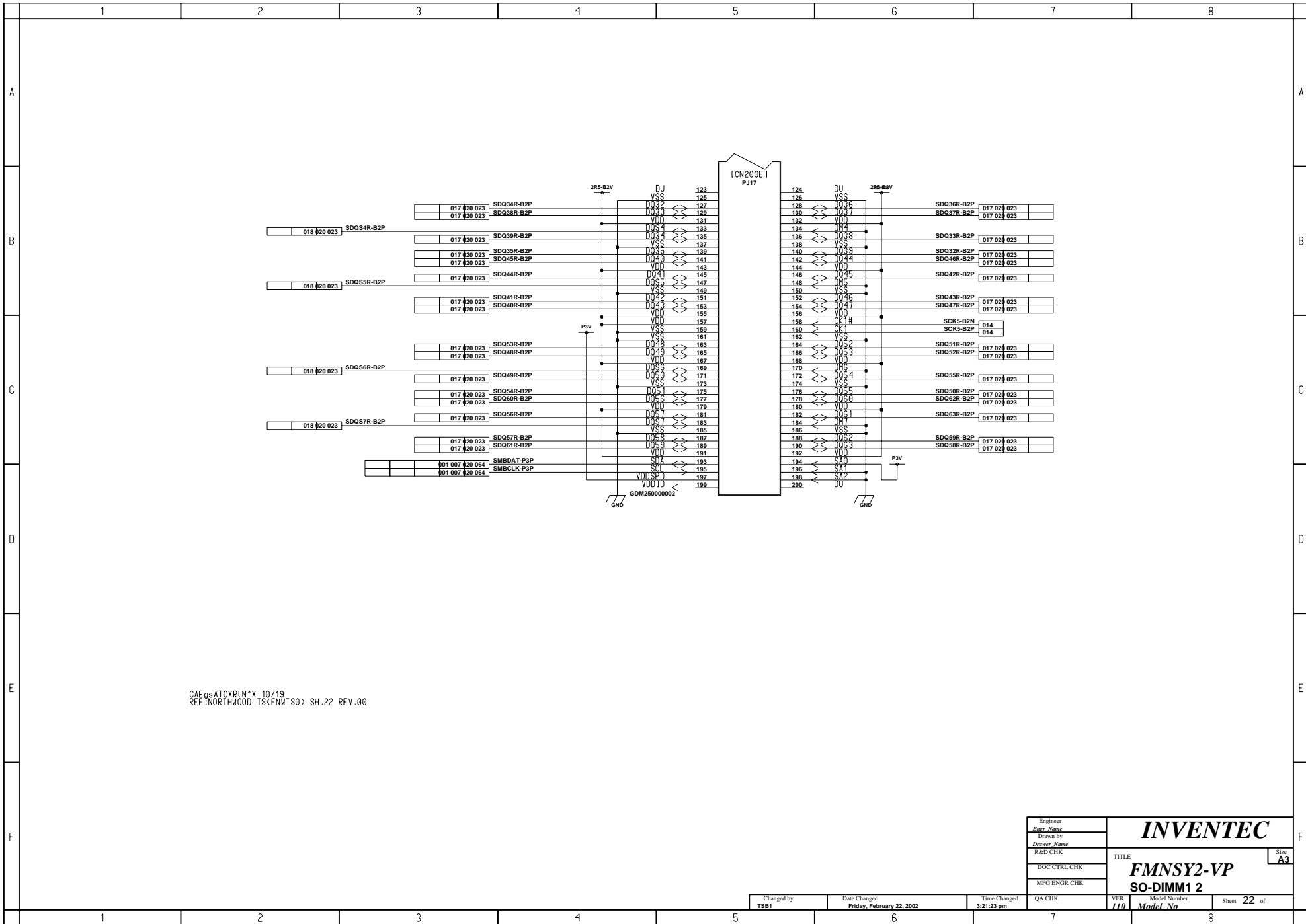
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REF: NORTHWOOD TS(FNWT50) SH.21 REV.00

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Drawn by <i>Drawer Name</i>	
R&D CHK	
DOC CTRL CHK	
MFG ENGR CHK	

Changed by TSB1	Date Changed Friday, February 22, 2002	Time Changed 3:21:18 pm	QA CHK	VER 110	Model Number Model No	Sheet 21 of
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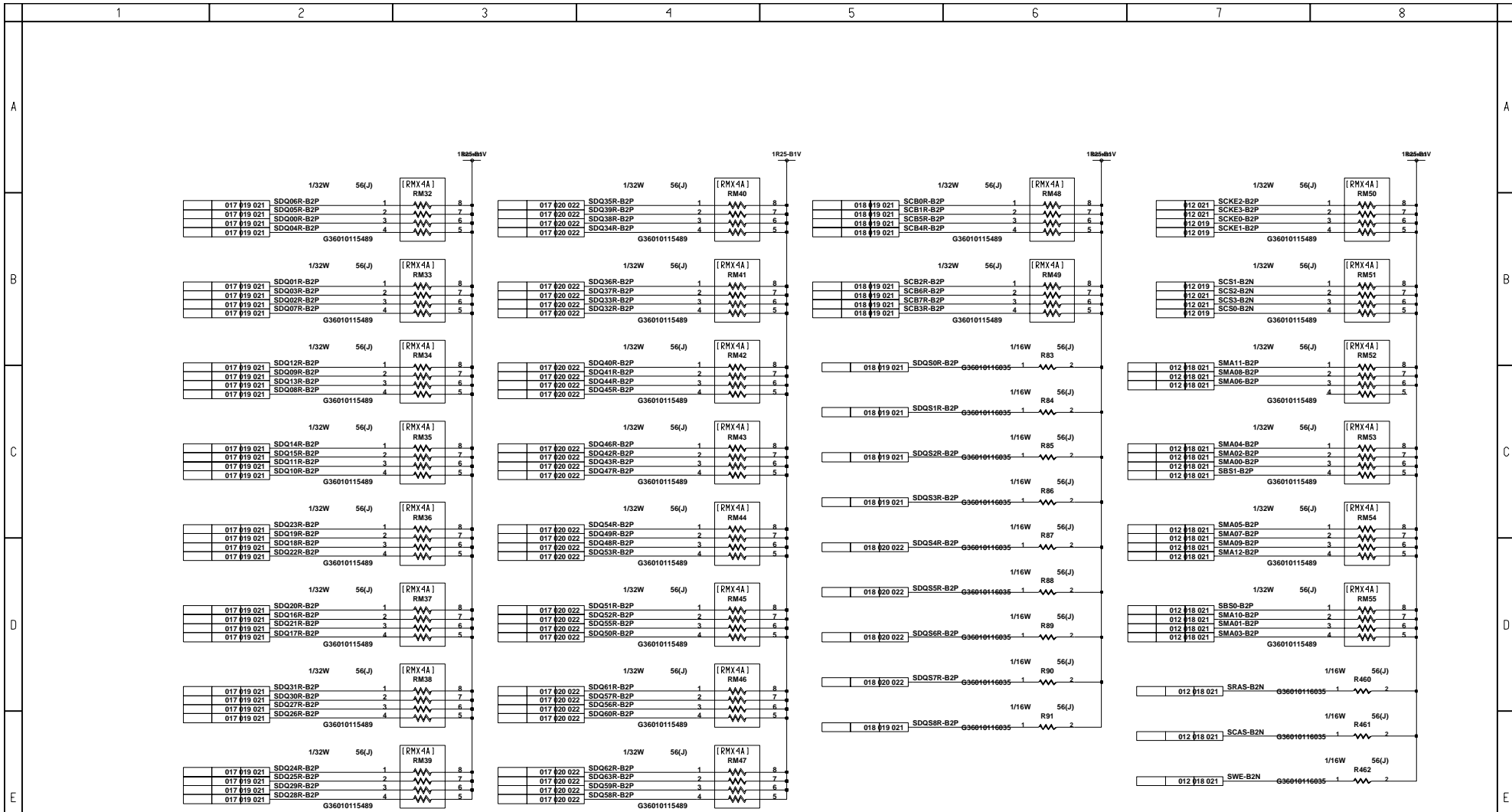


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Engineer	
Engr Name	
Drawn by	
Drawer Name	
R&D CHK	
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MFG ENGR CHK	
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	<b>FMNSY2-VP</b>
	<b>SO-DIMM1 2</b>
Model Number	Model No

Changed by	Date Changed	Time Changed	QA CHK	VER	Model Number	Sheet	of
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Size A3

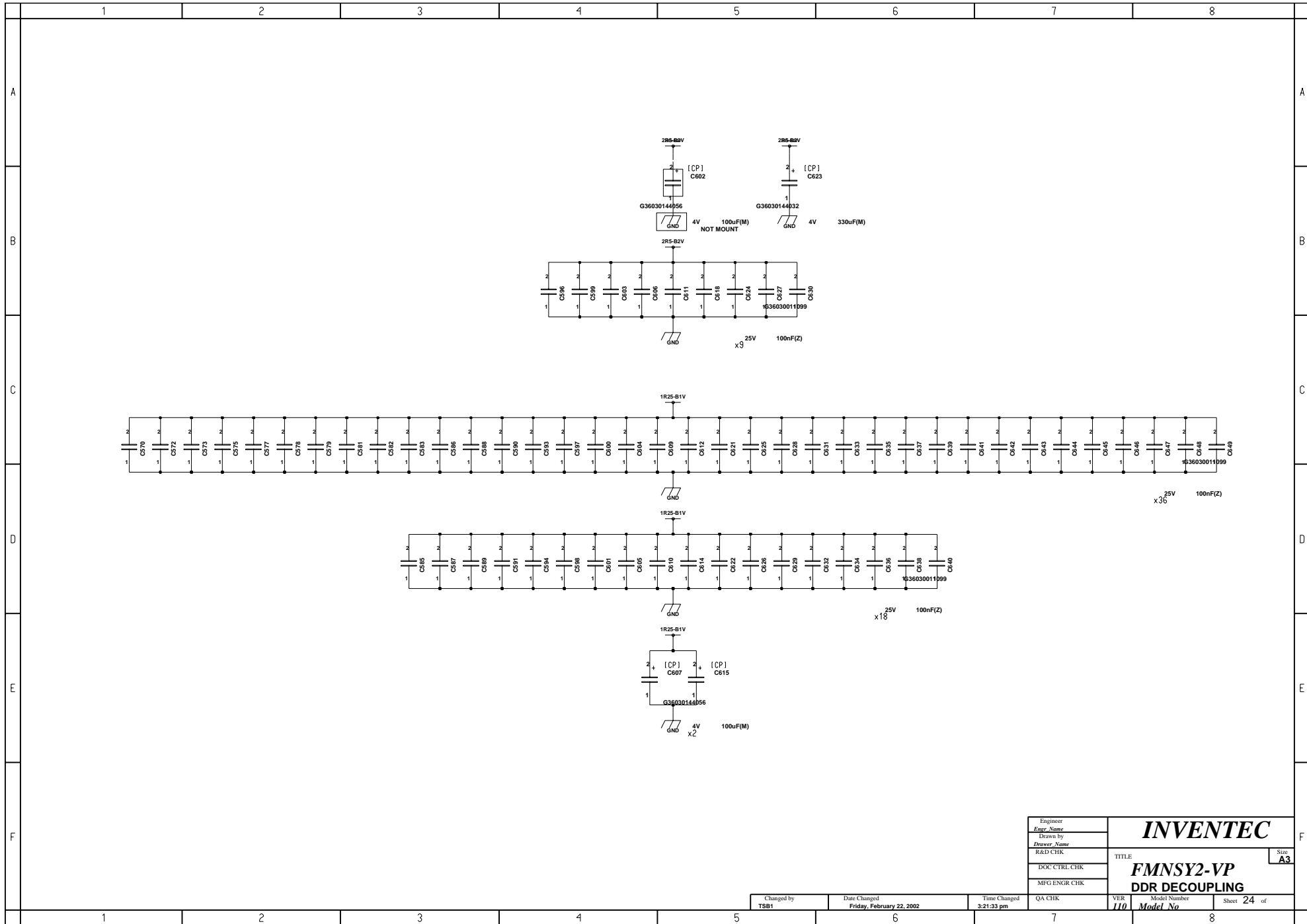


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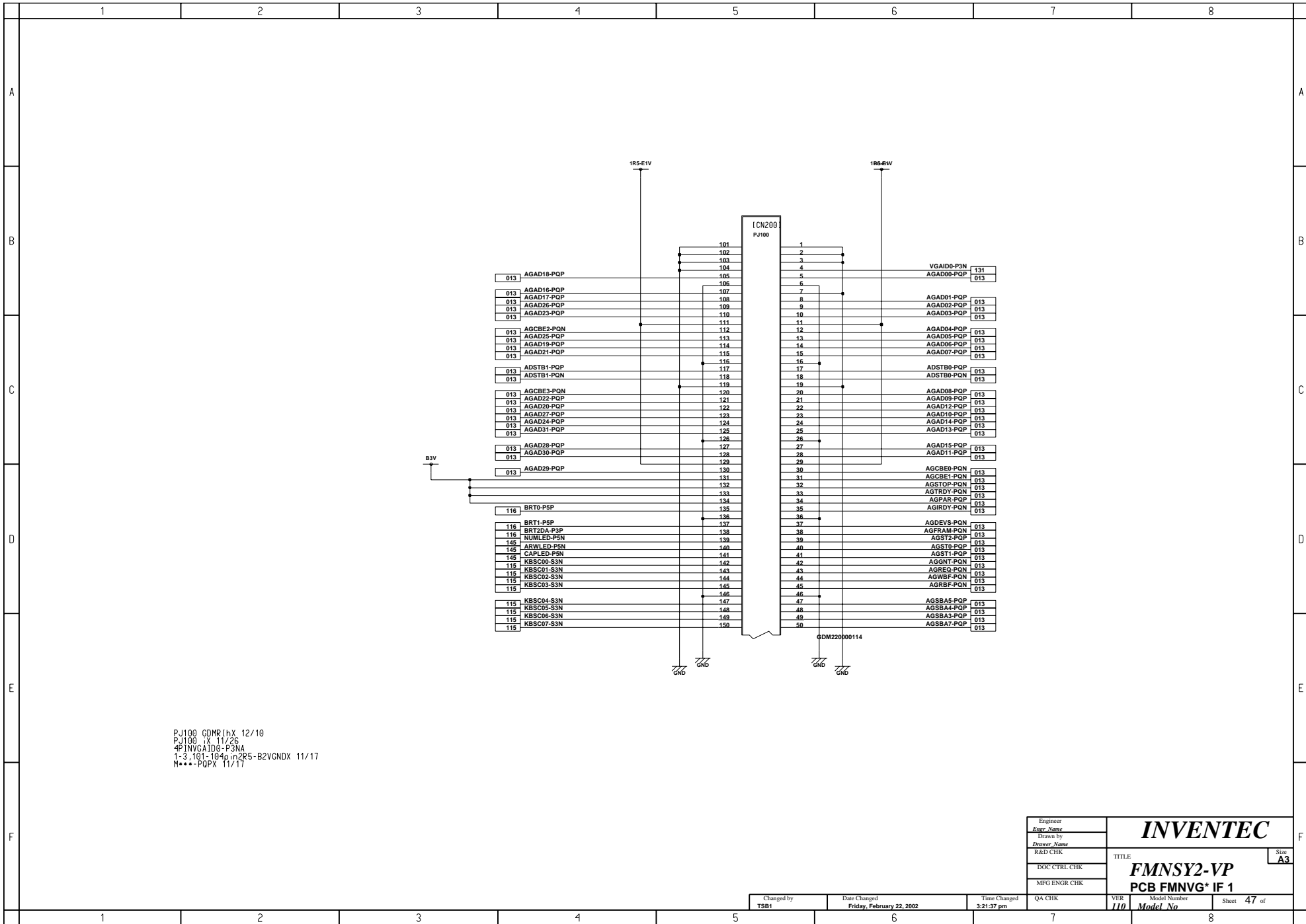
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Drawn by Drawer Name		
R&D CHK		
DOC CTRL CHK		
MFG ENGR CHK		

Changed by TSB1	Date Changed Friday, February 22, 2002	Time Changed 3:21:27 pm	QA CHK	VER 110	Model Number Model No	Sheet 23 of
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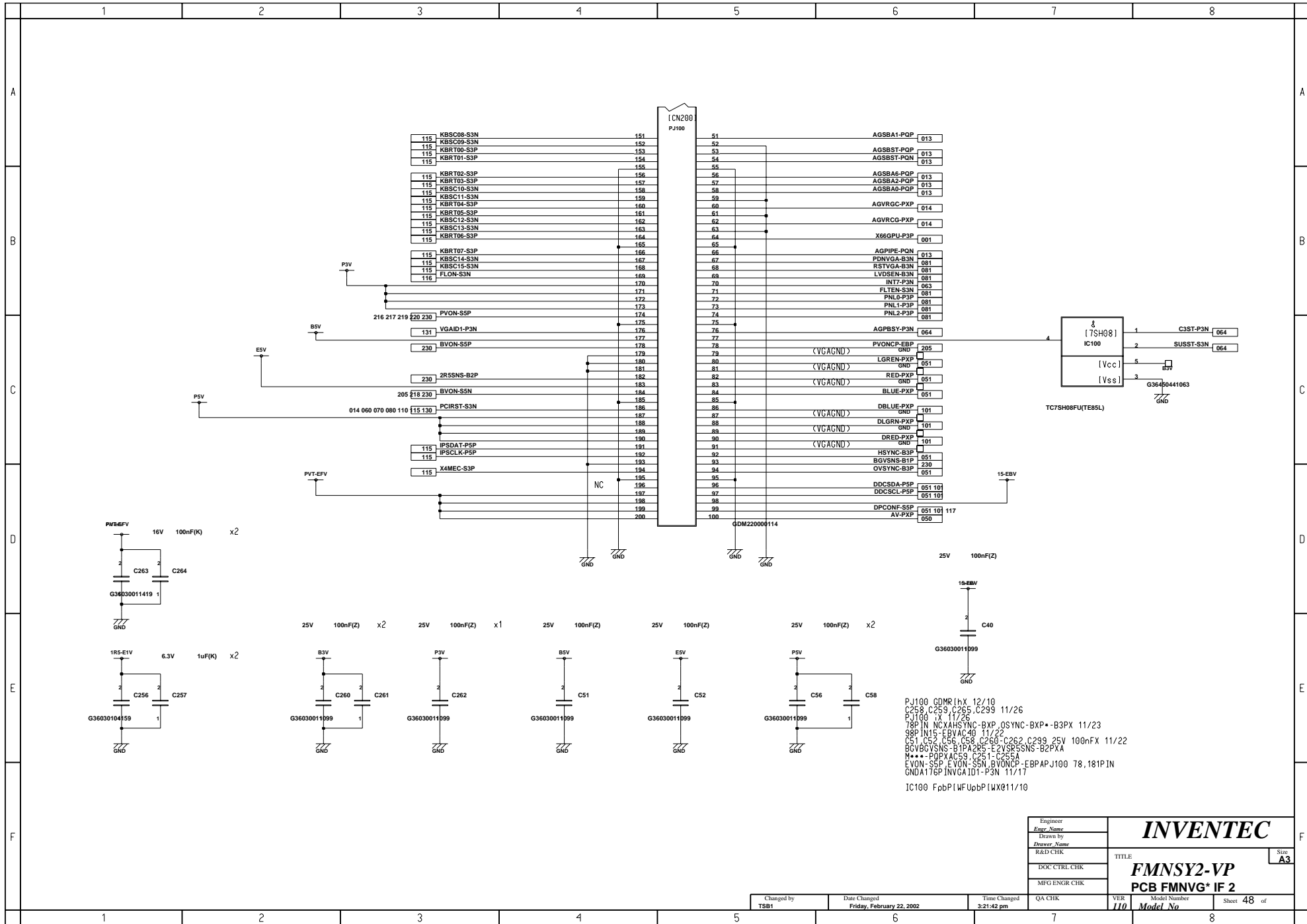
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R&D CHK				TITLE
DOC CTRL CHK				<b>FMNSY2-VP DDR DECOUPLING</b>
MFG ENGR CHK	VER	Model Number	Sheet 24 of	
Changed by <b>TSB1</b>	Date Changed <b>Friday, February 22, 2002</b>	Time Changed <b>3:21:33 pm</b>	QA CHK	
			VER <b>110</b>	
			Model No	
			Sheet 24 of	



PJ100 QMR1hX 12/10  
 PJ100 1X 11/26  
 #P INVC11D0-F3M4  
 1-3 181-1840-2B5-B2VGNX 11/17  
 M\*\*\*-POPX 11/17

Engineer <i>Engr Name</i>	<b>INVENTEC</b>  <b>FMNSY2-VP</b> <b>PCB FMNVG* IF 1</b>	Size <b>A3</b>
Drawn by <i>Drawer Name</i>		TITLE
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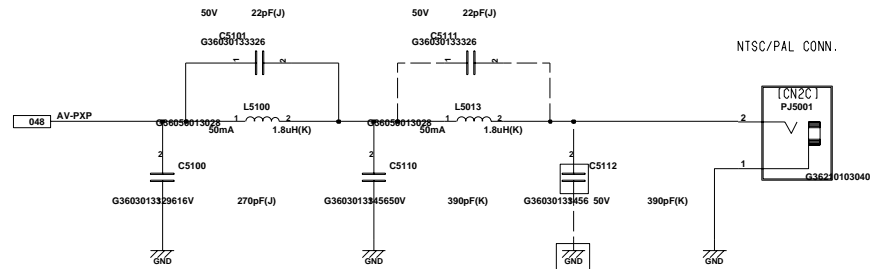
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PJ100 GDMR[hx 12/10  
 C258, C259, C265, C299 11/26  
 E1100 X 11/26  
 181IN NCX4HSYNC-BXP, OSYNC-BXP+, B3PX 11/23  
 98PIN15-EBVAC40 11/22  
 C51, C52, C56, C58, C260, C262, C293, 25V 100nF(X) 11/22  
 BVONCP-S5P, BVON-S5N, BVONCP-EBPAPJ100 78, 181PIN  
 GND A1176P INVGA1D1-P3N 11/17  
 IC100 FobPIWFUpbPIHX011/10

Engineer Engy Name	<b>INVENTEC</b> TITLE: <b>FMNSY2-VP</b> <b>PCB FMNVG* IF 2</b> Size: <b>A3</b>
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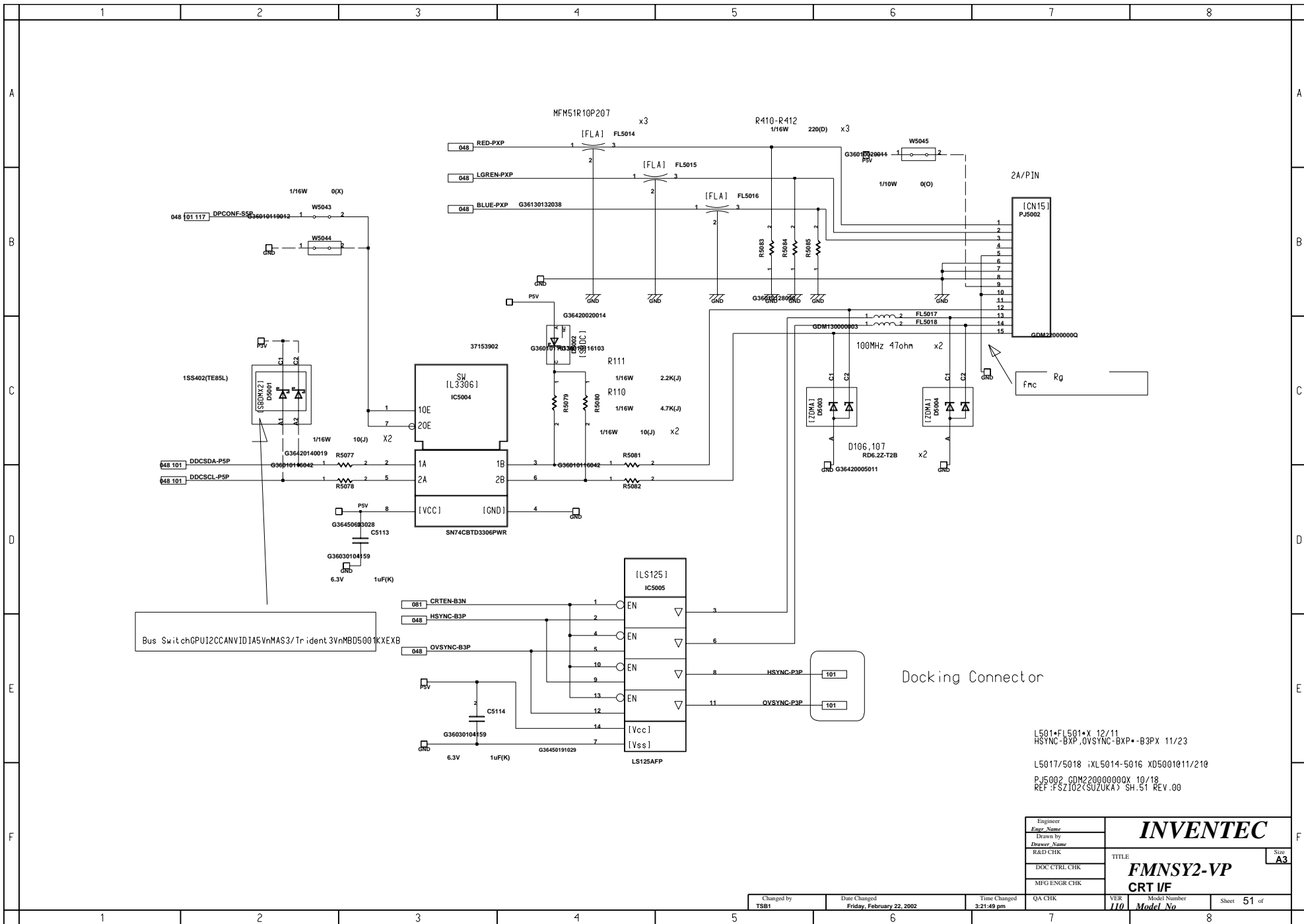
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NTSC/PALRN\*X 10/18  
Filter\X011/210

Engineer <i>Engr Name</i>	<b>INVENTEC</b>
Drawn by <i>Drawer Name</i>	
R&D CHK	
DOC CTRL CHK	
MFG ENGR CHK	
TITLE	Size <b>A3</b>
<b>FMNSY2-VP</b>	
<b>TV IF</b>	

Changed by <b>TSB1</b>	Date Changed <b>Friday, February 22, 2002</b>	Time Changed <b>3:21:46 pm</b>	QA CHK	VER <b>110</b>	Model Number <i>Model No</i>	Sheet <b>50</b> of
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Bus Switch GPU12CCANVID1A5VnMAS3/Trident3VnMBD5001KXEB

Docking Connector

L501\*FL501\*X 12/11  
 HSYNC-BXP,OVSYNC-BXP\*-B3PX 11/23  
 L5017/5018 :XL5014-5016 XD5001011/210  
 PJ5002 GDM220000000X 10/18  
 REF :FSZ102CSUZUKA) SH.51 REV.00

Engineer Eng. Name	<b>INVENTEC</b>	Size <b>A3</b>	
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MFG ENGR CHK			<b>CRT I/F</b>
Changed by TSB1	Date Changed Friday, February 22, 2002	Time Changed 3:21:49 pm	
QA CHK	VER 110	Model Number Model No	
Sheet 51 of			

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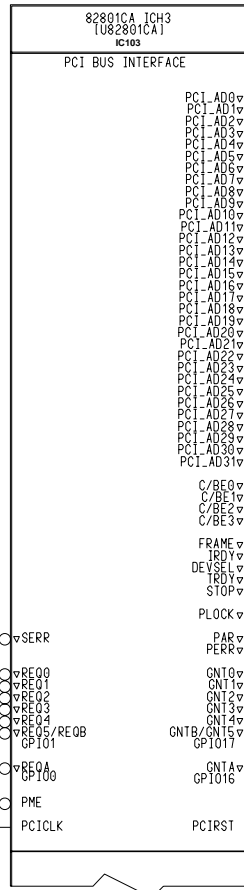
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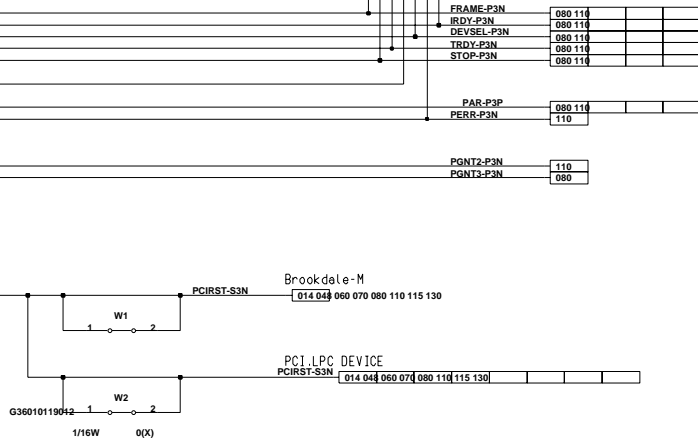
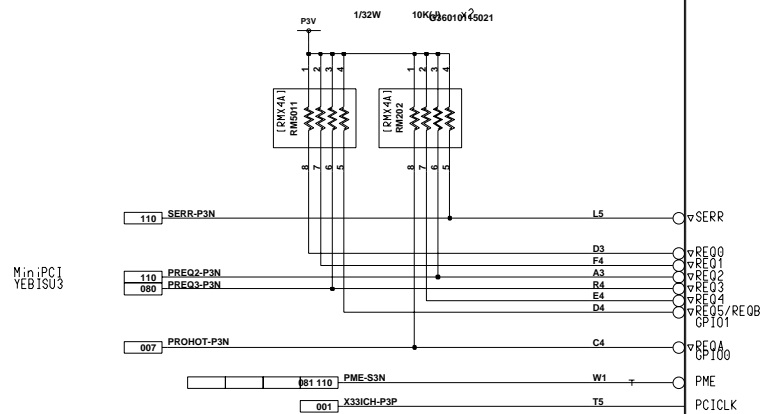
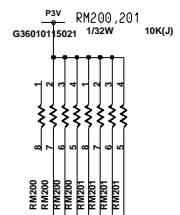
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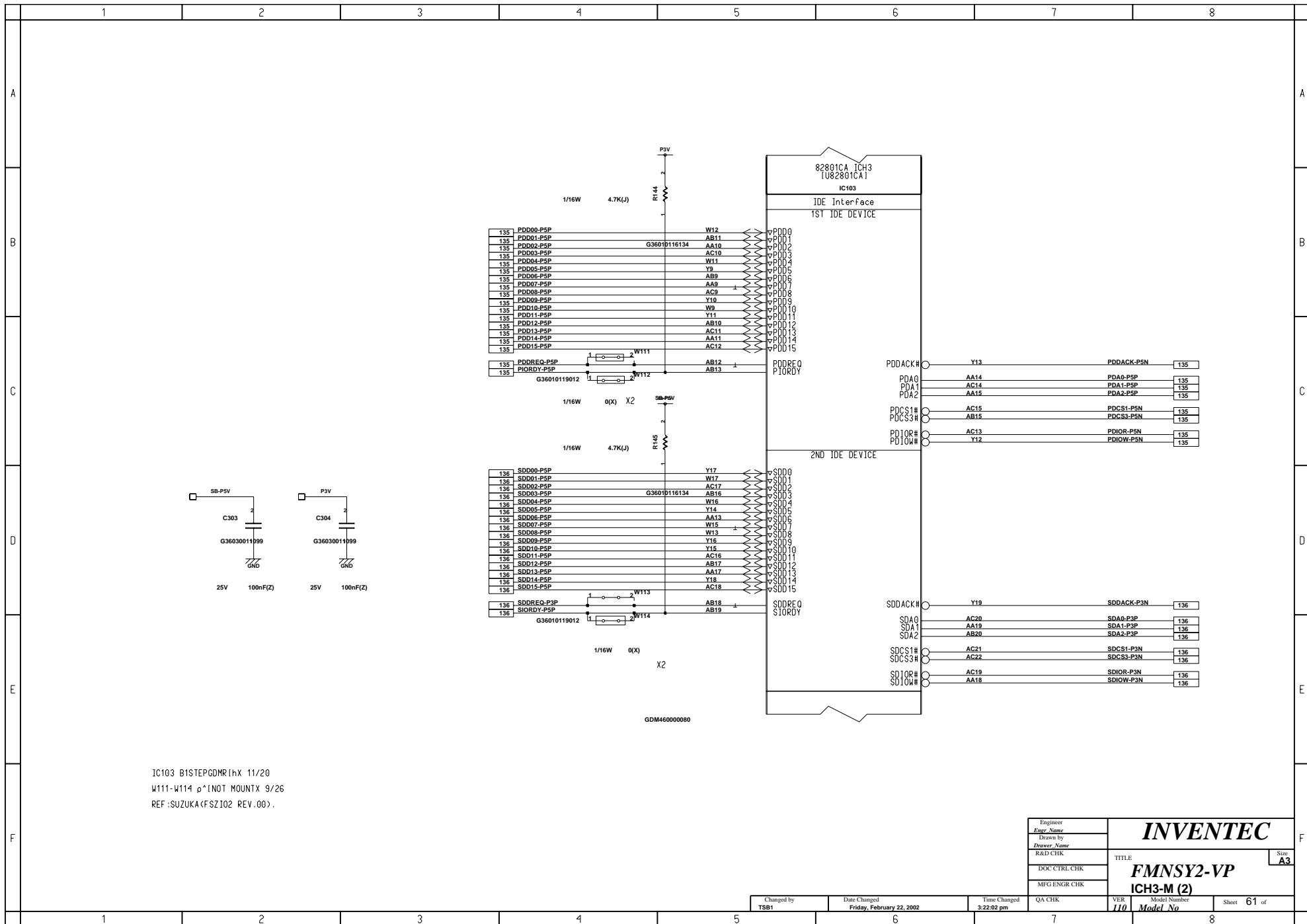
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PCI_AD3v	K3	AD03-P3P	080 110		
PCI_AD4v	H5	AD04-P3P	080 110		
PCI_AD5v	K4	AD05-P3P	080 110		
PCI_AD6v	H3	AD06-P3P	080 110		
PCI_AD7v	L1	AD07-P3P	080 110		
PCI_AD8v	L2	AD08-P3P	080 110		
PCI_AD9v	G2	AD09-P3P	080 110		
PCI_AD10v	L4	AD10-P3P	080 110		
PCI_AD11v	H4	AD11-P3P	080 110		
PCI_AD12v	M4	AD12-P3P	080 110		
PCI_AD13v	J3	AD13-P3P	080 110		
PCI_AD14v	M5	AD14-P3P	080 110		
PCI_AD15v	J1	AD15-P3P	080 110		
PCI_AD16v	F5	AD16-P3P	080 110		
PCI_AD17v	N2	AD17-P3P	080 110		
PCI_AD18v	G4	AD18-P3P	080 110		
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PCI_AD20v	G1	AD20-P3P	080 110		
PCI_AD21v	F1	AD21-P3P	080 110		
PCI_AD22v	F2	AD22-P3P	080 110		
PCI_AD23v	P3	AD23-P3P	080 110		
PCI_AD24v	F3	AD24-P3P	080 110		
PCI_AD25v	R1	AD25-P3P	080 110		
PCI_AD26v	E2	AD26-P3P	080 110		
PCI_AD27v	N4	AD27-P3P	080 110		
PCI_AD28v	D1	AD28-P3P	080 110		
PCI_AD29v	P4	AD29-P3P	080 110		
PCI_AD30v	E1	AD30-P3P	080 110		
PCI_AD31v	P5	AD31-P3P	080 110		
C/BE0v	K2	CBE0-P3N	080 110		
C/BE1v	K5	CBE1-P3N	080 110		
C/BE2v	N1	CBE2-P3N	080 110		
C/BE3v	R2	CBE3-P3N	080 110		
FRAMEv	F1	FRAME-P3N	080 110		
IRDYv	N3	IRDY-P3N	080 110		
DEVSELv	H1	DEVSEL-P3N	080 110		
TRDYv	H2	TRDY-P3N	080 110		
STOPv	H2	STOP-P3N	080 110		
PLOCKv	M1				
PARv	G5	PAR-P3P	080 110		
PERRv	M2	PERR-P3N	110		
GNT0v	A4				
GNT1v	E3				
GNT2v	D2	PGNT2-P3N	110		
GNT3v	D6	PGNT3-P3N	080		
GNT4v	B4				
GNT5v	B3				
GNT6v	B6				
GNT7v					
GNT8v					
GNT9v					
GP10v					
GP11v					
GP12v					
GP13v					
GP14v					
GP15v					
GP16v					
PCIRST	Y1				



IC103 C4 PINPROHOT-P3N 11/22  
IC103 BISTEP0DMR INX 11/20  
REF109-REV04 GNT#09/27  
PCIRST=brookdale  
REF:NORTHWOOD TS REV.00

Engineer		<b>INVENTEC</b>	TITLE: <b>FMNSY2-VP ICH3-M(1)</b>	Size: <b>A3</b>
Engy Name				
Drawn by				
Drawn Name				
R&D CHK				
DOC CTRL CHK				
MFG ENGR CHK				

Changed by	Date Changed	Time Changed	QA CHK	VER	Model Number	Sheet
TSB1	Friday, February 22, 2002	3:21:53 pm		110	Model No	60 of

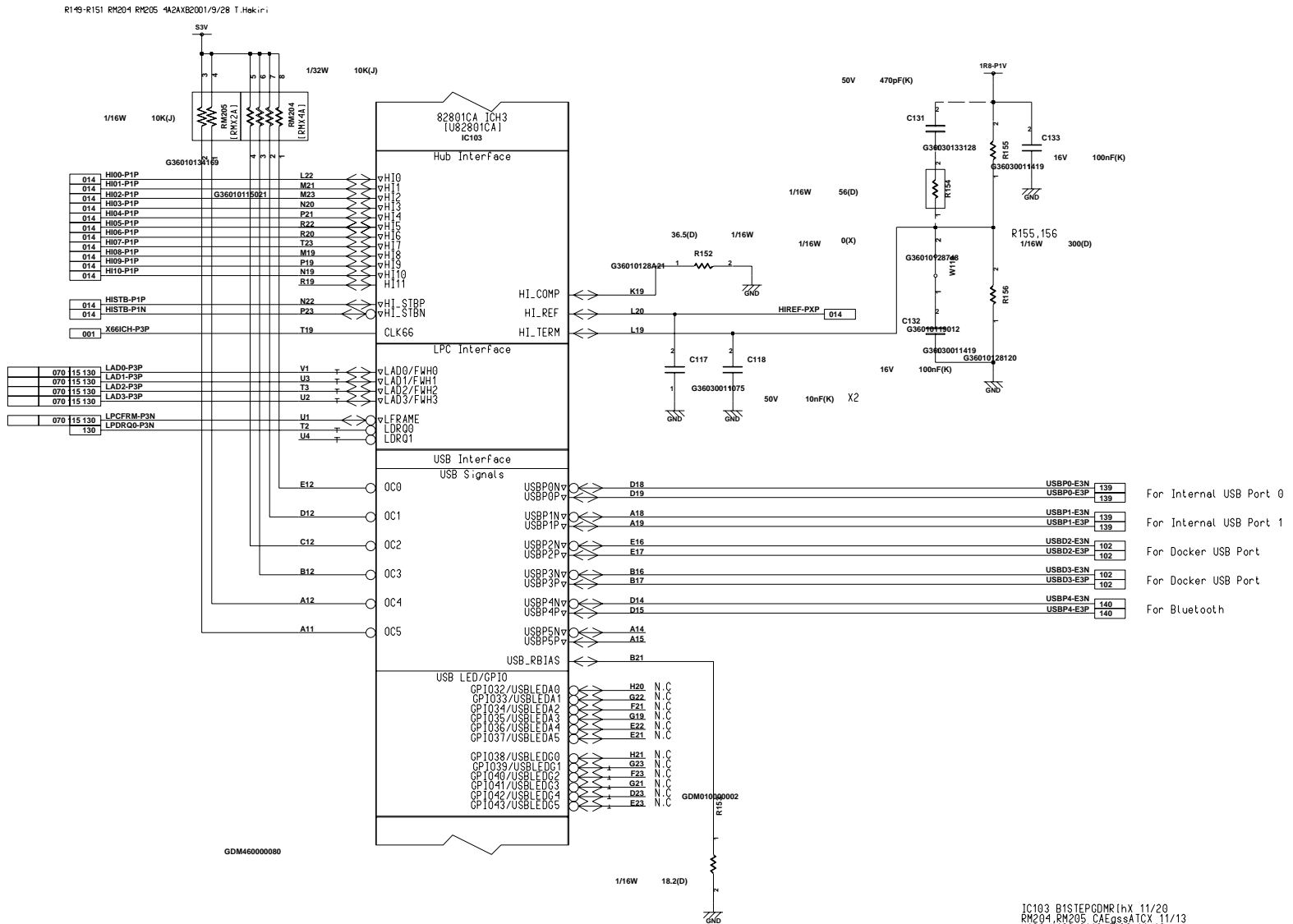


IC103 B1STEPGDMR[hx 11/20  
W111-W114 do NOT MOUNT 9/26  
REF:SUZUKA(FS2102 REV.00).

Engineer Engr. Name	<b>INVENTEC</b>
Drawn by Drawer Name	
R&D CHK	
DOC CTRL CHK	
MFG ENGR CHK	
TITLE: <b>FMNSY2-VP</b> <b>ICH3-M (2)</b>	
Model Number	Sheet 61 of

Changed by TSB1	Date Changed Friday, February 22, 2002	Time Changed 3:22:02 pm	QA CHK
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VER 110	Model No	Sheet 61 of
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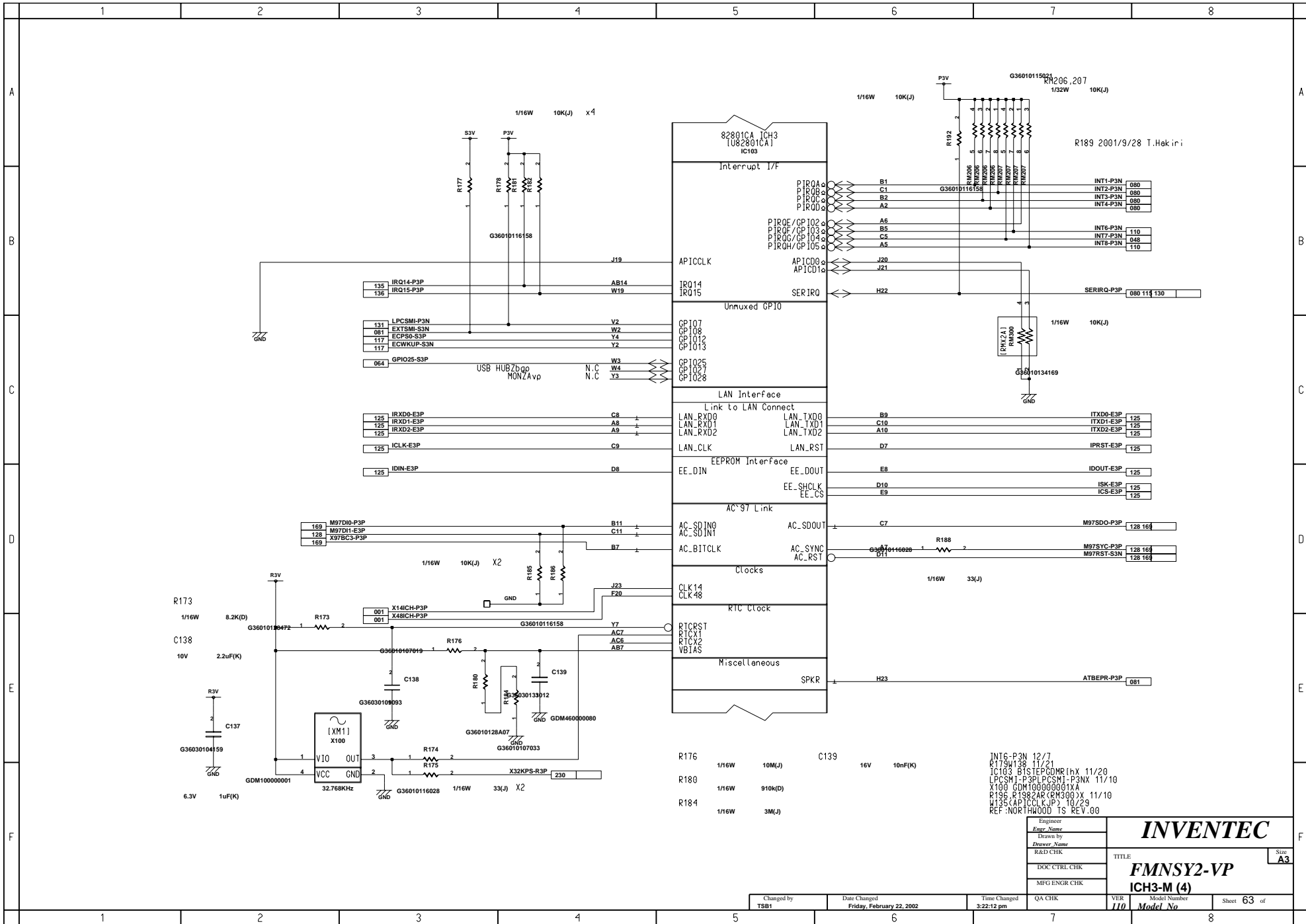


IC103 B1STEPQMR[hx 11/20  
 RM204, RM205 CAEgssATCX 11/13  
 W120-W129 C119-C130 11/10  
 REF:NORTHWOOD TS REV.00

Engineer		<b>INVENTEC</b>	Size <b>A3</b>
Drawn by			
R&D CHK			
DOC CTRL CHK			
MFG ENGR CHK			
TITLE		<b>FMNSY2-VP</b>	
Model Number		<b>ICH3-M (3)</b>	

Changed by <b>TSB1</b>	Date Changed <b>Friday, February 22, 2002</b>	Time Changed <b>3:22:06 pm</b>	QA CHK	VER <b>110</b>	Model No <b>Model No</b>	Sheet <b>62</b> of
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G36010115921  
RM206,207  
1/32W 10K(J)

R189 2001/9/28 T.Hakini

INT6-P3N 12/7  
R179W138 11/21  
IC103 BISTEPGDMR[hx 11/20  
LPCSM1-P3PLPCSM1-P3NX 11/10  
X100 GDM100000001A  
R186 R1882AR(RM300)X 11/10  
R185(APICCLKJP) 10/29  
REF-NORTHWOOD TS REV.00

Engineer	
Engr Name	
Drawn by	
Drawer Name	
R&D CHK	
DOC CTRL CHK	
MFG ENGR CHK	
TITLE	Size
<b>FMNSY2-VP</b>	<b>A3</b>
<b>ICH3-M (4)</b>	

Changed by	Date Changed	Time Changed	QA CHK	VER	Model Number	Sheet
TSB1	Friday, February 22, 2002	3:22:12 pm		110	Model No	63 of

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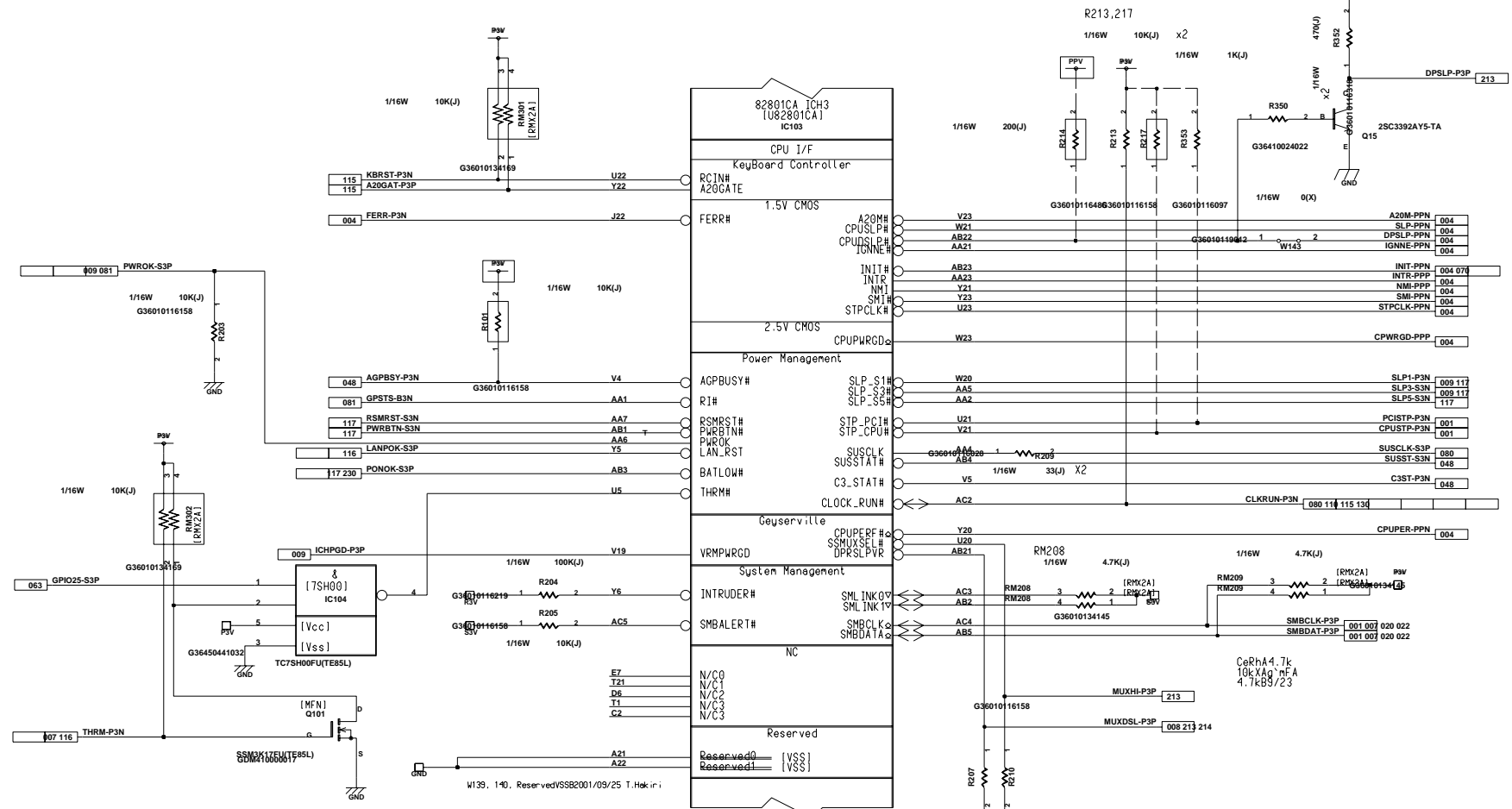
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C

D

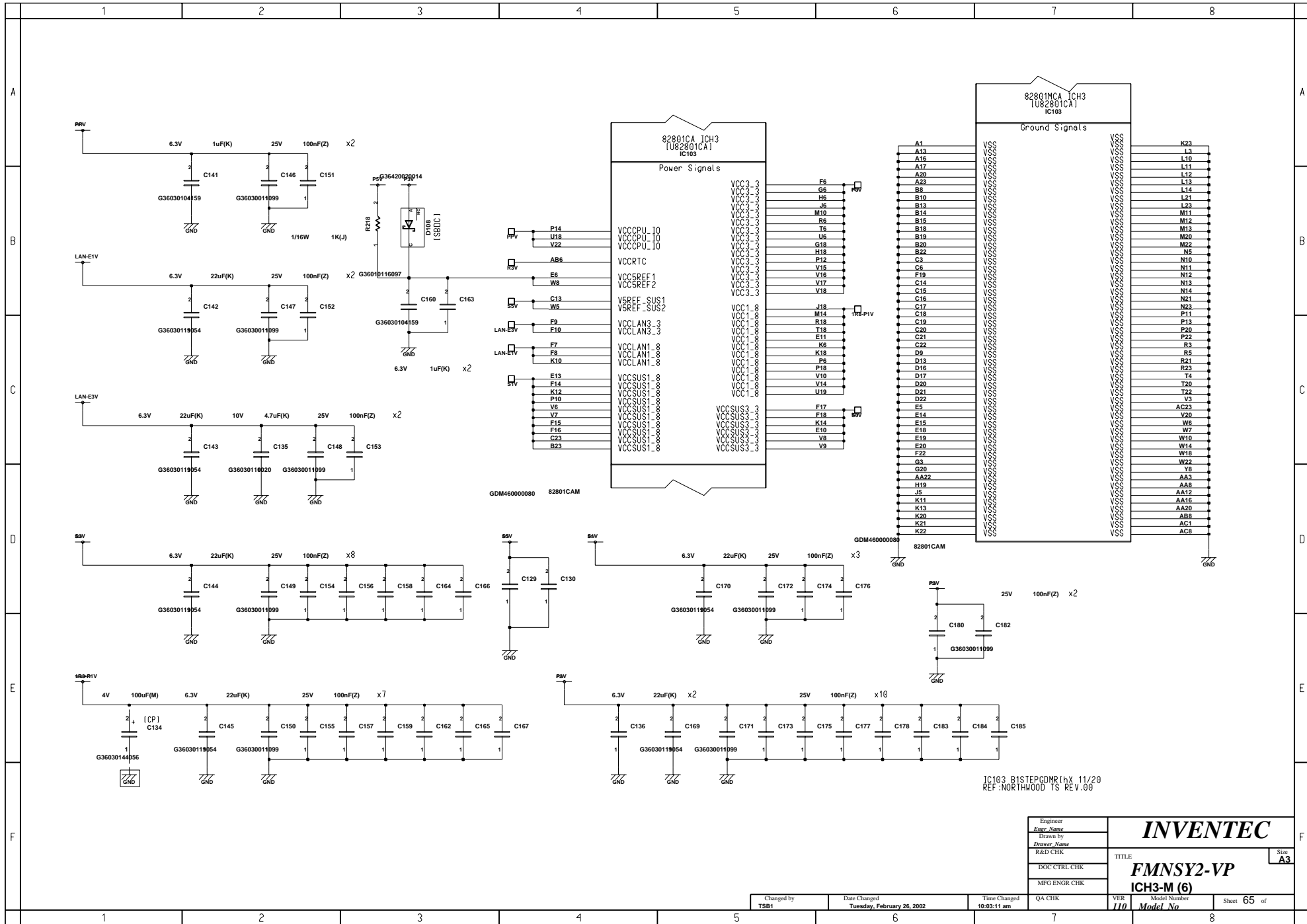
E

F



01010: X 12/6  
 R202, W300, 11/21  
 IC103 BISTEPGDMR InX 11/20  
 R207 MOUNTX 11/20  
 G15 1X 11/17  
 R207 NOT MOUNTXAW300AR103,  
 R199, R2002AR(RM302)XA  
 R203, R2052AR(RM301)X 11/10  
 W141(GPSTS-B3NJP) 10/23  
 C140  
 REF: NORTHWOOD TS REV.00

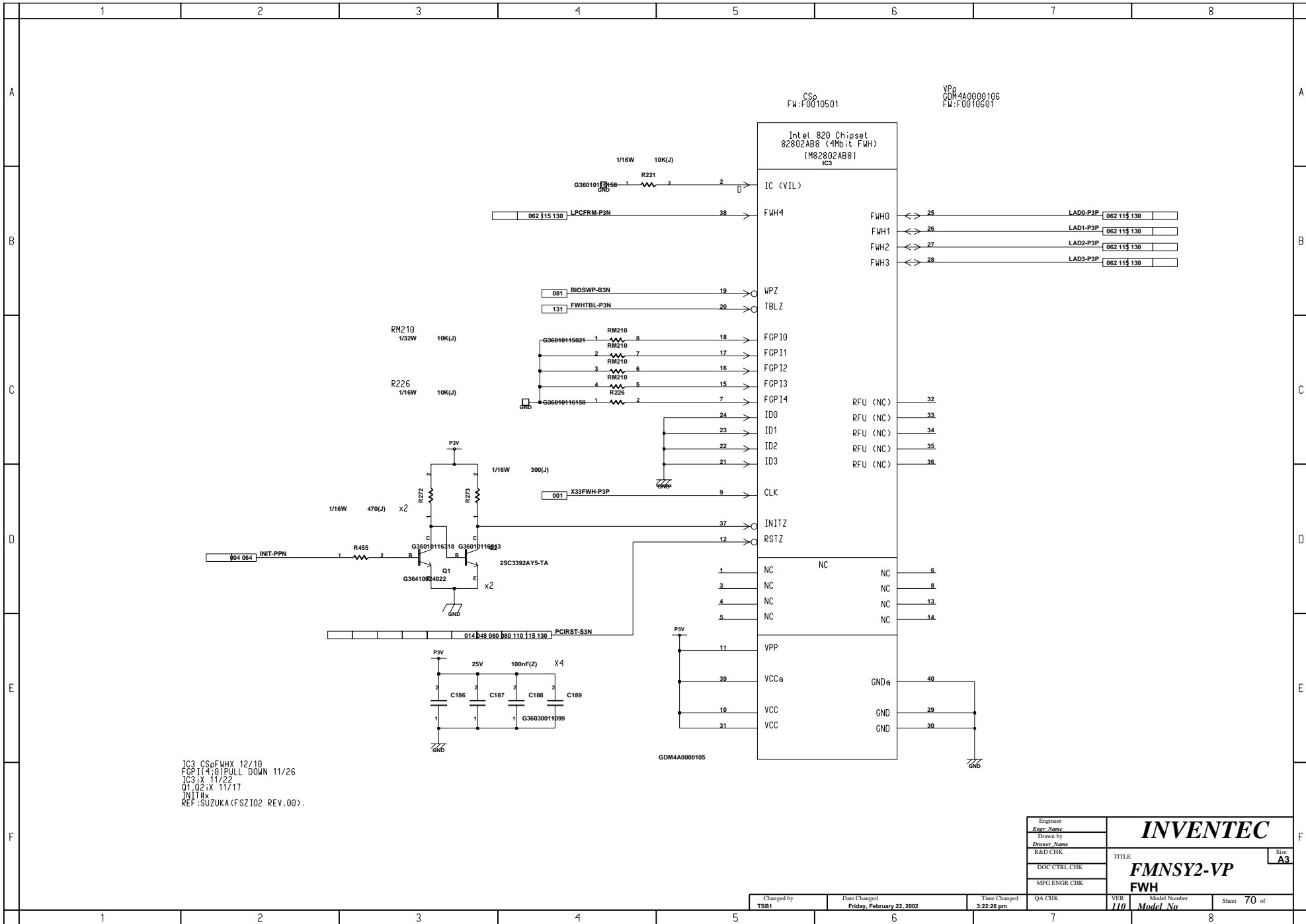
Engineer <i>Engy Name</i>	<b>INVENTEC</b>						
Drawn by <i>Drawer Name</i>							
R&D CHK			TITLE <b>FMNSY2-VP</b>	Size <b>A3</b>			
DOC CTRL CHK			Model No				
MFG ENGR CHK			Model No				
Chang'd by <b>TSB1</b>		Date Changed <b>Friday, February 22, 2002</b>	Time Changed <b>3:22:17 pm</b>	QA CHK	VER <b>110</b>	Model Number <b>Model No</b>	Sheet <b>64</b> of
1		2		3		8	



IC103 B1STFPCDMR(hx 11/20  
REF:NORTHWOOD TS REV.00

Engineer <i>Engy Name</i>	<b>INVENTEC</b> TITLE: <b>FMNSY2-VP</b> <b>ICH3-M (6)</b> Model No	Size <b>A3</b>
Drawn by <i>Drawn Name</i>		
R&D CHK <i>R&amp;D Name</i>		
DOC CTRL CHK <i>Doc Name</i>		
MFG ENGR CHK <i>Mfg Name</i>		

Changed by <b>TSB1</b>	Date Changed <b>Tuesday, February 26, 2002</b>	Time Changed <b>10:03:11 am</b>	QA CHK	VER <b>110</b>	Sheet <b>65</b> of
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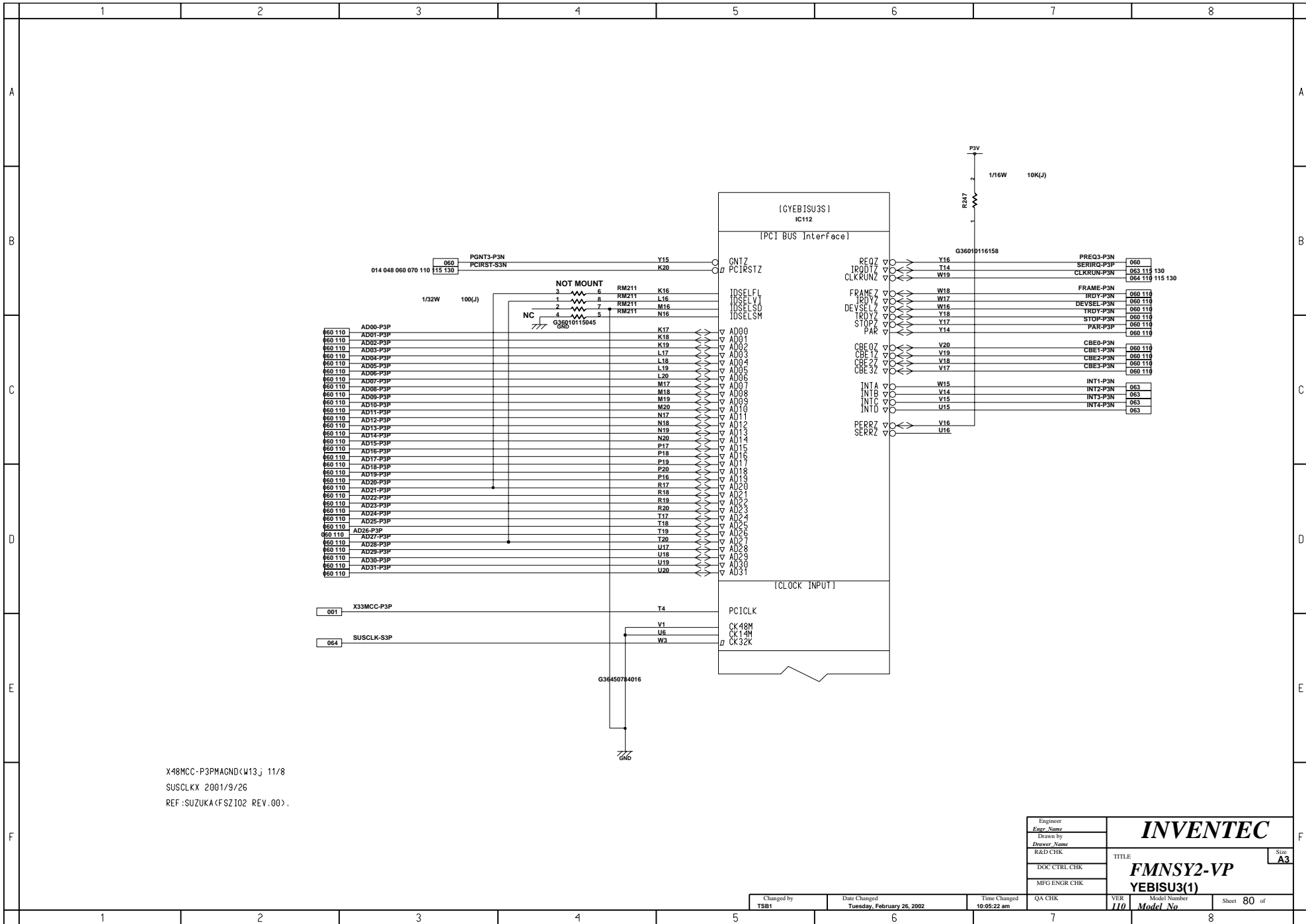
IC3 CS0 FWHX 12/10  
 FGP14:0 PULL DOWN 11/26  
 IC3-X 11/22  
 Q1 Q2: X 11/17  
 INIT: X  
 REF: SUZUKA(FS2102 REV.00).

CS0  
 FW:F0010501  
 VP: GDM440000106  
 FW:F0010601

Engineer	
Engr Name	
Drawn by	
Drawn Name	
R&D CHK	TITLE: <b>FMNSY2-VP</b>
DOC CTRL CHK	Model Number
MFG ENGR CHK	Sheet 70 of 70

**INVENTEC**  
**FMNSY2-VP**  
**FWH**  
 Model No  
 Size A3

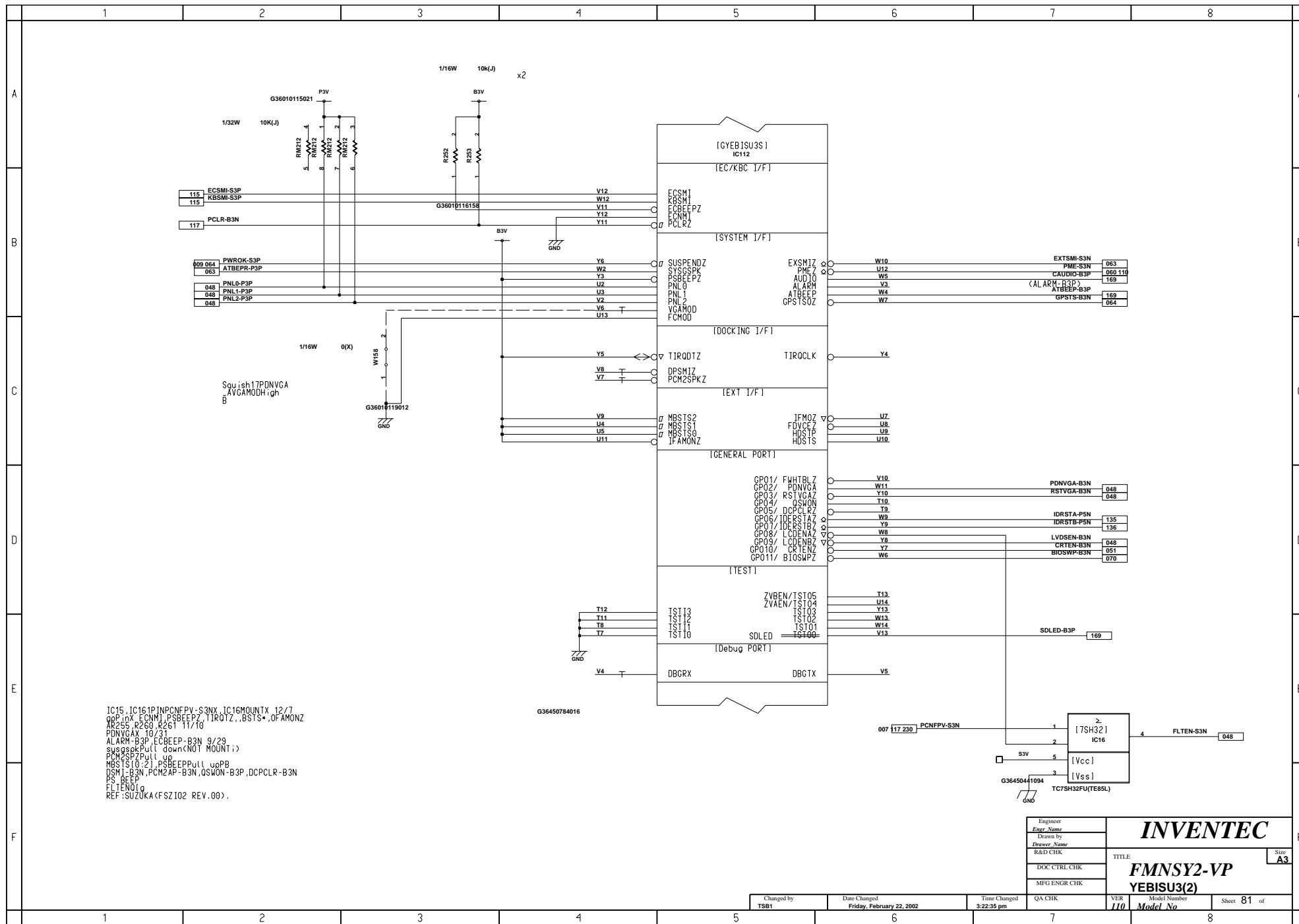
Changed by	Date Changed	Time Changed	QA CHK	VER	Model No	Sheet
TSB1	Friday, February 22, 2002	3:22:26 pm		110		70 of 70



X48MCC-P3PMAGND(W13J 11/8  
 SUSCLKX 2001/9/26  
 REF:SUZUKA(FSZ102 REV.00).

Engineer Eng Name	<b>INVENTEC</b> TITLE: <b>FMNSY2-VP</b> <b>YEBISU3(1)</b>	Size <b>A3</b>
Drawn by Drawer Name		
R&D CHK		
DOC CTRL CHK		
MFG ENGR CHK		

Changed by TSB1	Date Changed Tuesday, February 26, 2002	Time Changed 10:05:22 am	QA CHK	VER 110	Model Number ModL No	Sheet 80 of
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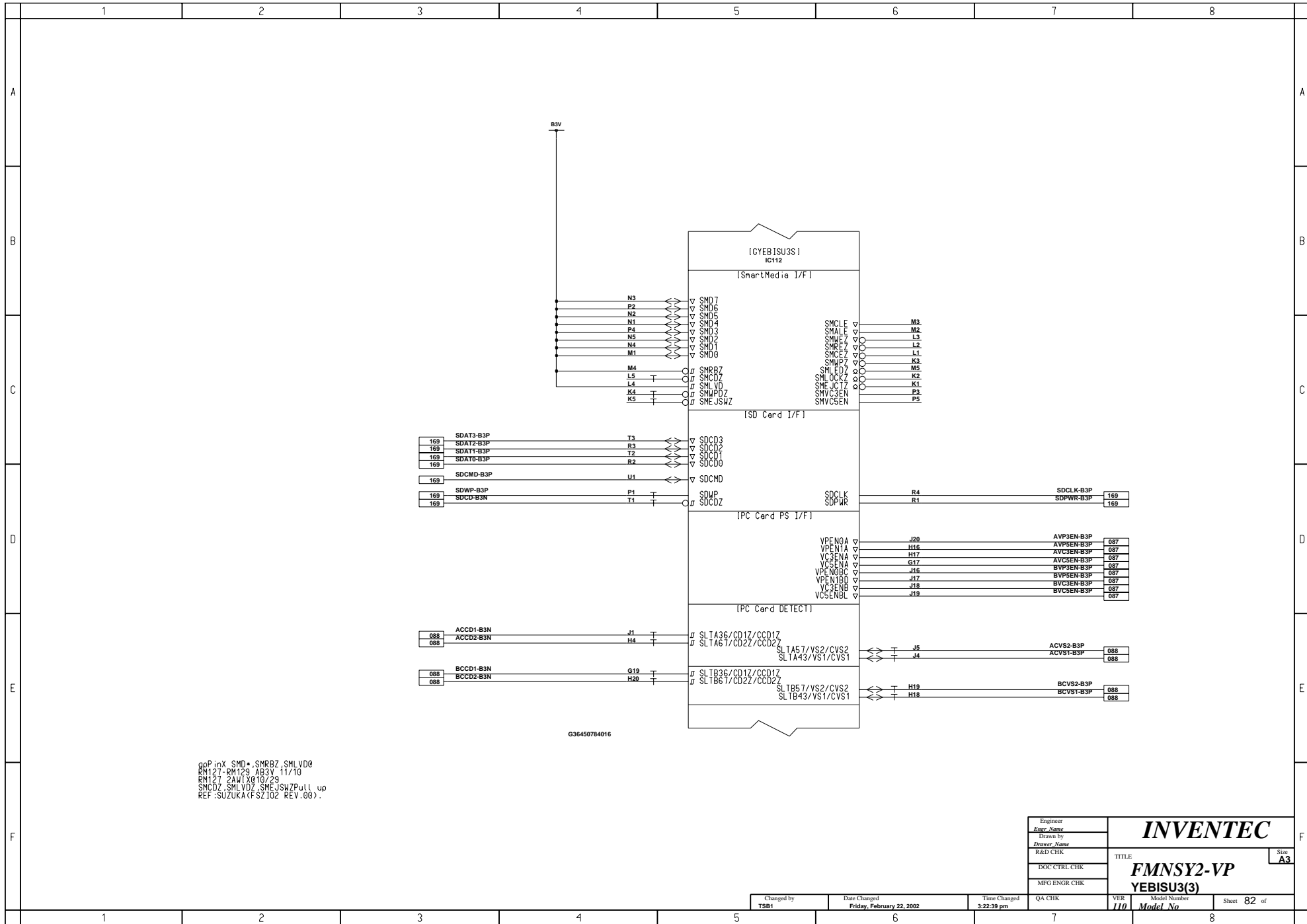
IC15, IC16, IPINCPV, S3N, IC16MOUNTX, 12/7  
 GP, ON, FCON, PSBEEPZ, TIRQDZ, BSTS, OFAMONZ  
 AK25, R253, R251, 11/10  
 PDNVGA, 10/31  
 ALARM-B3P, ECBEEP-B3N, 9/29  
 SUSPENDZ, pull down (NOT MOUNTED)  
 PDM2AP-B3N, 11/10  
 PMSBEEP, pull up  
 MBSTS[0:2], PSBEEP, pull up  
 DSM1-B3N, PCM2AP-B3N, QSWON-B3P, DCPCLR-B3N  
 PS-BEEP  
 FLIEN01  
 REF:SUZUKA(FSZ102 REV.00).

G36450784016

Engineer	
Engr Name	
Drawn by	
Drawer Name	
R&D CHK	
DOC CTRL CHK	
MFG ENGR CHK	

<b>INVENTEC</b>		Size <b>A3</b>
TITLE: <b>FMNSY2-VP</b>		
<b>YEBISU3(2)</b>		
Model Number	Sheet <b>81</b> of	

Changed by	Date Changed	Time Changed	QA CHK	VER	Model No
TSB1	Friday, February 22, 2002	3:22:35 pm		110	



G36450784016

qpP inX\_SMD\*.SMRBZ.SMLVD0  
 RM127-RM129\_AB3V\_11/10  
 RM127\_2411X010/29  
 SMC0Z\_SMLVDZ\_SMEJSWPull up  
 REF:SUZUKA(FSZ102 REV.00).

Engineer <i>Engr Name</i>	<b>INVENTEC</b>  <b>FMNSY2-VP</b> <b>YEBISU3(3)</b>	Size <b>A3</b>
Drawn by <i>Drawer Name</i>		TITLE
R&D CHK		
DOC CTRL CHK		
MFG ENGR CHK		

Changed by <b>TSB1</b>	Date Changed <b>Friday, February 22, 2002</b>	Time Changed <b>3:22:39 pm</b>	QA CHK	VER <b>110</b>	Model Number <i>Model No</i>	Sheet <b>82</b> of
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A

B

C

D

E

F

A

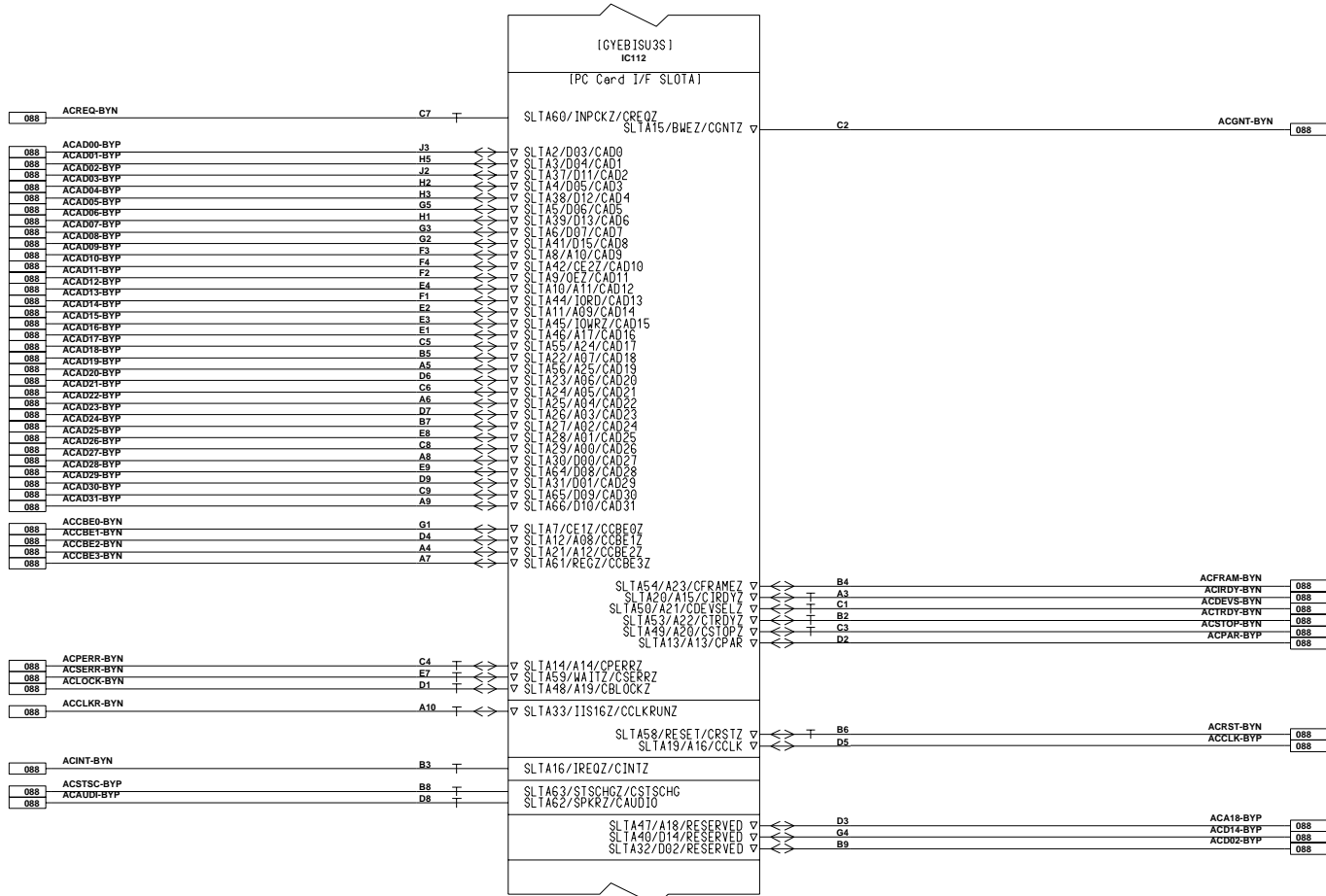
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C

D

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F



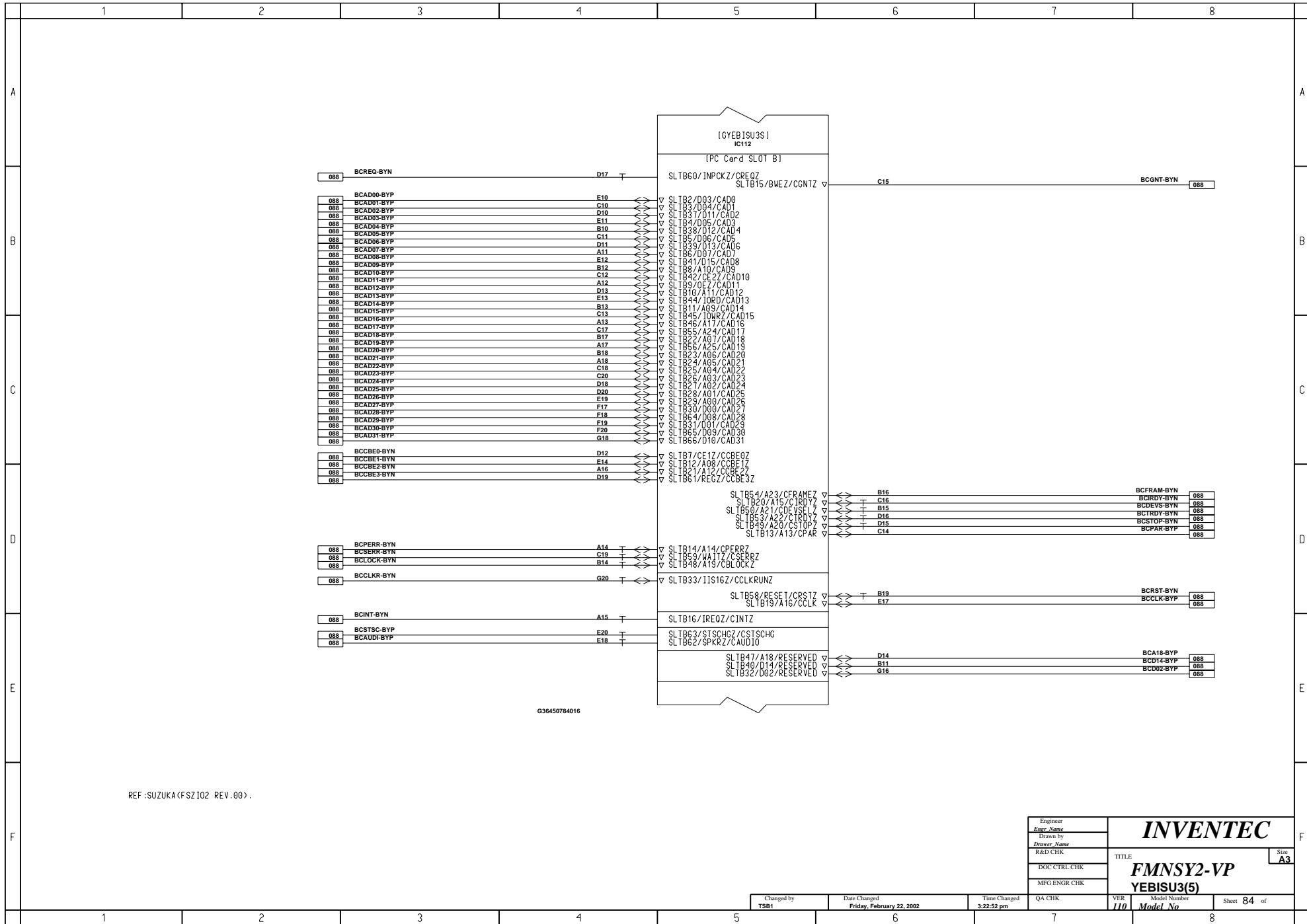
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REF:SUZUKA(FS2I02 REV.00).

Engineer <i>Engr. Name</i>	<b>INVENTEC</b>
Drawn by <i>Drawer Name</i>	
R&D CHK	
DOC CTRL CHK	
MFG ENGR CHK	
TITLE	Size <b>A3</b>
<b>FMNSY2-VP</b>	
<b>YEBISU3(4)</b>	

Changed by <b>TSB1</b>	Date Changed <b>Friday, February 22, 2002</b>	Time Changed <b>3:22:42 pm</b>	QA CHK	VER <b>110</b>	Model Number <i>Model No</i>	Sheet <b>83</b> of
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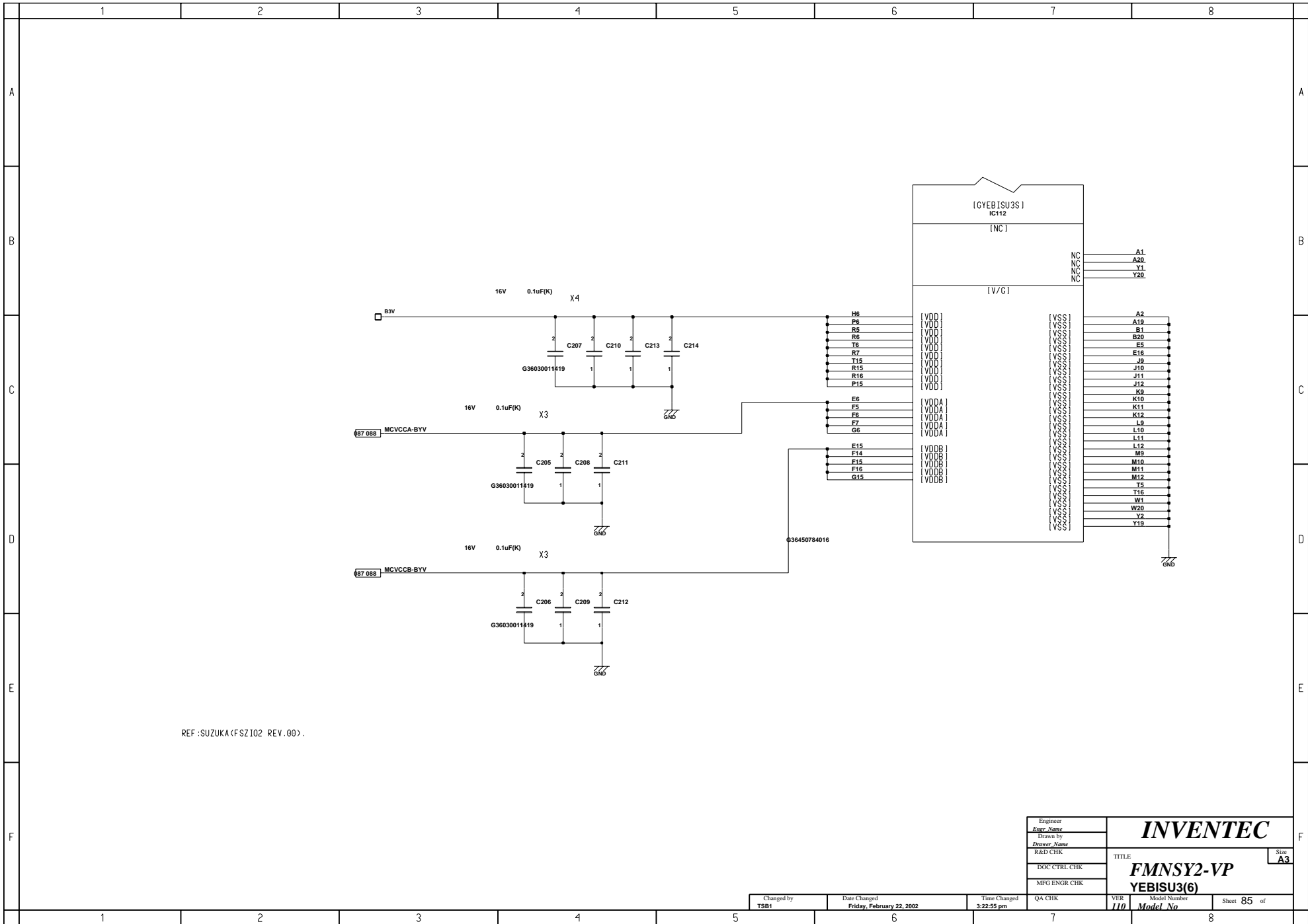


REF:SUZUKA(FSZ102 REV.00).

G36450784016

Engineer		<b>INVENTEC</b> TITLE: <b>FMNSY2-VP</b> <b>YEBISU3(5)</b>	Size <b>A3</b>
Eng. Name			
Drawn by			
Drawer Name			
R&D CHK			
DOC CTRL CHK			
MFG ENGR CHK			

Changed by	Date Changed	Time Changed	QA CHK	VER	Model Number	Sheet	of
TSB1	Friday, February 22, 2002	3:22:52 pm		110	Model No	84	



REF:SUZUKA(FS2102 REV.00).

Engineer <i>Engr Name</i>	<b>INVENTEC</b>  <b>FMNSY2-VP</b> <b>YEBISU3(6)</b>	Size <b>A3</b>
Drawn by <i>Drawer Name</i>		TITLE
R&D CHK		
DOC CTRL CHK		
MFG ENGR CHK		

Changed by <b>TSB1</b>	Date Changed <b>Friday, February 22, 2002</b>	Time Changed <b>3:22:55 pm</b>	QA CHK	VER <b>110</b>	Model Number <i>Model No</i>	Sheet <b>85</b> of
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	1	2	3	4	5	6	7	8	
A	BLANK SHEET								A
B									B
C									C
D									D
E									E
F									F
	1	2	3	4	5	6	7	8	

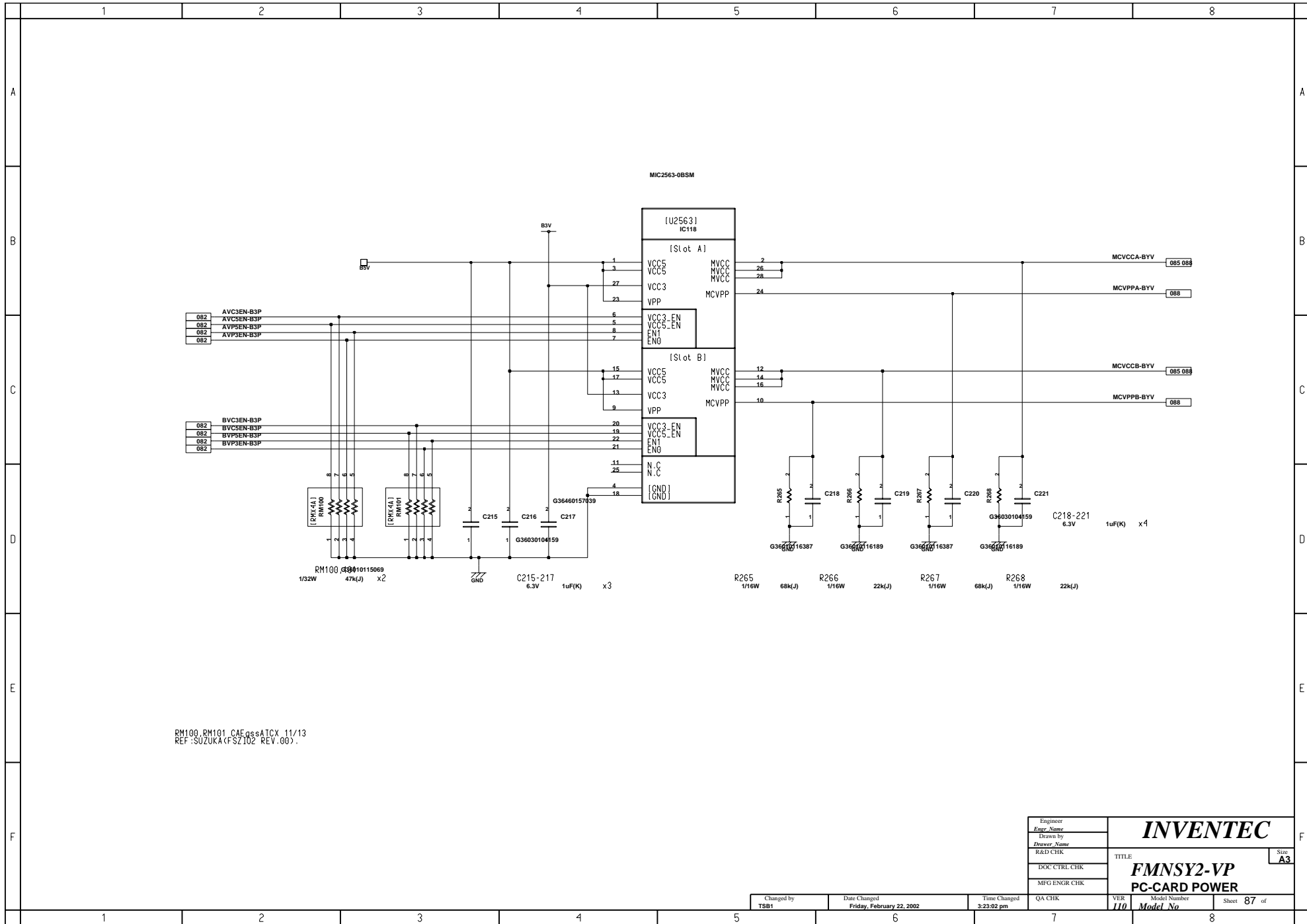
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Engr Name		
Drawn by		
Drawer Name		
R&D CHK		
DOC CTRL CHK		
MFG ENGR CHK		
QA CHK		
VER	110	Size A3
Model Number	86 of	
Model No	86 of	

Changed by	Date Changed	Time Changed
TSB1	Friday, February 22, 2002	3:22:59 pm

**INVENTEC**

**FMNSY2-VP**  
**BLANK SHEET**



RM100\_RM101\_CAEgssATCX.11/13  
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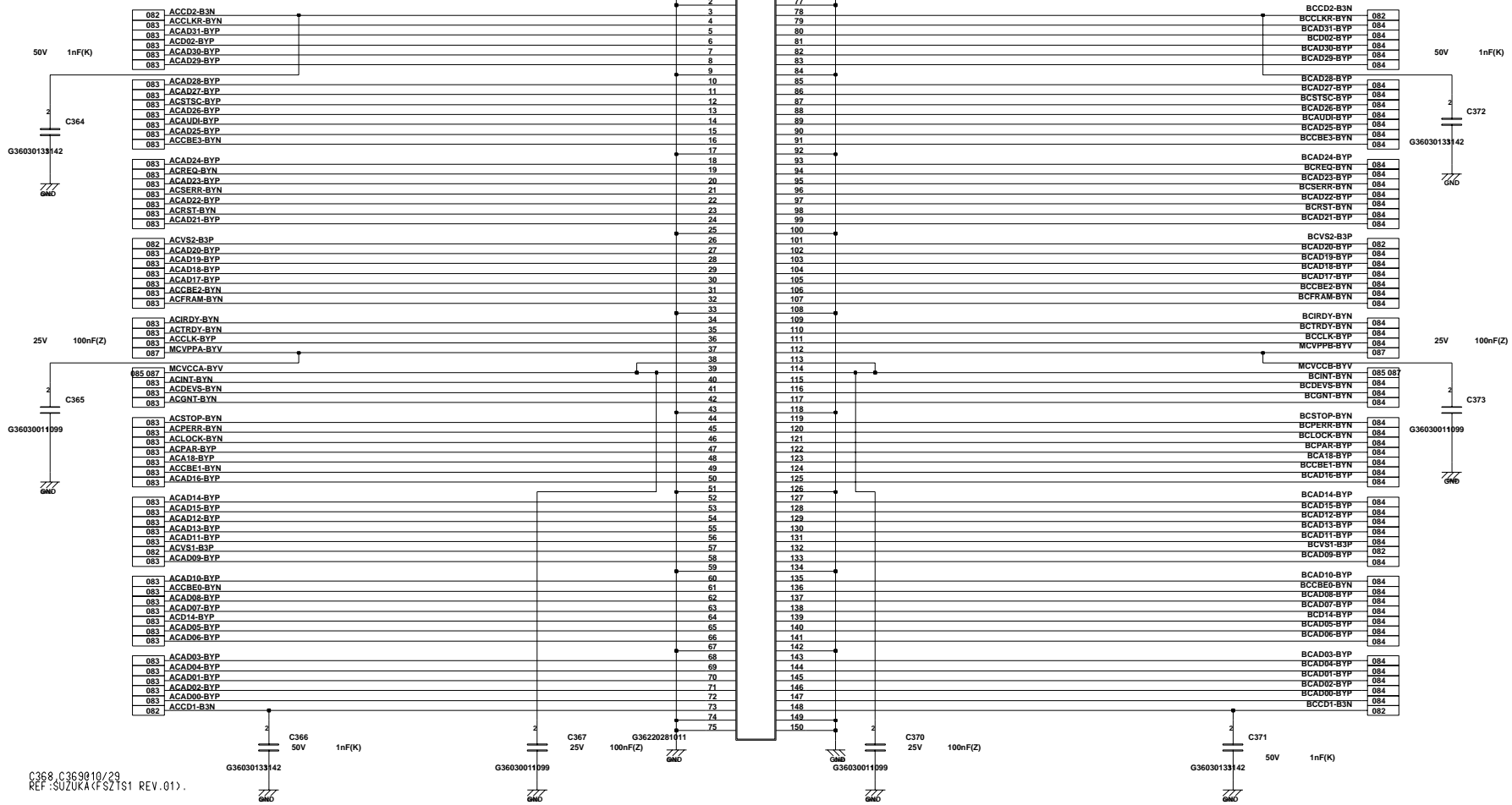
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Engr Name	
Drawn by	
Drawer Name	
R&D CHK	
DOC CTRL CHK	
MFG ENGR CHK	
<b>INVENTEC</b>	
TITLE: <b>FMNSY2-VP</b>	
<b>PC-CARD POWER</b>	
Size <b>A3</b>	

Changed by	Date Changed	Time Changed	QA CHK	VER	Model Number	Sheet	of
TSB1	Friday, February 22, 2002	3:23:02 pm		110	Model No	87	

(PJ36) EJECTOR GDM22000000B

0.5A/PIN

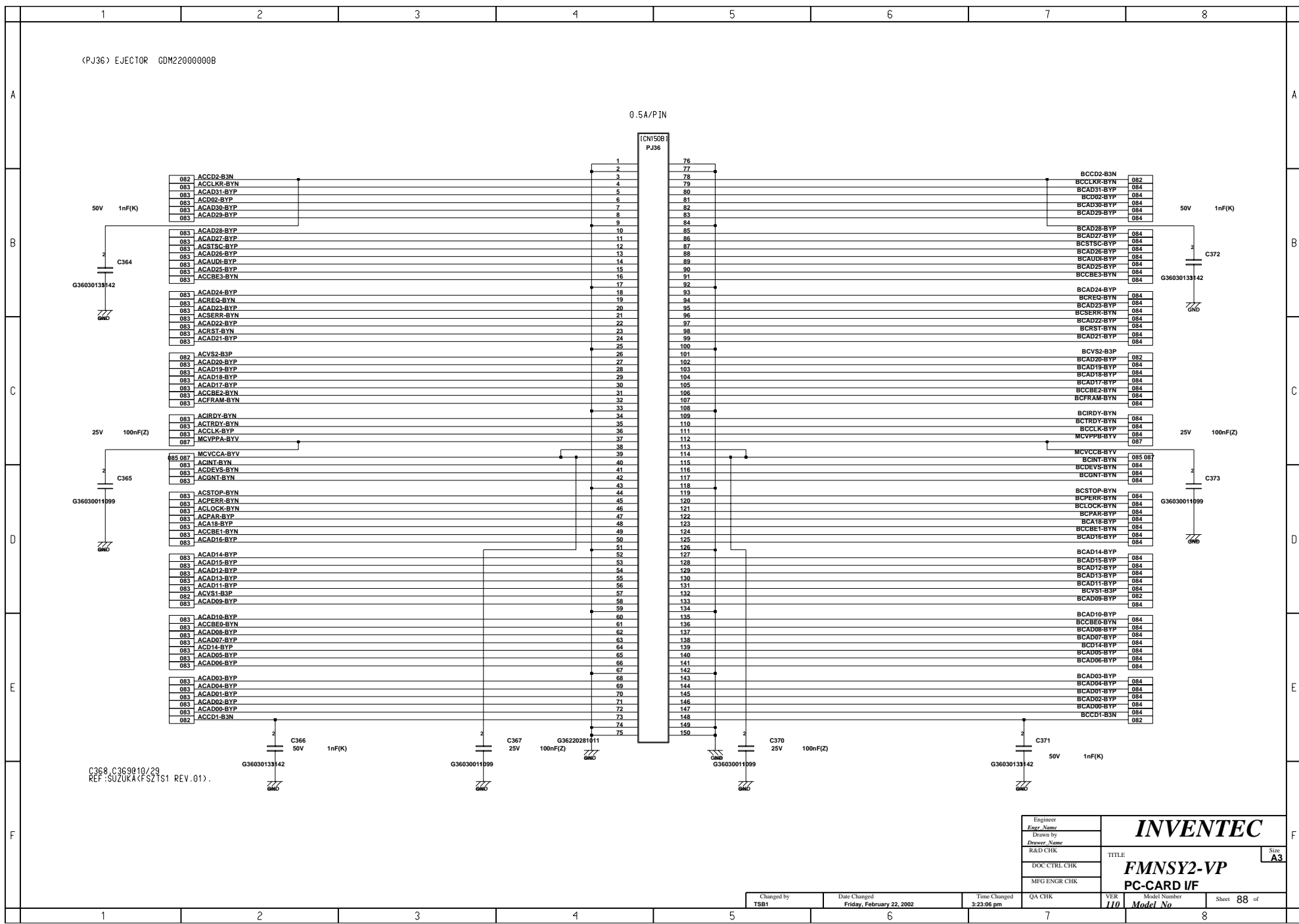
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PJ36

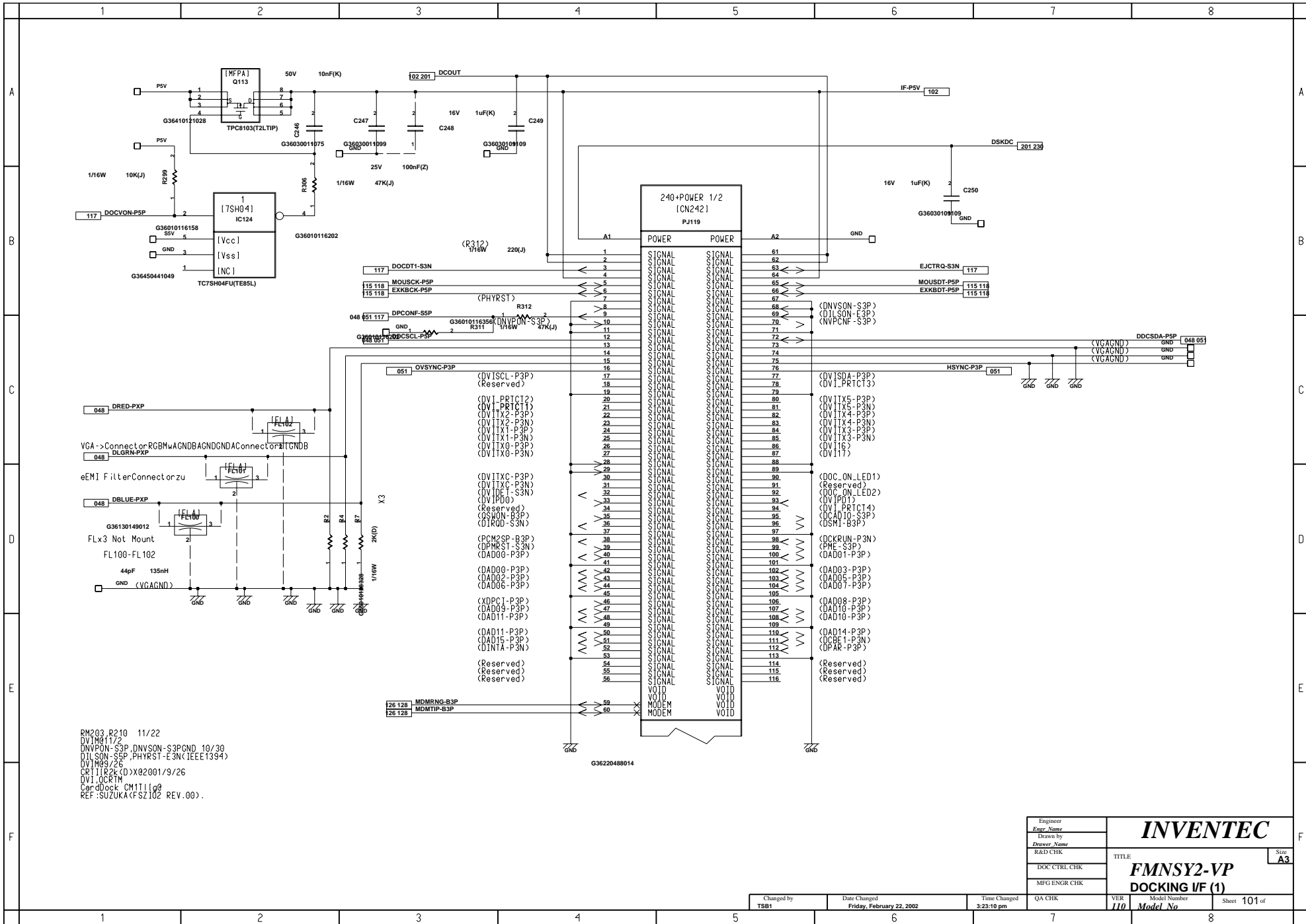


C368, C369, 10/24  
REF: SUZUKA(FS2T1S1 REV.01).

Engineer <i>Engr Name</i>	<b>INVENTEC</b> TITLE: <b>FMNSY2-VP</b> <b>PC-CARD I/F</b> Size <b>A3</b>
Drawn by <i>Drawer Name</i>	
R&D CHK	
DOC CTRL CHK	
MFG ENGR CHK	

Changed by TSB1	Date Changed Friday, February 22, 2002	Time Changed 3:23:06 pm	QA CHK	VER 110	Model Number Model No	Sheet 88 of
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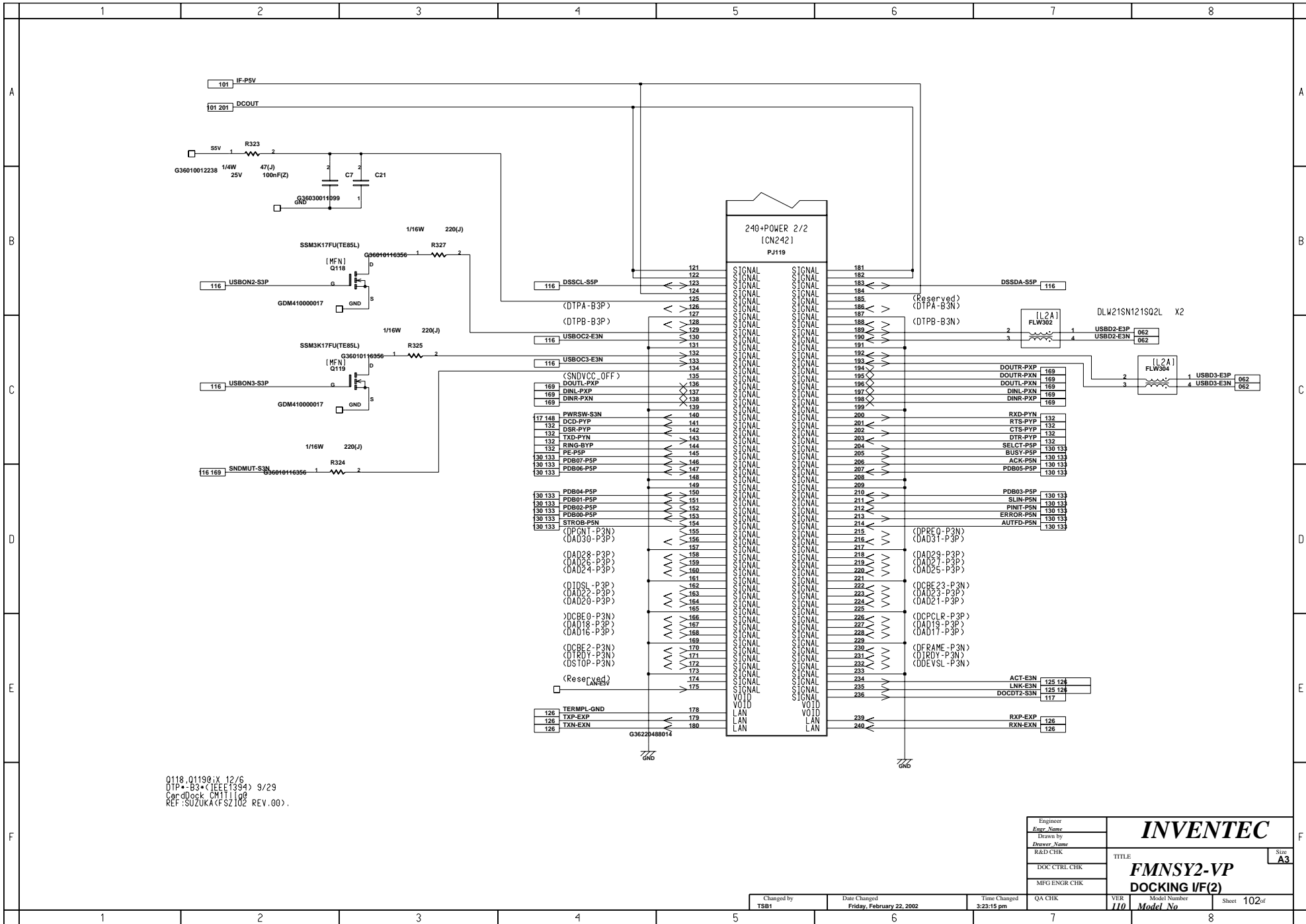
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 DILSON-S3P\_PHYRST-E-3N1EEE1334)  
 DVI109/26  
 CRT11R2E(D)X02001/9/26  
 DVI10CRTH  
 GndDock\_CM1111.g0  
 REF:SUZUKA(FSZ102 REV.00).

Engineer	
Engr Name	
Drawn by	
Drawer Name	
R&D CHK	
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MFG ENGR CHK	

**INVENTEC**  
 TITLE: **FMNSY2-VP DOCKING I/F (1)**  
 Model No

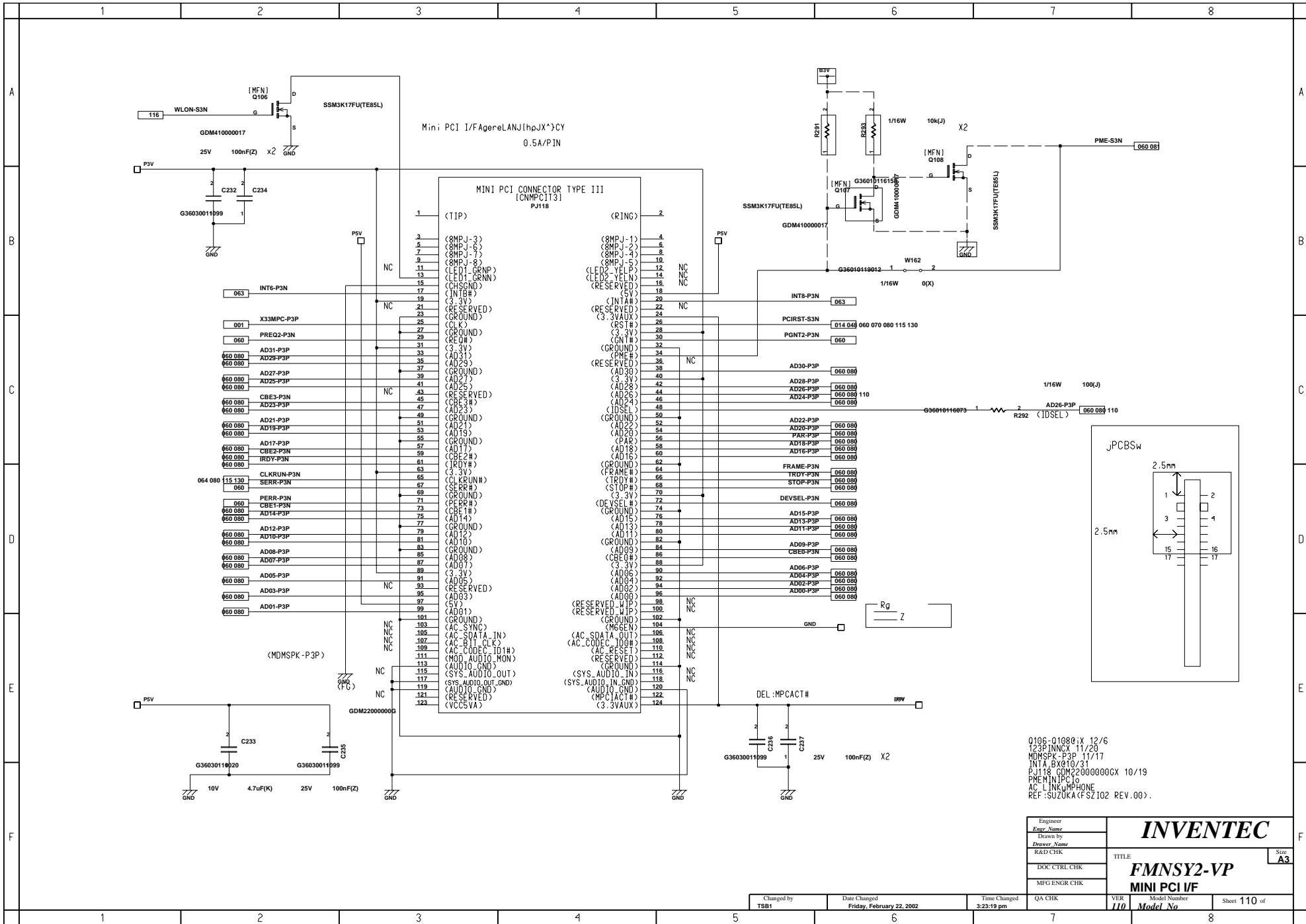
Changed by	Date Changed	Time Changed	QA CHK	VER	Model Number	Sheet
TSB1	Friday, February 22, 2002	3:23:10 pm		110		101 of

Size A3



0118\_01190;X 12/6  
DTPA-B3N<IEEE1394> 9/29  
CarDock\_CM111.gd  
REF:SUZUKA(FS2102 REV.00).

Engineer Engr Name		<b>INVENTEC</b>  <b>FMNSY2-VP</b> <b>DOCKING I/F(2)</b>	Size A3
Drawn by Drawer Name			TITLE
R&D CHK			
DOC CTRL CHK			
MFG ENGR CHK			
Changed by TSB1	Date Changed Friday, February 22, 2002	Time Changed 3:23:15 pm	QA CHK Model Number Sheet 102 of

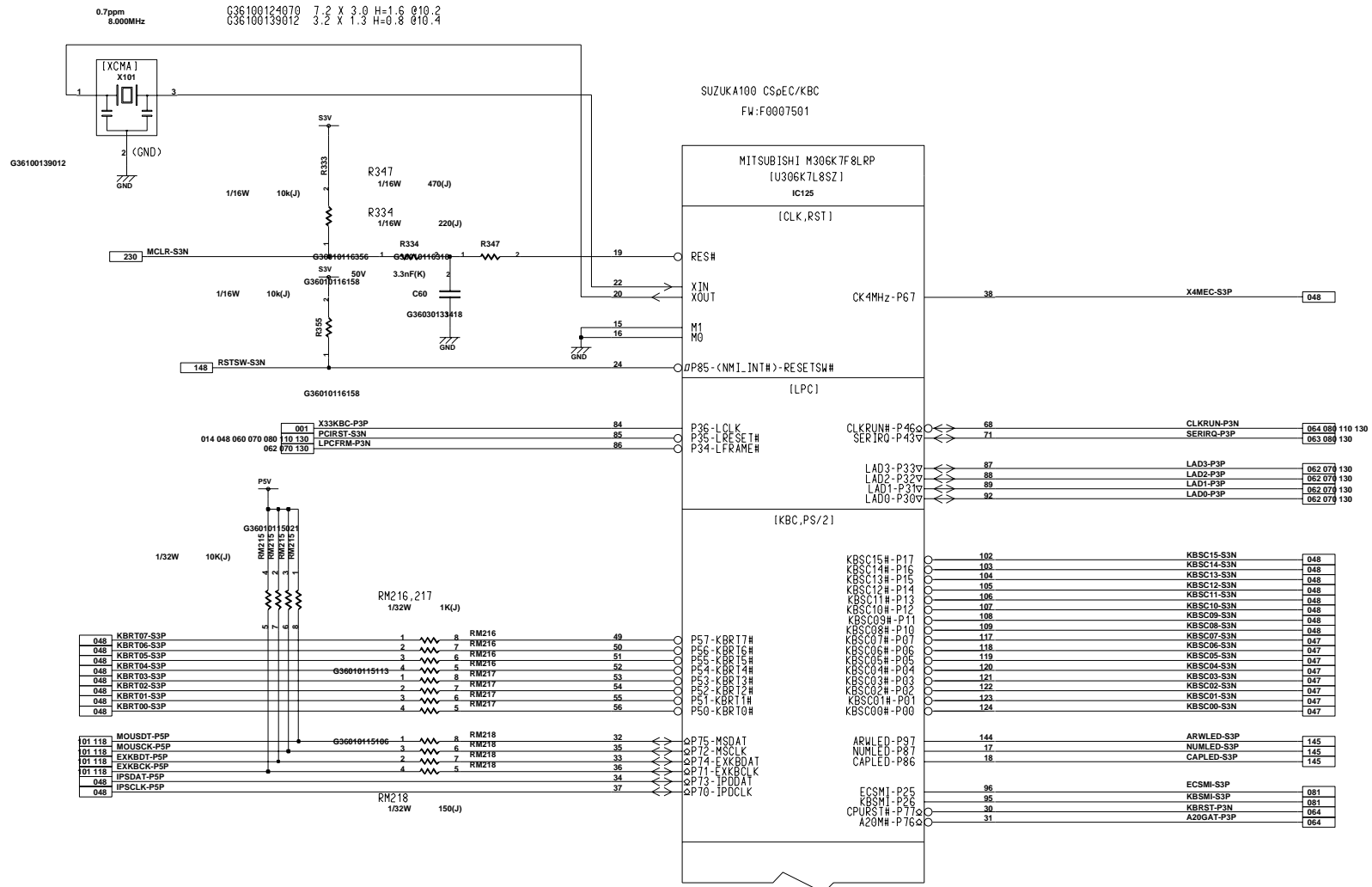


0106-01080;X 12/6  
 123P-INVCX 11/20  
 MDNSPK-P3P 11/17  
 INTA\_BX010/31  
 PJ118\_GDM22000000GX 10/19  
 PHC MINI PCI I/F  
 AG-1 LINEPHONE  
 REF:SUZUKA(FSZ102 REV.00).

Engineer		<b>INVENTEC</b>	Size <b>A3</b>
Eng. Name			
Drawn by			
Drawer Name			
R&D CHK			
DOC CTRL CHK	TITLE	<b>FMNSY2-VP</b>	
MFG ENGR CHK		<b>MINI PCI I/F</b>	
Changed by	Date Changed	Time Changed	QA CHK
TSB1	Friday, February 22, 2002	3:23:19 pm	
VER	Model Number	Sheet	110 of
<b>110</b>	<b>Model No</b>		



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A									A																																										
B									B																																										
C									C																																										
D									D																																										
E									E																																										
F	<p>SH 12/7  0109,01110:IX 12/6  JPB3VX 11/17  JPP3VX 2001/9/26  REF:SUZUKA(FSZ102 REV.00).</p> <table border="1" data-bbox="1630 1332 2040 1449"> <tr> <td>Engineer</td> <td colspan="3"></td> </tr> <tr> <td>Engr Name</td> <td colspan="3"><b>INVENTEC</b></td> </tr> <tr> <td>Drawn by</td> <td colspan="3"></td> </tr> <tr> <td>Drawer Name</td> <td colspan="3"></td> </tr> <tr> <td>R&amp;D CHK</td> <td colspan="3">TITLE: <b>FMNSY2-VP</b></td> </tr> <tr> <td>DOC CTRL CHK</td> <td colspan="3">Size <b>A3</b></td> </tr> <tr> <td>MFG ENGR CHK</td> <td colspan="3"><b>MINIPCI POWER</b></td> </tr> </table> <table border="1" data-bbox="1218 1449 2040 1481"> <tr> <td>Changed by</td> <td>Date Changed</td> <td>Time Changed</td> <td>QA CHK</td> <td>VER</td> <td>Model Number</td> <td>Sheet</td> </tr> <tr> <td><b>TSB1</b></td> <td><b>Friday, February 22, 2002</b></td> <td><b>3:23:23 pm</b></td> <td></td> <td><b>110</b></td> <td><b>Model No</b></td> <td><b>111 of</b></td> </tr> </table>								Engineer				Engr Name	<b>INVENTEC</b>			Drawn by				Drawer Name				R&D CHK	TITLE: <b>FMNSY2-VP</b>			DOC CTRL CHK	Size <b>A3</b>			MFG ENGR CHK	<b>MINIPCI POWER</b>			Changed by	Date Changed	Time Changed	QA CHK	VER	Model Number	Sheet	<b>TSB1</b>	<b>Friday, February 22, 2002</b>	<b>3:23:23 pm</b>		<b>110</b>	<b>Model No</b>	<b>111 of</b>	F
Engineer																																																			
Engr Name	<b>INVENTEC</b>																																																		
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Changed by	Date Changed	Time Changed	QA CHK	VER	Model Number	Sheet																																													
<b>TSB1</b>	<b>Friday, February 22, 2002</b>	<b>3:23:23 pm</b>		<b>110</b>	<b>Model No</b>	<b>111 of</b>																																													
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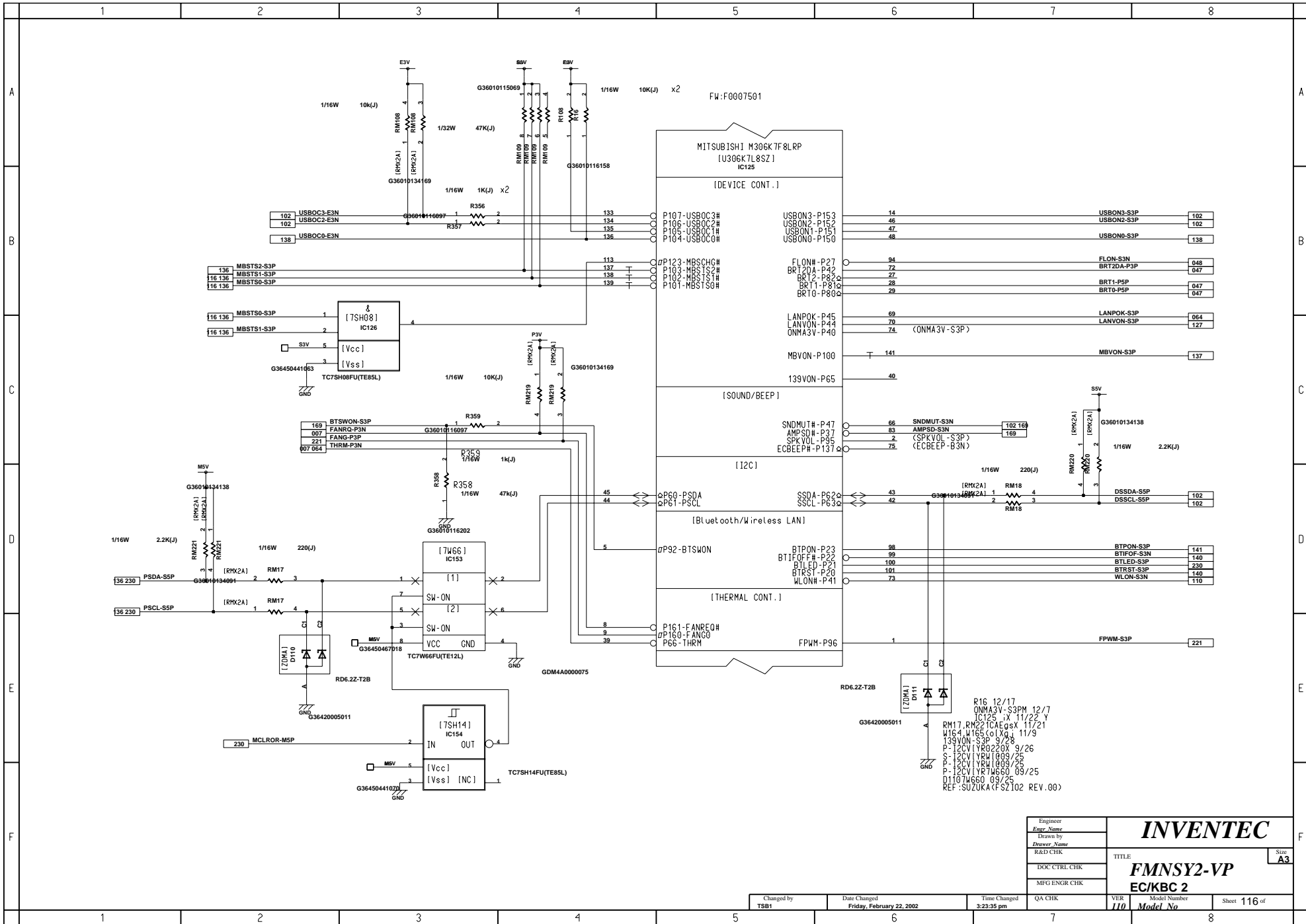


IC125 :X 11/22 Y  
 RM216, RM217 s 11/21  
 RM216, RM217 s 11/1  
 X101: X  
 PCB: RSTSW-S3NVIYR  
 9/25  
 REF: SUZUKA(FS2102 REV.06)

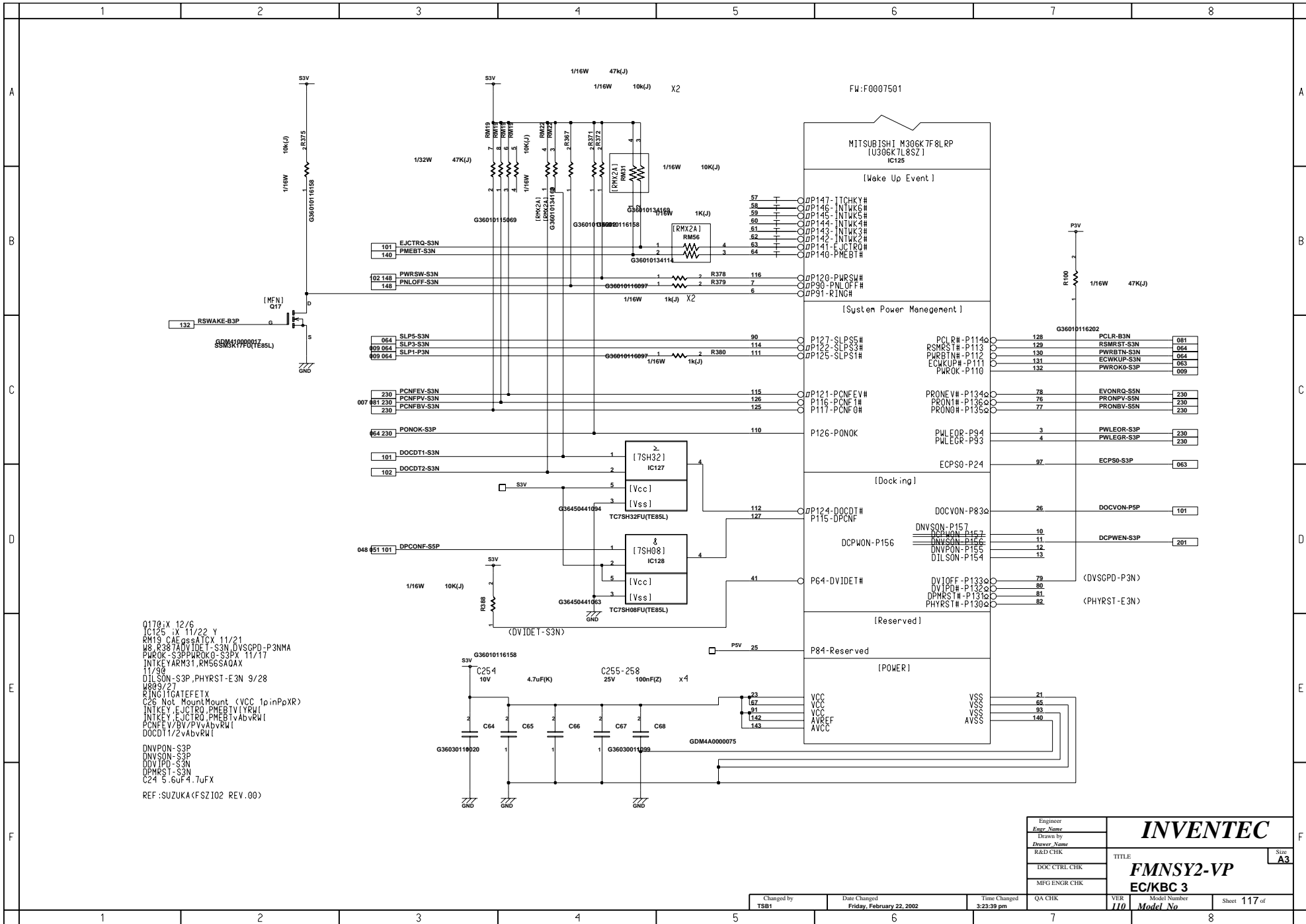
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Engineer Engr Name	<b>INVENTEC</b> TITLE: <b>FMNSY2-VP</b> <b>EC/KBC 1</b> Model No
Drawn by Drawer Name	
R&D CHK	
DOC CTRL CHK	
MFG ENGR CHK	

Changed by TSB1	Date Changed Friday, February 22, 2002	Time Changed 3:23:26 pm	QA CHK	VER 110	Sheet 115 of
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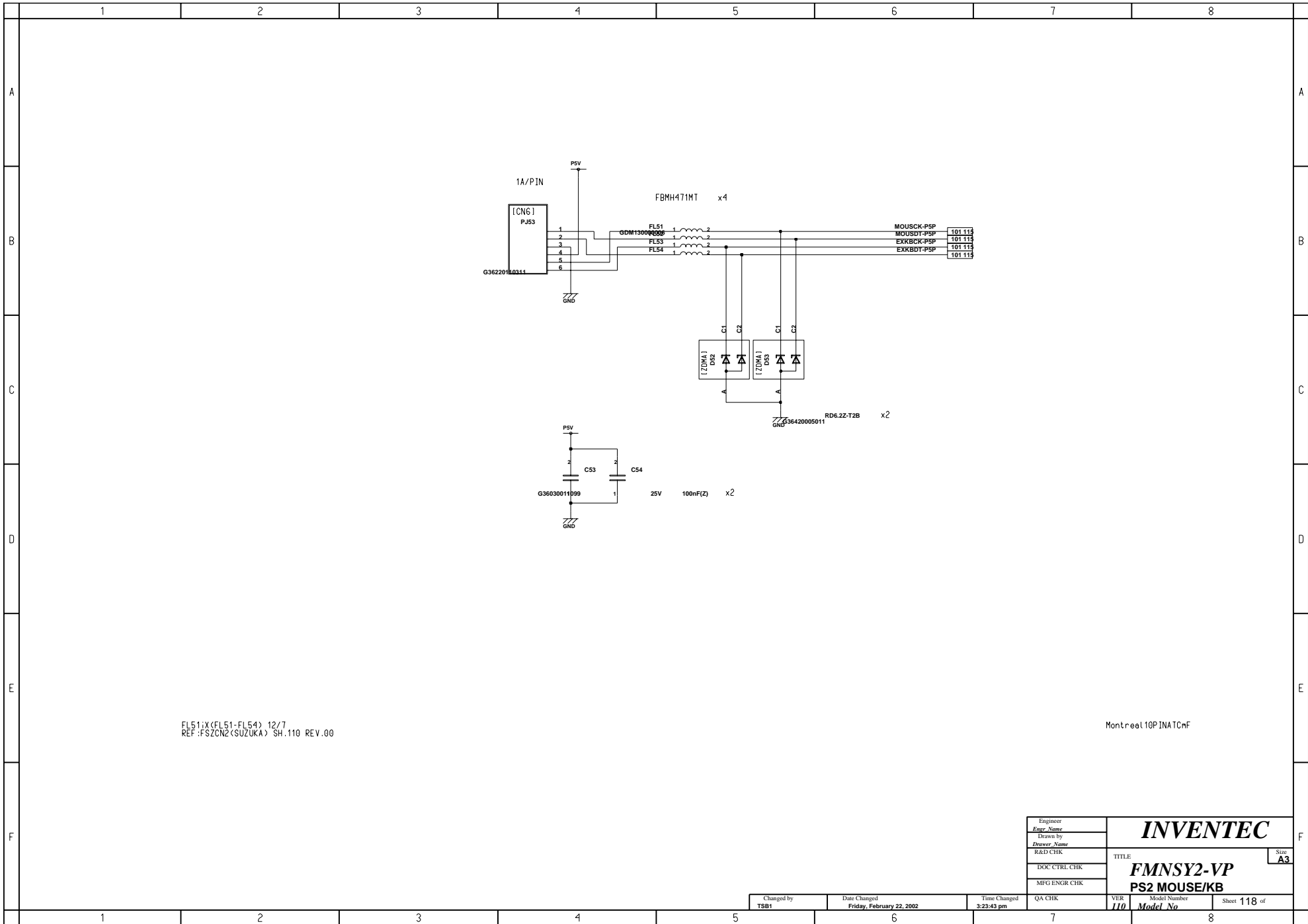


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DOC CTRL CHK			
MFG ENGR CHK			
Changed by	Date Changed	Time Changed	QA CHK
TSB1	Friday, February 22, 2002	3:23:35 pm	
VER	Model Number	Sheet	
110		116 of	



Q170: X 12/6  
 IC125 : X 11/22 Y  
 RM19 : CAEGSSA(TX 11/21  
 W8 : R387A(DVIDET-S3N DVSGPD-P3NMA  
 PWROK-S3PPWROK-S3PX 11/17  
 INTKEYARM31, RM56SAOAX  
 11/98  
 DILSON-S3P, PHYRST-E3N 9/28  
 W889/2  
 RING1(GATEFETX  
 C26 Not Mount Mount (VCC 1pinPpXR)  
 INTKEY-EJCTRO, PMBETV(YRWI  
 INTKEY-EJCTRO, PMBETV(AbvrWI  
 PONFEV(BV/PV)AbvrWI  
 DOCDT1/2VAbvrWI  
 DNVPON-S3P  
 DNVSON-S3P  
 DDVIDP-S3N  
 DPMRST-S3N  
 C24 5.6uF 4.7uFX  
 REF : SUZUKA(FSZ102 REV.00)

Engineer		<b>INVENTEC</b> <b>FMNSY2-VP</b> <b>EC/KBC 3</b>	Size <b>A3</b>
Eng. Name			
Drawn by			
Draw. Name			
R&D CHK			
TITLE			
DOC CTRL. CHK			
MFG ENGR. CHK			
Changed by	Date Changed	Time Changed	QA CHK
TSB1	Friday, February 22, 2002	3:23:39 pm	
VER	Model Number	Sheet	
110	Model No	117 of	

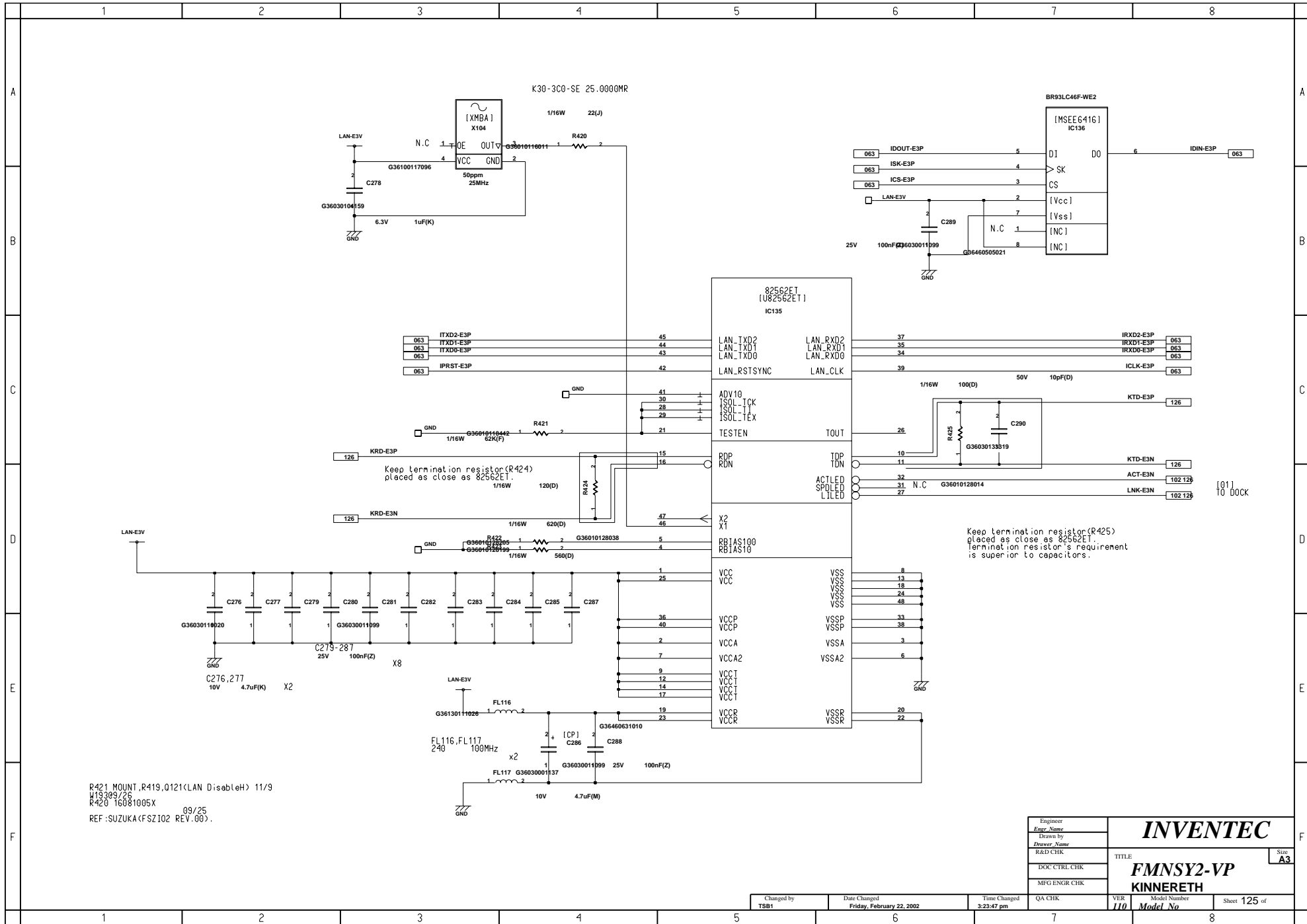


FL51:Y(FL51-FL54) 12/7  
 REF:FSZCN2(SUZUKA) SH.110 REV.00

Montreal 10P INATcNF

Engineer <i>Engr Name</i>	<b>INVENTEC</b>
Drawn by <i>Drawer Name</i>	
R&D CHK	
DOC CTRL CHK	
MFG ENGR CHK	
TITLE	Size <b>A3</b>
<b>FMNSY2-VP</b>	
<b>PS2 MOUSE/KB</b>	

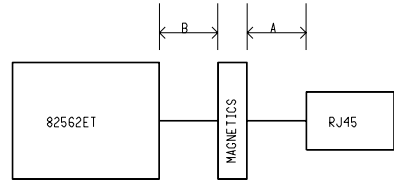
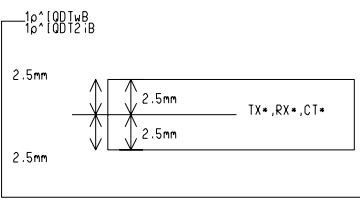
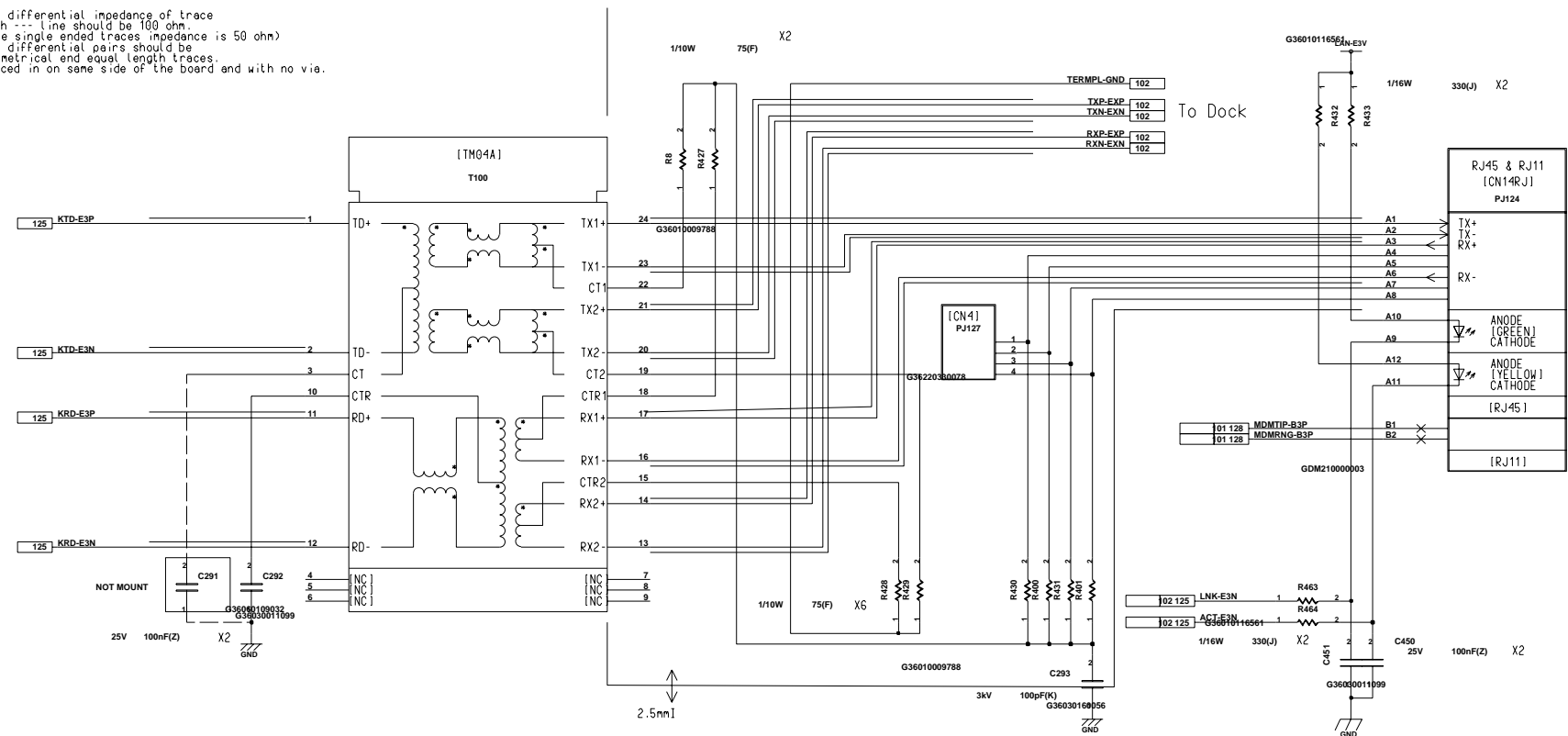
Changed by <b>TSB1</b>	Date Changed <b>Friday, February 22, 2002</b>	Time Changed <b>3:23:43 pm</b>	QA CHK	VER <b>110</b>	Model Number <i>Model No</i>	Sheet <b>118</b> of
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Engineer <i>Enger Name</i>	<b>INVENTEC</b> TITLE: <b>FMNSY2-VP</b> <b>KINNERETH</b>	Size <b>A3</b>
Drawn by <i>Drawer Name</i>		
R&D CHK		
DOC CTRL CHK		
MFG ENGR CHK		

Changed by <b>TSB1</b>	Date Changed <b>Friday, February 22, 2002</b>	Time Changed <b>3:23:47 pm</b>	QA CHK	VER <b>110</b>	Model Number <i>Model No</i>	Sheet <b>125</b> of
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The differential impedance of trace with --- line should be 100 ohm.  
 (the single ended traces impedance is 50 ohm)  
 The differential pairs should be symmetrical end equal length traces.  
 Placed in on same side of the board and with no via.

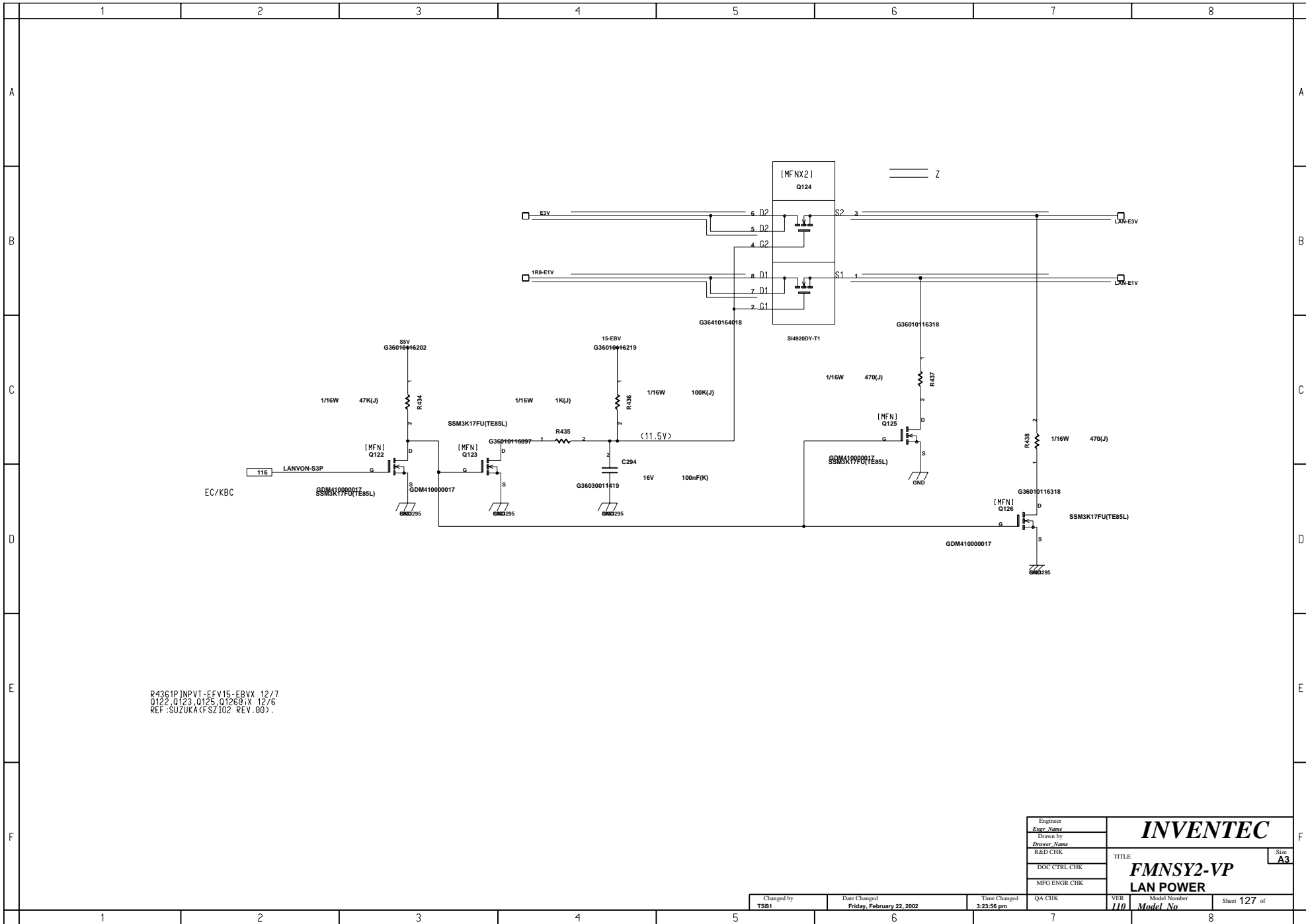


The distance labeled "A" should be given the highest priority in board layout. Distance A should be kept to less than one inch of separation.  
 Distance B should also be designed to extend less than one inch between device.

- R400, R401, R430, R431, C293 PJ124t (GND) uB
- R400, R401, R430, R431 NOT MOUNTX 12/10
- R400, R401, PJ127 12/7
- R463, R464, C450, C451, R432, R433 Xidq, 11/21
- PJ124T 100X
- PJ124 GDM210000003X 10/18
- REF: FSZ102(SUZUKA) SH.126 REV.00

Engineer Engr Name	<b>INVENTEC</b> TITLE: <b>FMNSY2-VP LAN/MODEM I/F CONN</b> Size <b>A3</b>
Drawn by Drawer Name	
R&D CHK	
DOC CTRL CHK	
MFG ENGR CHK	

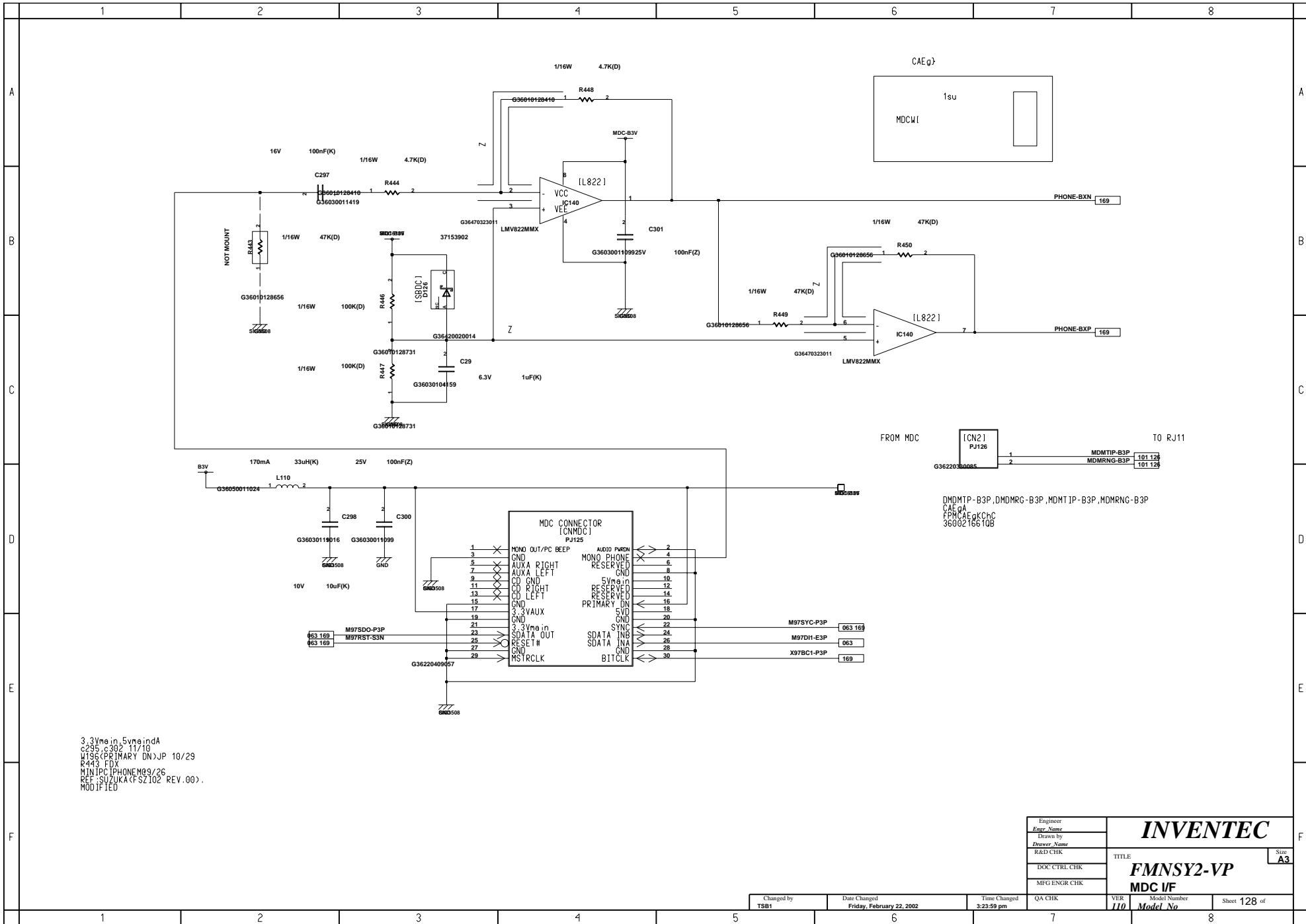
Changed by TSB1	Date Changed Friday, February 22, 2002	Time Changed 3:23:52 pm	QA CHK	VER 110	Model Number Model No	Sheet 126 of
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Engineer <i>Engr Name</i>	<b>INVENTEC</b>
Drawn by <i>Drawn Name</i>	
R&D CHK	
DOC CTRL CHK	
MFG ENGR CHK	
TITLE	<b>FMNSY2-VP LAN POWER</b>
Model Number	Size <b>A3</b>

Changed by <b>TSB1</b>	Date Changed <b>Friday, February 22, 2002</b>	Time Changed <b>3:23:56 pm</b>	QA CHK	VER <b>110</b>	Model Number <i>Model No</i>	Sheet <b>127</b> of
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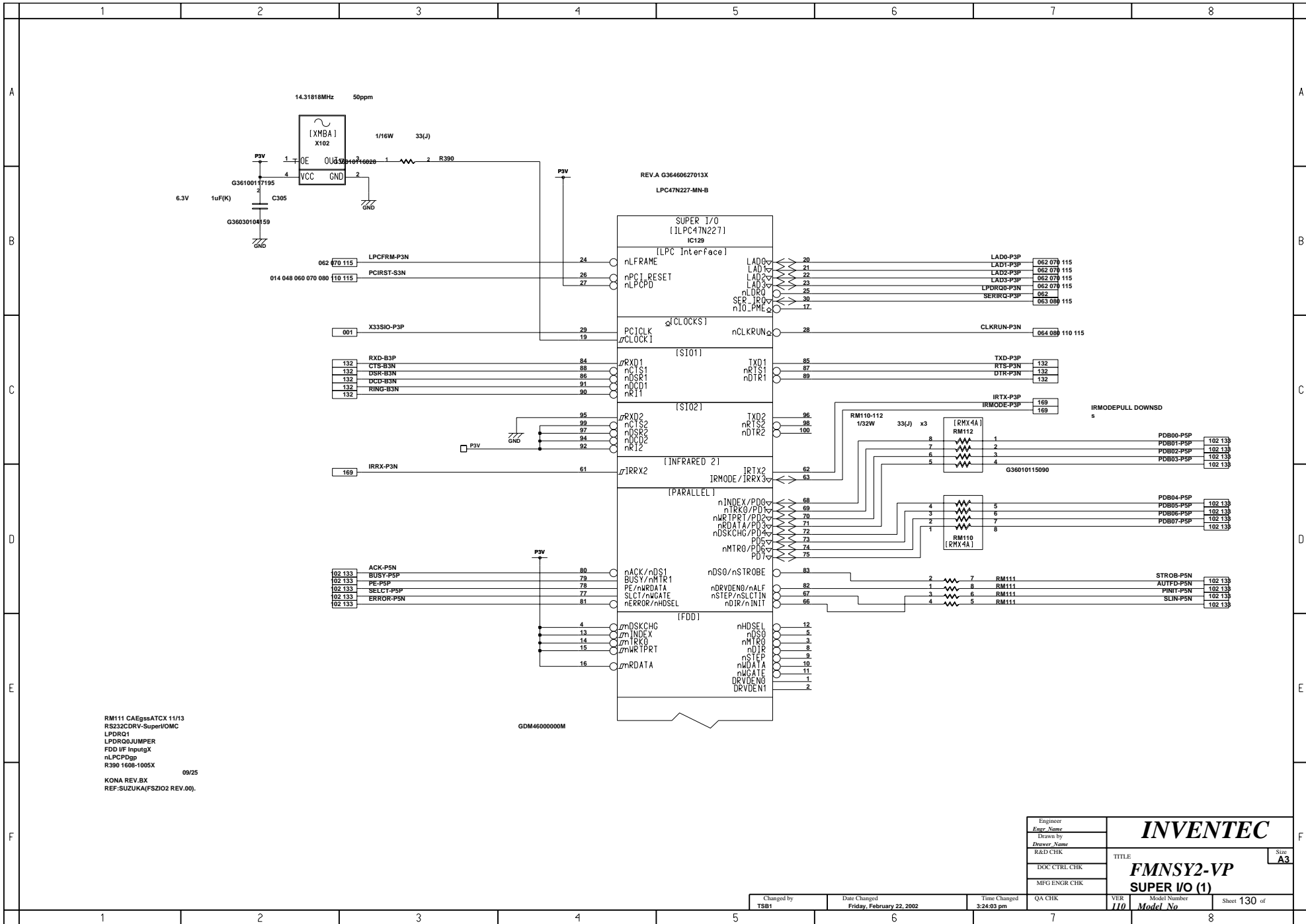


FROM MDC [CN2] P126 TO RJ11  
 G3622030085 MDMTIP-B3P 101 128  
 MDMRNG-B3P 101 128

DMDTIP-B3P, DMDMRG-B3P, MDMTIP-B3P, MDMRNG-B3P  
 CAEg  
 FPM:AEgKChC  
 3600216610B

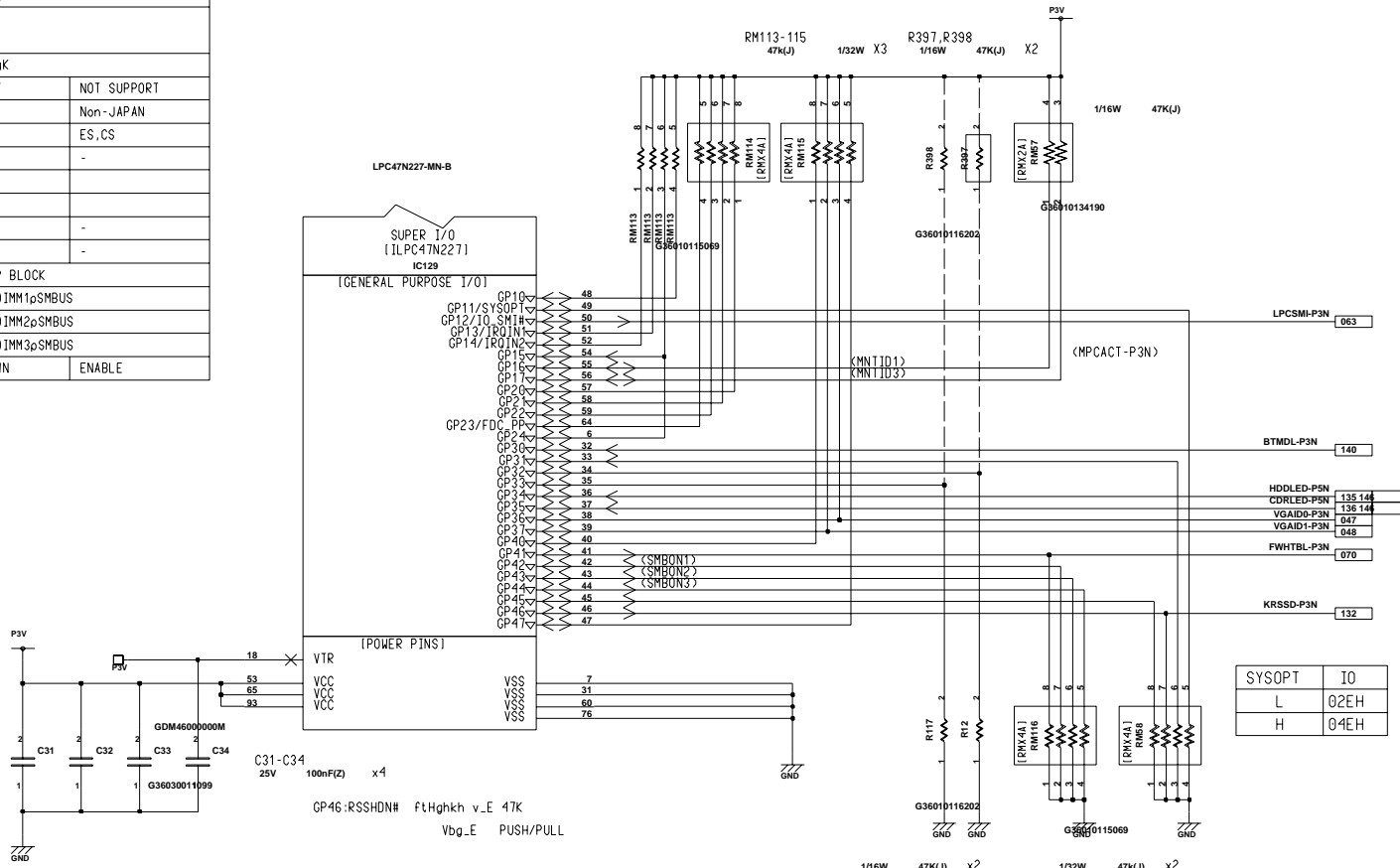
3.3Vmain 5Vmain  
 c295 c302 11/10  
 v336(PRIMARY DN)JP 10/29  
 R443 FDX  
 MINIPCIPHONEM09/26  
 REF:SUZUKA(FS2102 REV.00).  
 MODIFIED

Engineer Eng Name	<b>INVENTEC</b> TITLE: <b>FMNSY2-VP</b> <b>MDC I/F</b> Model No					
Drawn by Drawer Name						
R&D CHK				Sheet <b>A3</b> of		
DOC CTRL CHK						
MFG ENGR CHK						
Changed by TSB1	Date Changed Friday, February 22, 2002	Time Changed 3:23:59 pm	QA CHK	VER 110	Model Number Model No	Sheet <b>128</b> of



<GPIO PIN ASSIGN>

FUNCTION	IO	L	H
GP11 <SYSOPT>	I	02EH	04EH
GP12 <LPCSMI>	(O)		
GP15 <MPCACT>	I	MPCACTgK	
GP16 <DDCSDA>	(IO)	DDCMp	
GP17 <DDCSCL>	(IO)		
GP24 <MPCACT>	I	MPCACTgK	
GP30 <BLUE_TOOTH>	I	SUPPORT	NOT_SUPPORT
GP31 <JMODEL>	I	JAPAN	Non-JAPAN
GP32 <SYS_LEVEL>	I	VP	ES_CS
GP33 <reserved>	I	-	-
GP34 <HDDLED>	I		
GP35 <CDROM_LED>	I		
GP36 <VGAID0>	I	-	-
GP37 <VGAID1>	I	-	-
GP41 <FWHTBL>	O	FWH_TOP_BLOCK	
GP42 <SMBON1>	(O)	SDRAM_DIMM1pSMBUS	
GP43 <SMBON2>	(O)	SDRAM_DIMM2pSMBUS	
GP44 <SMBON3>	(O)	SDRAM_DIMM3pSMBUS	
GP46 <KRSSDN>	O	SHUTDOWN	ENABLE



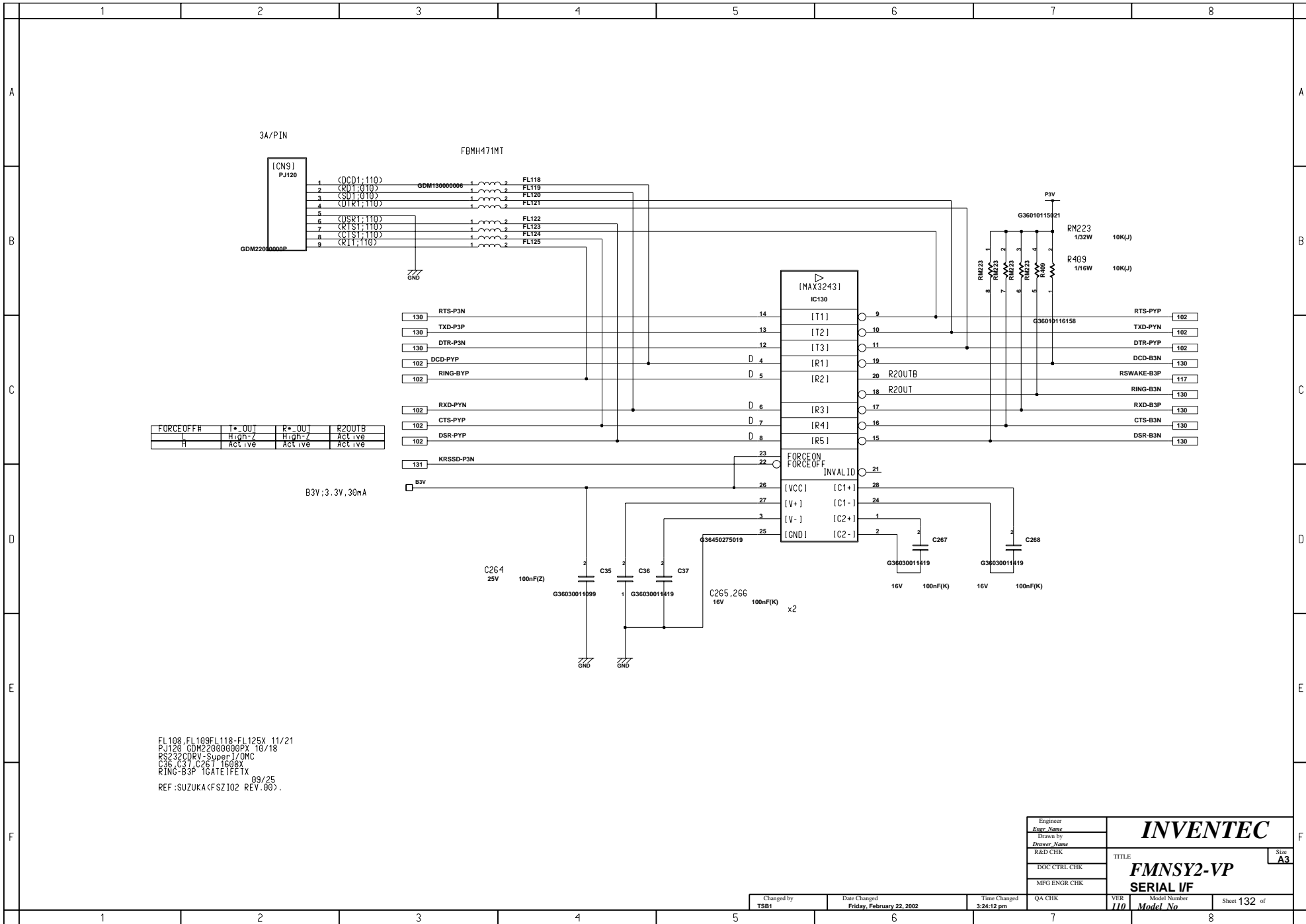
RM113, RM114, RM115, RM58 CAEG  
 ssATCX 11/13  
 LPCSMI-P3P1PCSMIP3NXA  
 V100, C30 (KONACTB\*)  
 11/10  
 GP10: 2801/9/26  
 GP1032: System Level  
 SYSOPT = 'L' (02EH)  
 GP32: W102Pxx  
 LPCSMI JUMPER 09/25  
 NVPCNF - S3PM0  
 C30 x (10V5, 3V) NOT MOUNTX  
 MNTID - SN+QNW  
 KONA REV. A REV. BX  
 REF : SUZUKA (FSZ102 REV.00).

MPCACT-P3NM 12/7  
 RM115 CAEG ssATCX 11/21  
 VGAID0-P3N 11/11  
 R3, R401, R402, R404, R4ARX 10/29  
 R399, R400, R4ARX

SYSOPT	IO
L	02EH
H	04EH

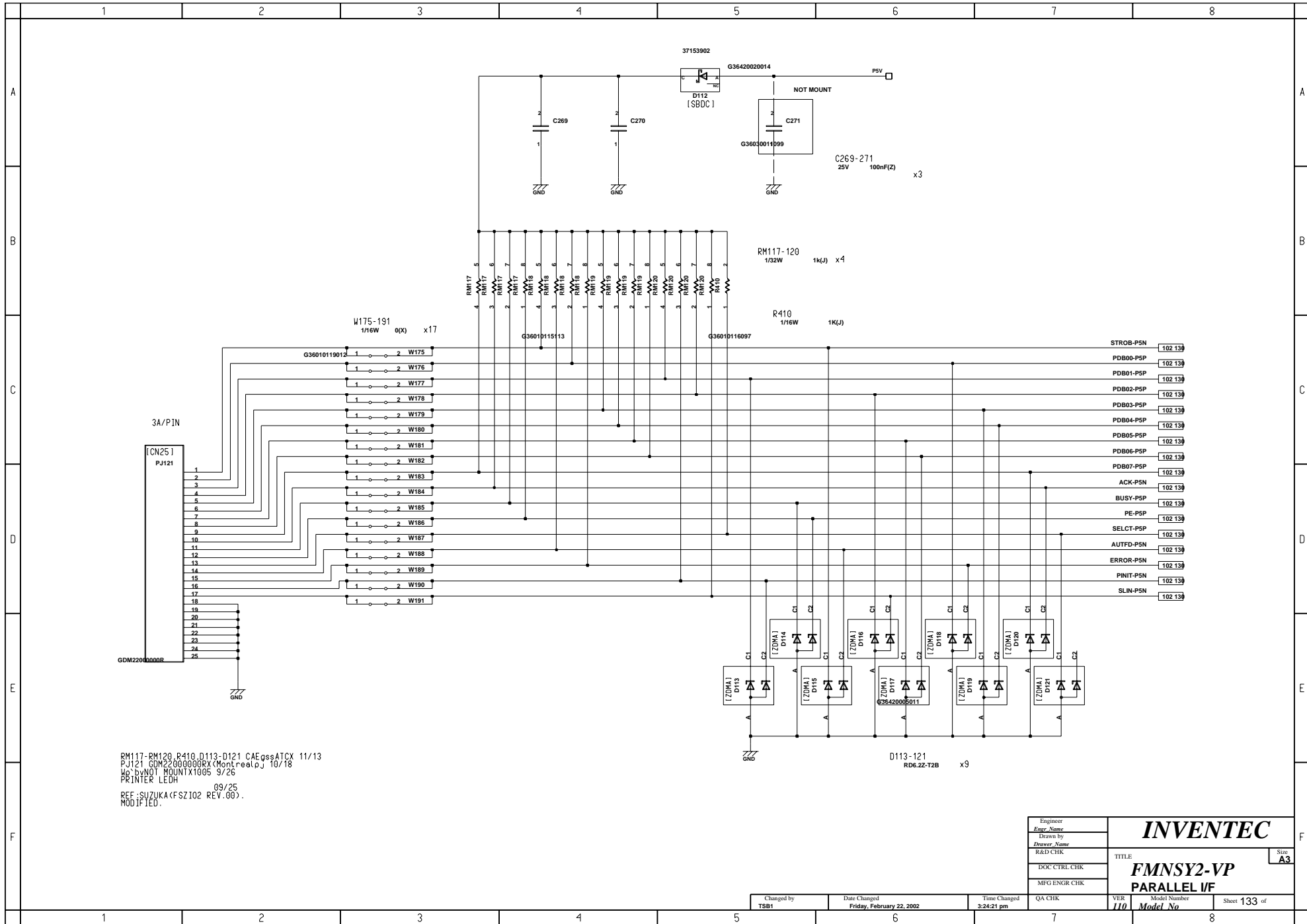
Engineer Engr Name	<b>INVENTEC</b> TITLE: <b>FMNSY2-VP          SUPER I/O (2)</b>
Drawn by Drawer Name	
R&D CHK	
DOC CTRL CHK	
MFG ENGR CHK	
Model Number Model No	Sheet 131 of

Changed by TSB1	Date Changed Friday, February 22, 2002	Time Changed 3:24:08 pm	QA CHK	VER 110
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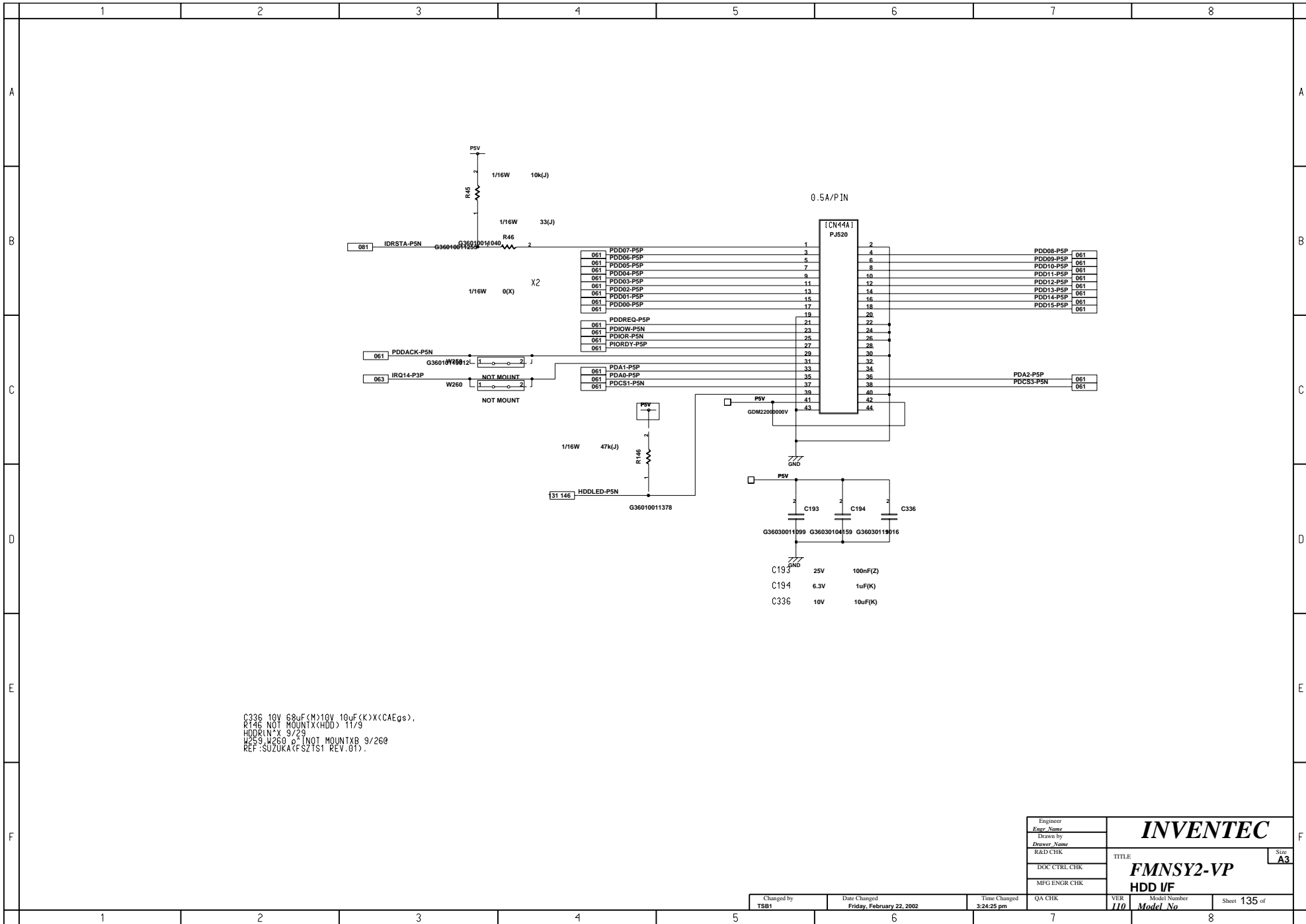
Engineer <i>Engr. Name</i>	<b>INVENTEC</b> TITLE: <b>FMNSY2-VP SERIAL I/F</b> Size: <b>A3</b>
Drawn by <i>Drawer Name</i>	
R&D CHK	
DOC CTRL CHK	
MFG ENGR CHK	

Changed by <b>TSB1</b>	Date Changed <b>Friday, February 22, 2002</b>	Time Changed <b>3:24:12 pm</b>	QA CHK	VER <b>110</b>	Model Number <b>Model No</b>	Sheet <b>132</b> of
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RM117-RM120, R410, D113-D121 CAEgssATCX 11/13  
 PJ121 GDM22000000RX (Montreal) 10/18  
 Ho by NOT MOUNT X1005 9/26  
 PRINTER LEAD  
 09/25  
 REF: SUZUKA (FSZ102 REV.00).  
 MODIFIED.

Engineer		<b>INVENTEC</b>	Size <b>A3</b>
Engr Name			
Drawn by			
Drawer Name			
R&D CHK			
TITLE		<b>FMNSY2-VP PARALLEL I/F</b>	
DOC CTRL CHK			
MFG ENGR CHK			
Changed by TSB1	Date Changed Friday, February 22, 2002	Time Changed 3:24:21 pm	QA CHK
VER 110	Model Number Model No	Sheet 133 of	



C336 10V 68uF(M)10V 10uF(K)(CAEgs),  
 R146 NOT MOUNT(XHDD) 11/S  
 HDDR(N)X 9/29  
 W259, W260 0 (NOT MOUNT)X B 9/260  
 REF: SUZUKA(FSZTST1 REV.01)

Engineer Engy Name	<b>INVENTEC</b> TITLE: <b>FMNSY2-VP</b> <b>HDD I/F</b>	Size A3
Drawn by Drawer Name		
R&D CHK		
DOC CTRL CHK		
MFG ENGR CHK		

Changed by TSB1	Date Changed Friday, February 22, 2002	Time Changed 3:24:25 pm	QA CHK	VER 110	Model Number Model No	Sheet 135 of
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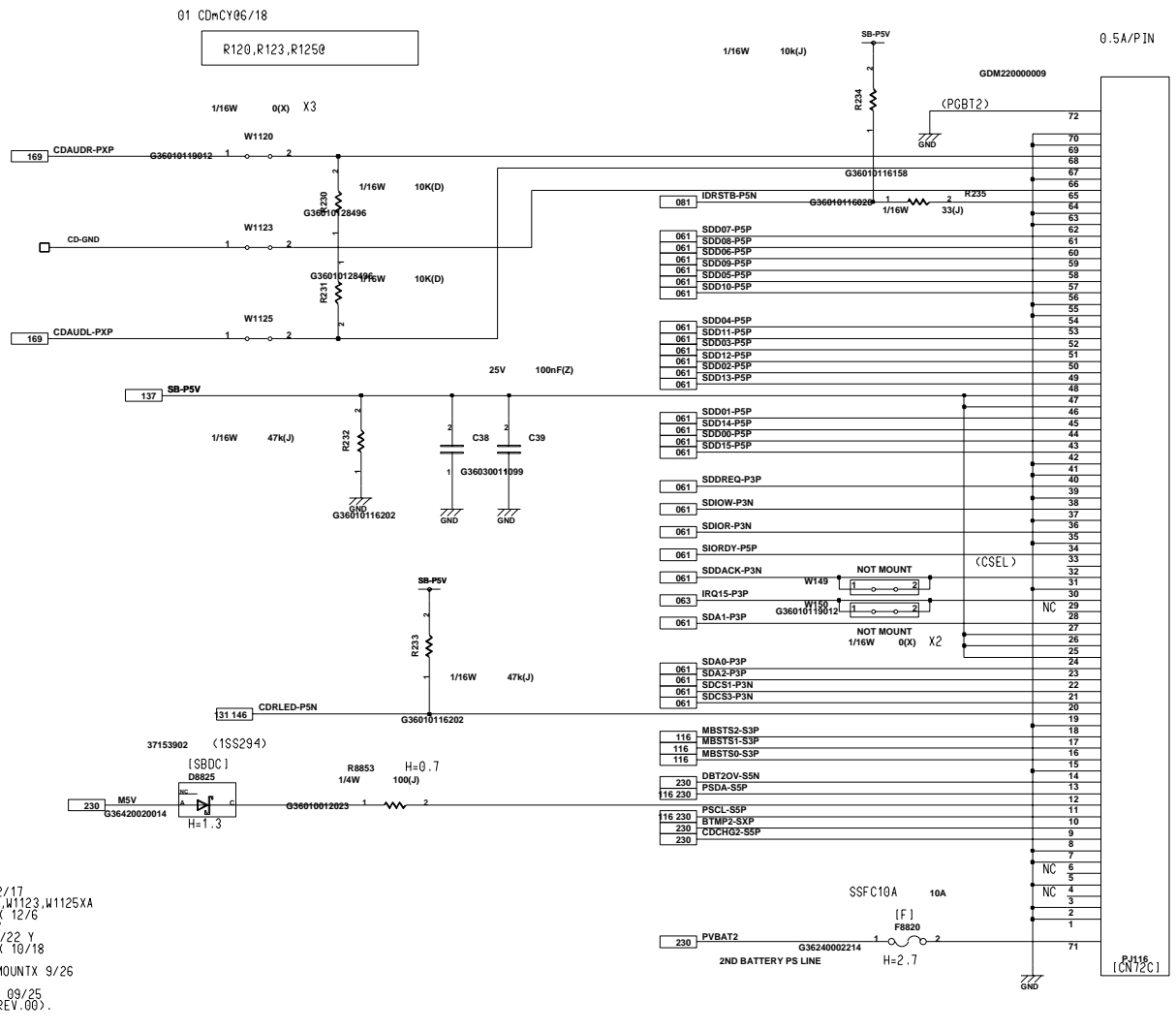
B

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D

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F



R230, R231 MOUNTX 12/17  
R120, R123, R125W1120, W1123, W1125XA  
R230, R231NOT MOUNTX 12/6  
R230, R231 FDX 11/22  
R120, R123, R125 FDX 11/22 Y  
PJ116 GDM22000009X 10/18  
F8820PS 9/28  
W149, W150 6" 1ANOT MOUNTX 9/26  
R230, R231 F, X  
09/25  
REF : SUZUKA (FSZ102 REV.00).

01 CD=CY06/18  
R120, R123, R125@  
1/16W 0(x) X3  
W1120  
CDAUDR-PXP  
G36010119012 1 2  
W1123  
CD-GND  
W1125  
CDAUDL-PXP  
G360101128496W 10K(D)  
G360101116158  
G36010116022 1 2  
R235 33(J)  
1/16W 10K(D)  
R231 1/16W 47K(J)  
R232  
G360101116202  
C38  
C39  
G36030011099  
SB-P5V  
25V 100nF(Z)  
R233  
SB-P5V  
R234  
G360101116202  
W149  
NOT MOUNT  
W150  
G360101116012 1 2  
NOT MOUNT 0(X) X2  
SDA0-P3P  
SDA2-P3P  
SDCS1-P3N  
SDCS3-P3N  
CORLED-P5N  
G36010116202  
37153902 (1S5294)  
[SBDC] D8825  
MSV G36420020014  
R8853 1/4W H=0.7  
H=1.3  
SSFC10A 10A  
[F] F8820  
H=2.7  
R116 [CN12]  
0.5A/PIN  
72  
70  
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GND

- 061 SDD07-P5P
- 061 SDD08-P5P
- 061 SDD06-P5P
- 061 SDD09-P5P
- 061 SDD05-P5P
- 061 SDD10-P5P
- 061 SDD04-P5P
- 061 SDD11-P5P
- 061 SDD03-P5P
- 061 SDD12-P5P
- 061 SDD02-P5P
- 061 SDD13-P5P
- 061 SDD01-P5P
- 061 SDD14-P5P
- 061 SDD00-P5P
- 061 SDD15-P5P
- 061 SDDREQ-P3P
- 061 SDIOW-P3N
- 061 SDIOR-P3N
- 061 SIORDY-P5P
- 061 SDDACK-P3N
- 061 IRQ15-P3P
- 061 SDA1-P3P
- 061 SDA0-P3P
- 061 SDA2-P3P
- 061 SDCS1-P3N
- 061 SDCS3-P3N
- 116 MBSTS2-S3P
- 116 MBSTS1-S3P
- 116 MBSTS0-S3P
- 230 DBT20V-SSN
- 116 230 PSDA-SSP
- 116 230 PSCL-SSP
- 230 BTMP2-SXP
- 230 CDCRG2-SSP
- NC 6
- NC 5
- NC 4
- 2 3
- 1 2
- 71

Engineer <i>Engr Name</i> Drawn by <i>Drawer Name</i> R&D CHK DOC CTRL CHK MFG ENGR CHK	<b style="font-size: 1.2em;">INVENTEC</b>  <b style="font-size: 1.1em;">FMNSY2-VP</b> <b style="font-size: 1.1em;">SELECT BAY I/F</b>
Changed by TSB1 Date Changed Friday, February 22, 2002 Time Changed 3:24:29 pm	TITLE: <span style="border: 1px solid black; padding: 2px;">A3</span> Model No Sheet 136 of

1

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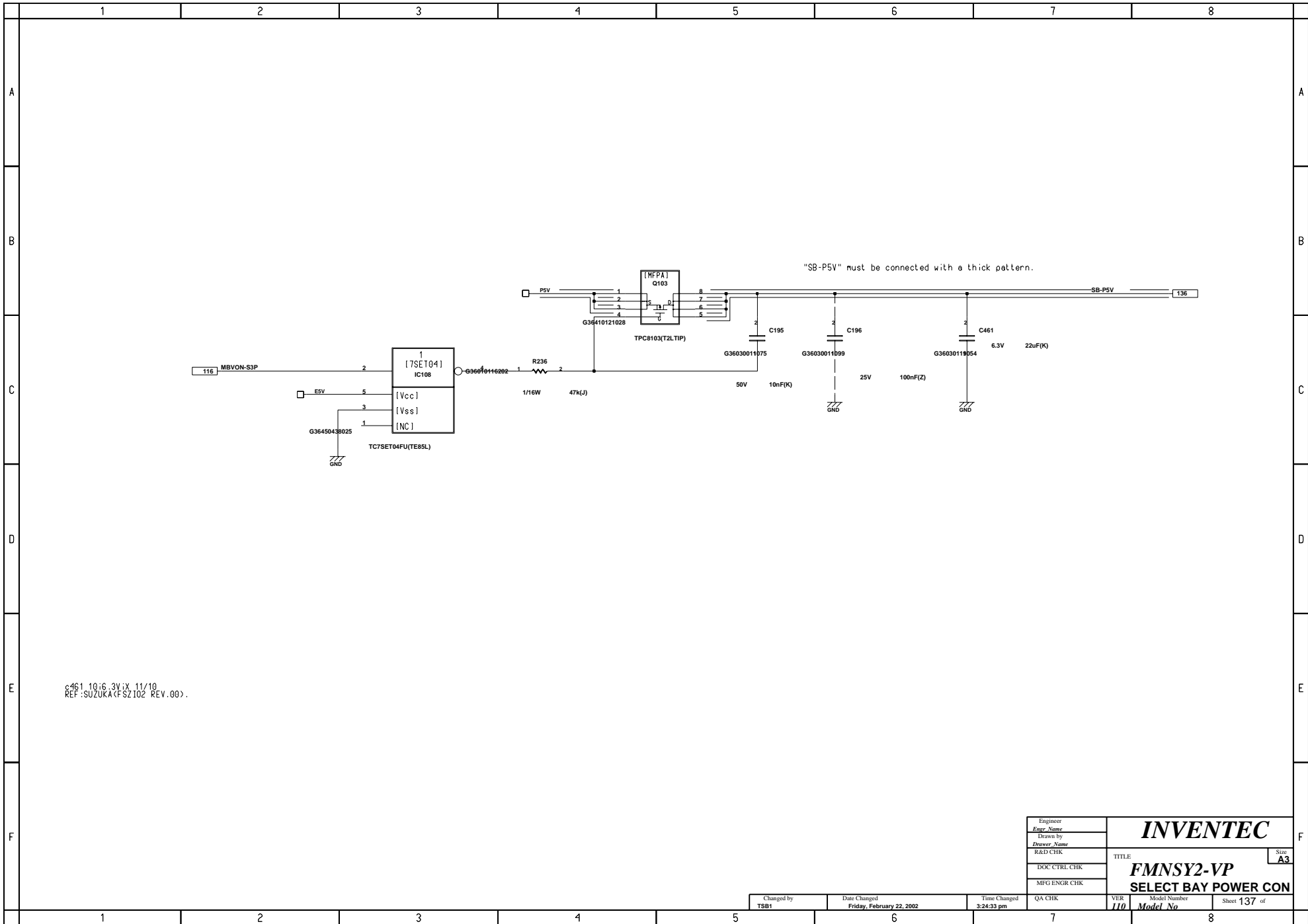
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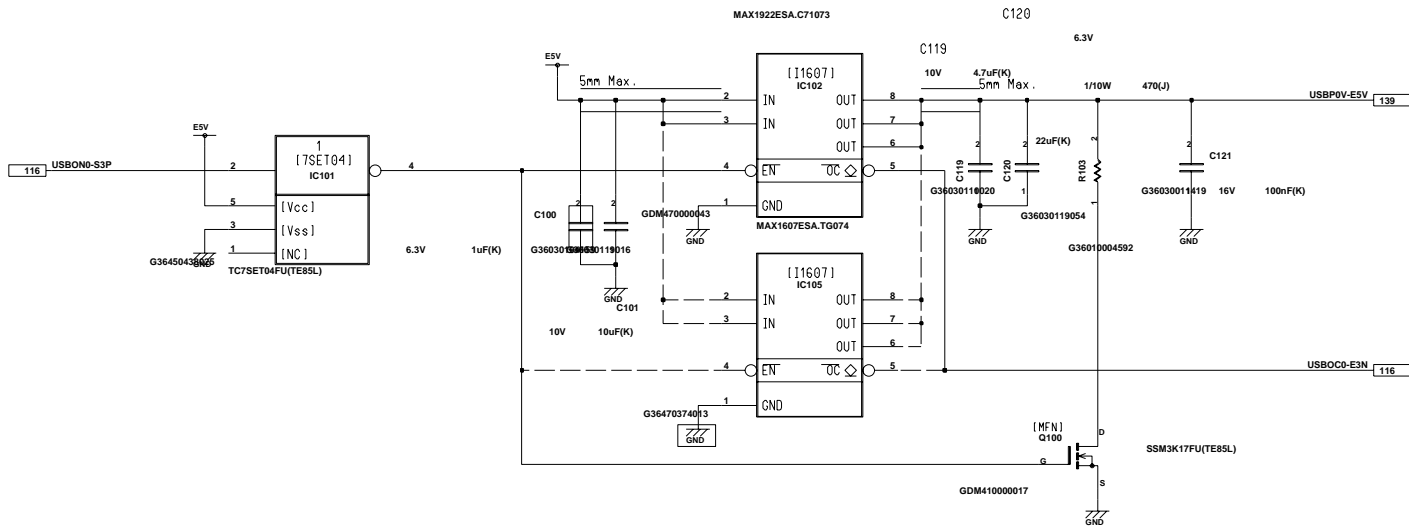


c461 10i6.3V.iX.11/10  
REF.:SUZUKA(FSZ102 REV.00).

Engineer <i>Engr Name</i>	<b>INVENTEC</b>
Drawn by <i>Drawer Name</i>	
R&D CHK	
DOC CTRL CHK	
MFG ENGR CHK	
TITLE	Size <b>A3</b>
<b>FMNSY2-VP</b>	
<b>SELECT BAY POWER CON</b>	

Changed by <b>TSB1</b>	Date Changed <b>Friday, February 22, 2002</b>	Time Changed <b>3:24:33 pm</b>	QA CHK	VER <b>110</b>	Model Number <i>Model No</i>	Sheet <b>137</b> of
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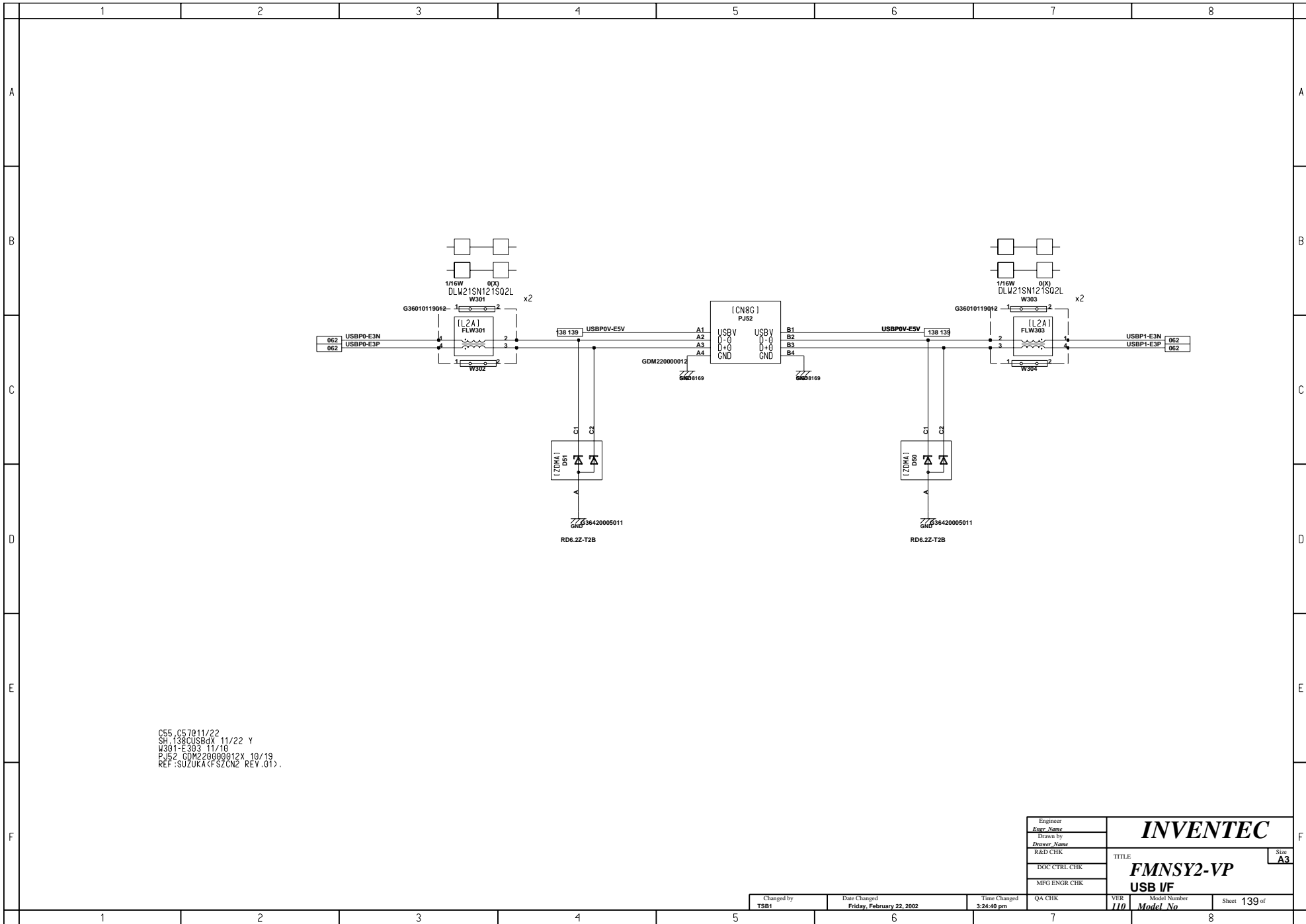




MON7A100HC:PIV8 ; 11/22Y  
 C199,C200,C203,C20410V6.3VX  
 11/10  
 REF:SUZUKA<FSZ102 REV.00>.

Engineer <i>Engr Name</i>	<b>INVENTEC</b>
Drawn by <i>Drawer Name</i>	
R&D CHK	
DOC CTRL CHK	
MFG ENGR CHK	
TITLE	Size <b>A3</b>
<b>FMNSY2-VP</b>	
<b>USB POWER</b>	

Changed by <b>TSB1</b>	Date Changed <b>Friday, February 22, 2002</b>	Time Changed <b>3:24:36 pm</b>	QA CHK	VER <b>110</b>	Model Number <i>Model No</i>	Sheet <b>138</b> of
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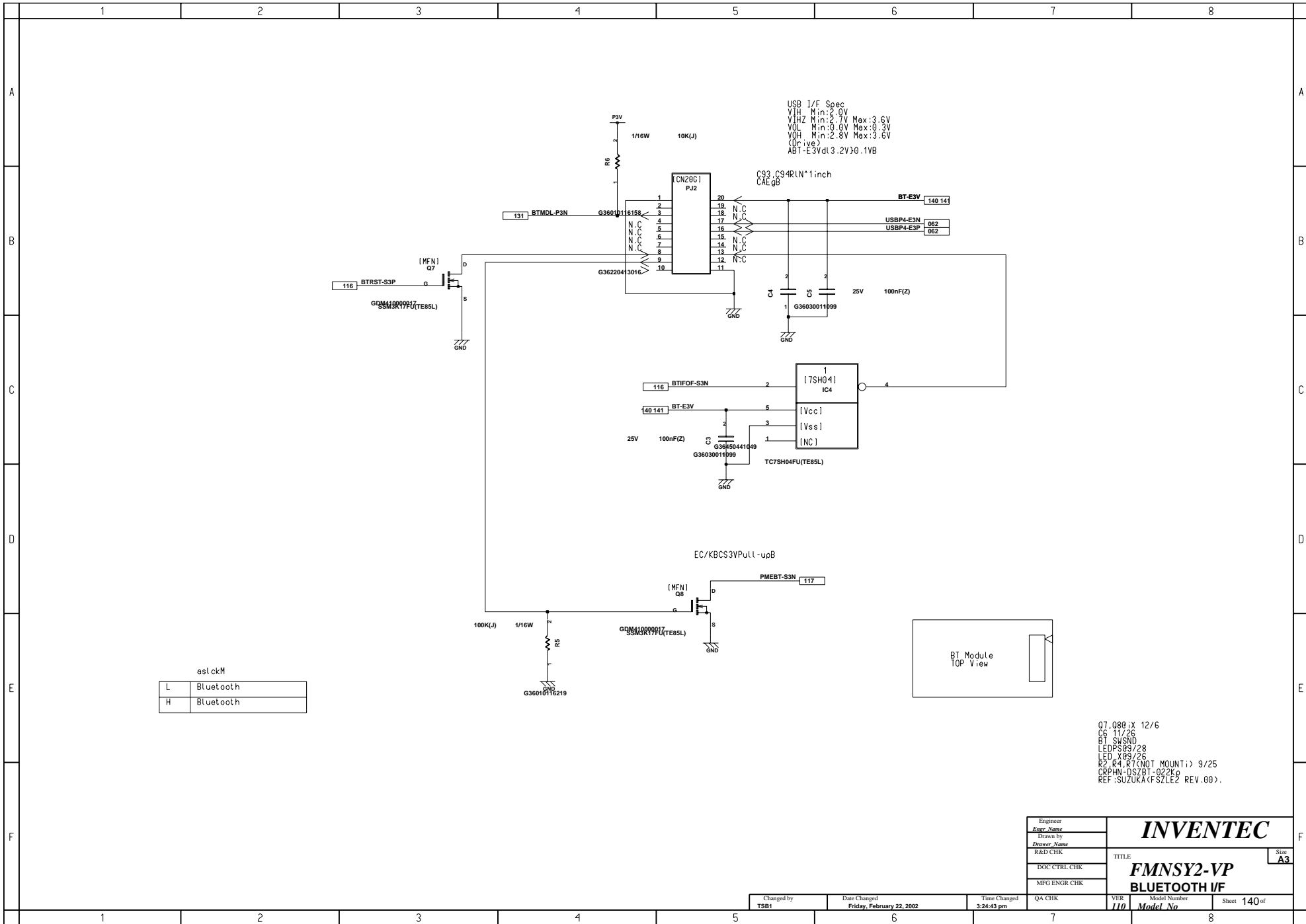


C55\_C57011/22  
 SH\_138CUSBdX\_11/22\_Y  
 W301-E303\_11/10  
 PJ52\_50M220000012X\_10/19  
 REF:SUZUKA(FSZCN2\_REV.01).

Engineer <i>Engr Name</i>	<b>INVENTEC</b>
Drawn by <i>Drawer Name</i>	
R&D CHK	
DOC CTRL CHK	
MFG ENGR CHK	
TITLE	FMNSY2-VP USB V/F

Changed by TSB1	Date Changed Friday, February 22, 2002	Time Changed 3:24:40 pm	QA CHK	VER 110	Model Number Model No	Sheet 139 of
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Size  
A3



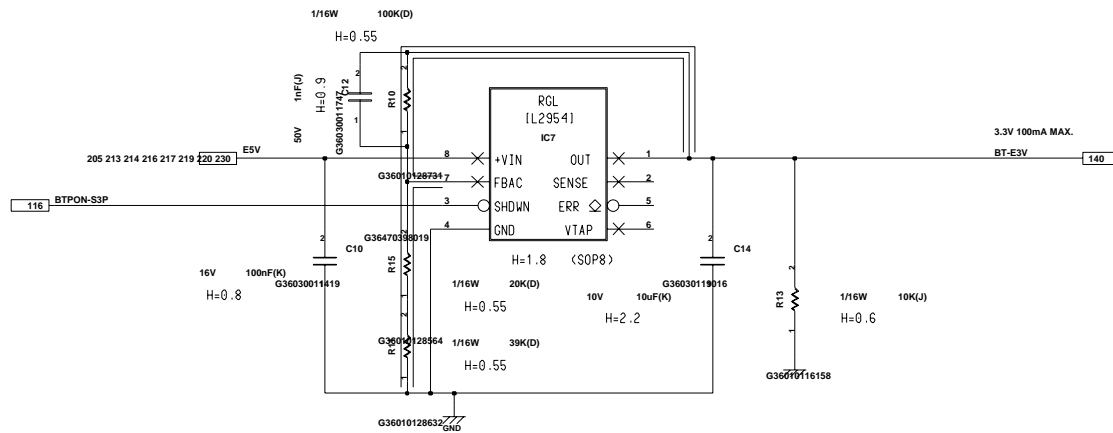
esl ckM	
L	Bluetooth
H	Bluetooth

07.080:IX 12/6  
 C6 11/26  
 BT SHSND  
 LEDPS83/28  
 LED YG3/26  
 R2,R4,R1(NGOT MOUNT): 9/25  
 CRPHN-DSZBT-0228  
 REF:SUZUKA(FSZLE2 REV.00)

Engineer	
Engr Name	
Drawn by	
Drawer Name	
R&D CHK	
DOC CTRL CHK	
MFG ENGR CHK	
TITLE	Size
<b>FMNSY2-VP</b>	<b>A3</b>
<b>BLUETOOTH I/F</b>	

Changed by	Date Changed	Time Changed	QA CHK	VER	Model Number	Sheet	140 of
TSB1	Friday, February 22, 2002	3:24:43 pm		110	Model No		

BTPONhCuAECg  
OPEN-DrainPinB

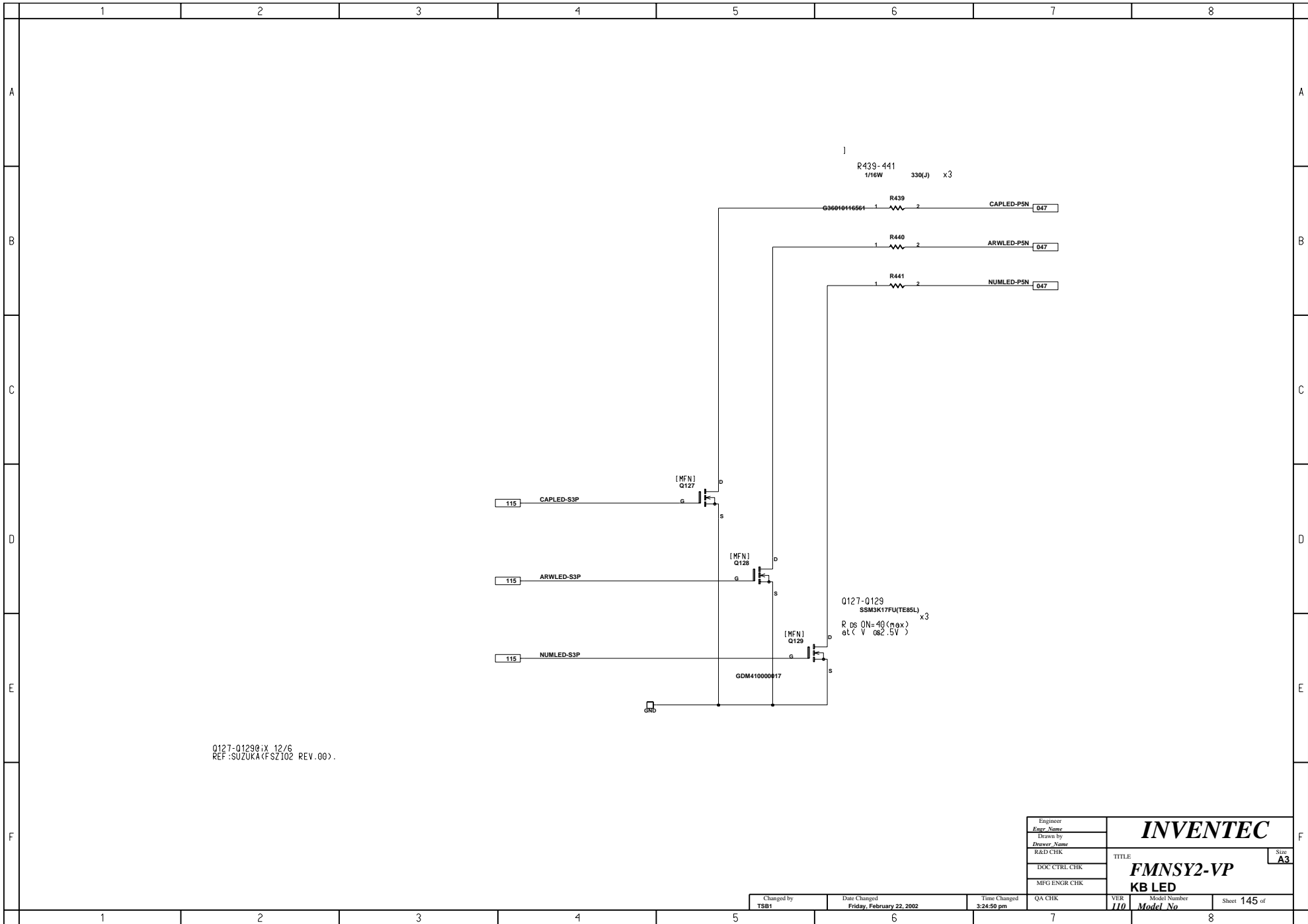


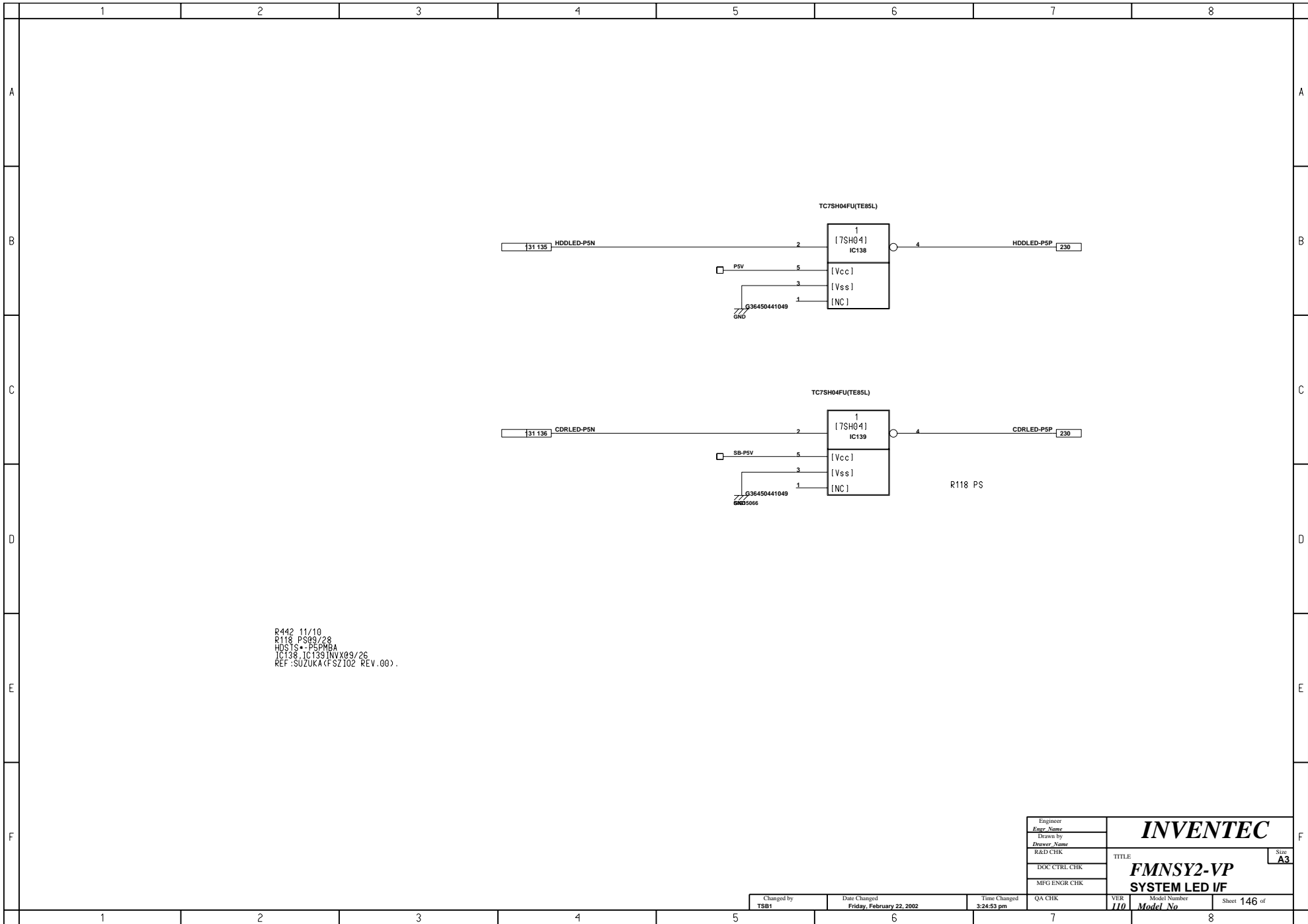
WI	R9, R11	R14, R15	od
ALPS S.W	1/16W 62K(D)	1/16W 0	3.2V
zUd	1/16W 39K(D)	1/16W 20K(D)	3.3V

08 SH 190 11/26  
PHN: DSZ81-021K0 9/25  
REF: SUZUKA(FSZLE2 REV.00).

Engineer Engr Name	<b>INVENTEC</b>  <b>FMNSY2-VP</b> <b>BLUETOOTH PS</b>	Size <b>A3</b>
Drawn by Drawer Name		
R&D CHK		
DOC CTRL CHK		
MFG ENGR CHK		

Changed by TSB1	Date Changed Friday, February 22, 2002	Time Changed 3:24:47 pm	QA CHK	VER 110	Model Number Model No	Sheet 141 of
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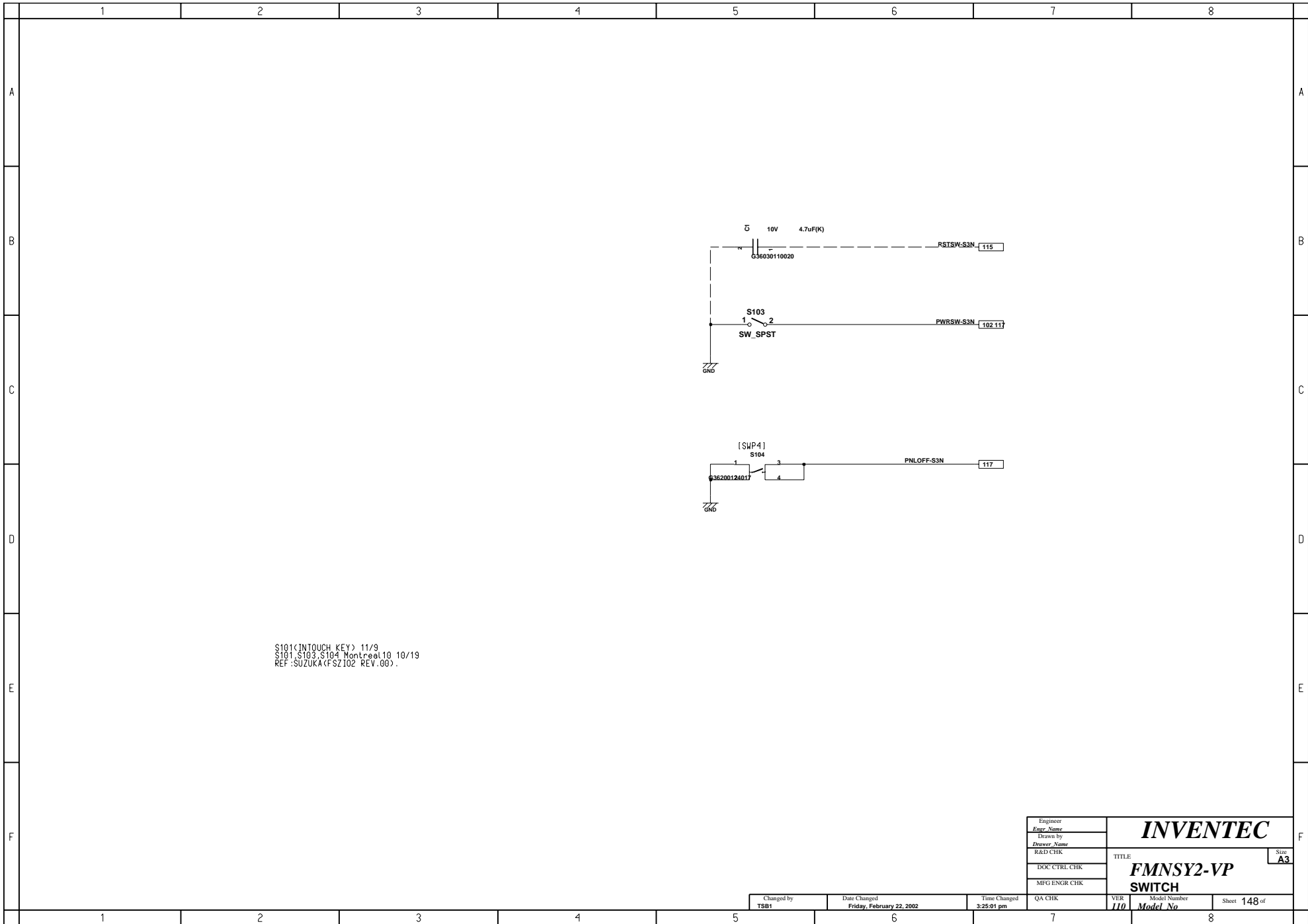




R442 11/10  
 R118 PS09/28  
 HDSTS\*-P5PMB  
 IC138, IC139 INX09/26  
 REF:SUZUKA(FSZ102 REV.00).

Engineer	
Engr Name	
Drawn by	
Drawer Name	
R&D CHK	
DOC CTRL CHK	
MFG ENGR CHK	
TITLE	Size
<b>FMNSY2-VP</b>	<b>A3</b>
<b>SYSTEM LED I/F</b>	

Changed by	Date Changed	Time Changed	QA CHK	VER	Model Number	Sheet
TSB1	Friday, February 22, 2002	3:24:53 pm		110	Model No	146 of

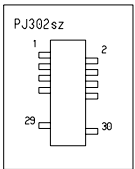
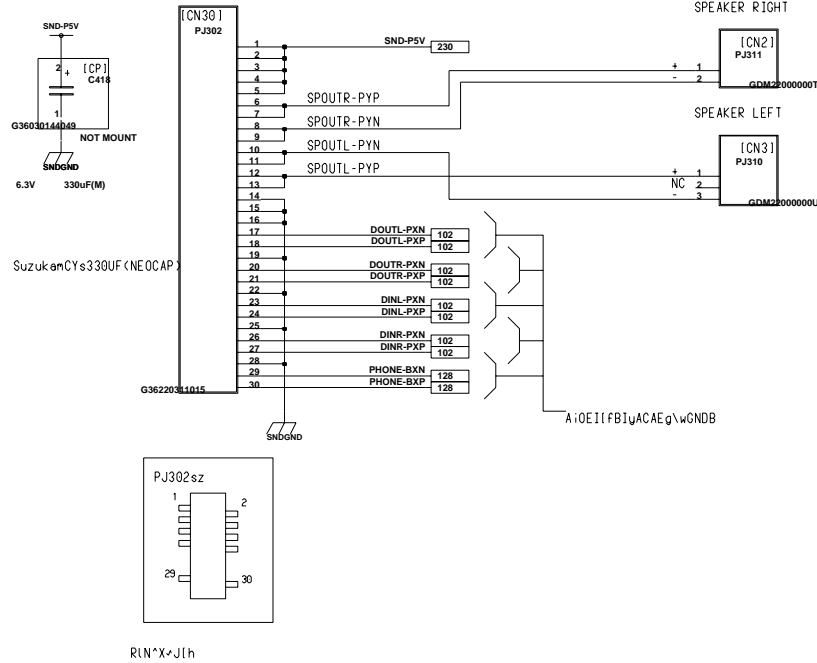


S101<INTOUCH KEY> 11/9  
 S101, S103, S104 Montreal 10 10/19  
 REF:SUZUKA<FSZ102 REV.00>.

Engineer	<b>INVENTEC</b> TITLE: <b>FMNSY2-VP SWITCH</b> Size <b>A3</b>
Engr Name	
Drawn by	
Drawer Name	
R&D CHK	
DOC CTRL CHK	
MFG ENGR CHK	

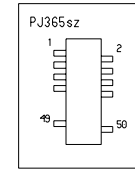
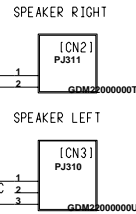
Changed by	Date Changed	Time Changed	QA CHK	VER	Model Number	Sheet
TSB1	Friday, February 22, 2002	3:25:01 pm		110	Model No	148 of

Montreal-XRJ[hsATC  
 0.5A/pin



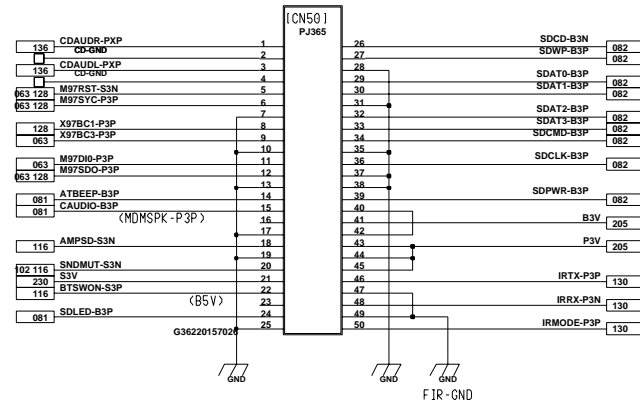
RIN\*X\*Jlh

PJ365 23PINNXCAC730(B5VgXRj 11/23  
 PJ365 24PINA-GNDCC-GNDX 11/23  
 MDHSPK-P3P 11/17  
 REF:FNOSYQ(Montreal10 VP), SH94, REV00



RIN\*X\*Jlh

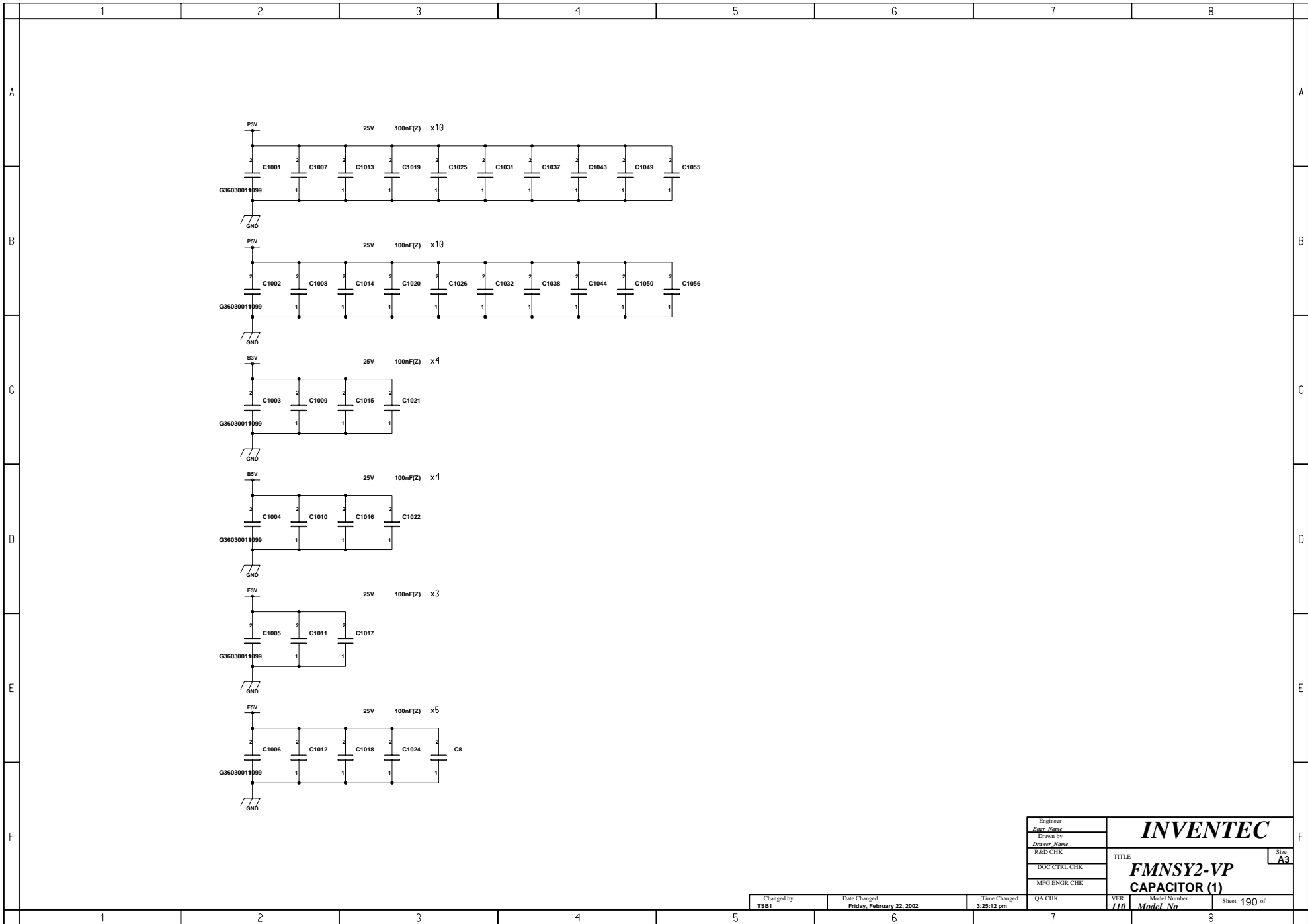
Montreal-XRJ[hsATC  
 0.4A/pin



Engineer	Engy_Name	<b>INVENTEC</b>
Drawn by	Drawer_Name	
R&D CHK	DOC CTRL CHK	
MFG ENGR CHK		
TITLE:	<b>FMNSY2-VP</b>	
Model Number	<b>SOUND (FMNSD*) I/F</b>	
Model No	Sheet	Size
110	169 of	A3

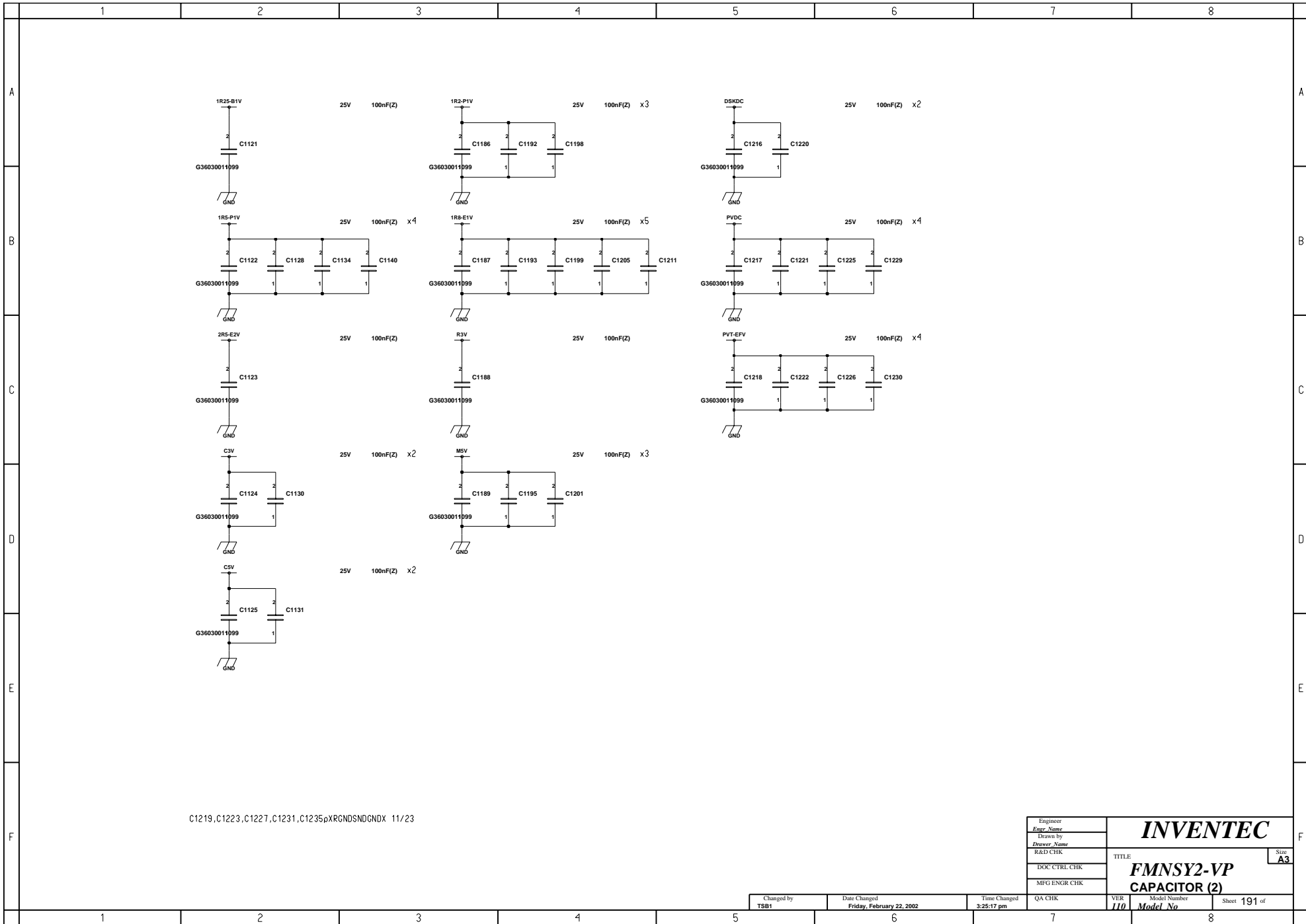
Changed by	Date Changed	Time Changed	QA CHK	VER	Model Number	Sheet
TSB1	Friday, February 22, 2002	3:25:06 pm		110	Model No	169 of





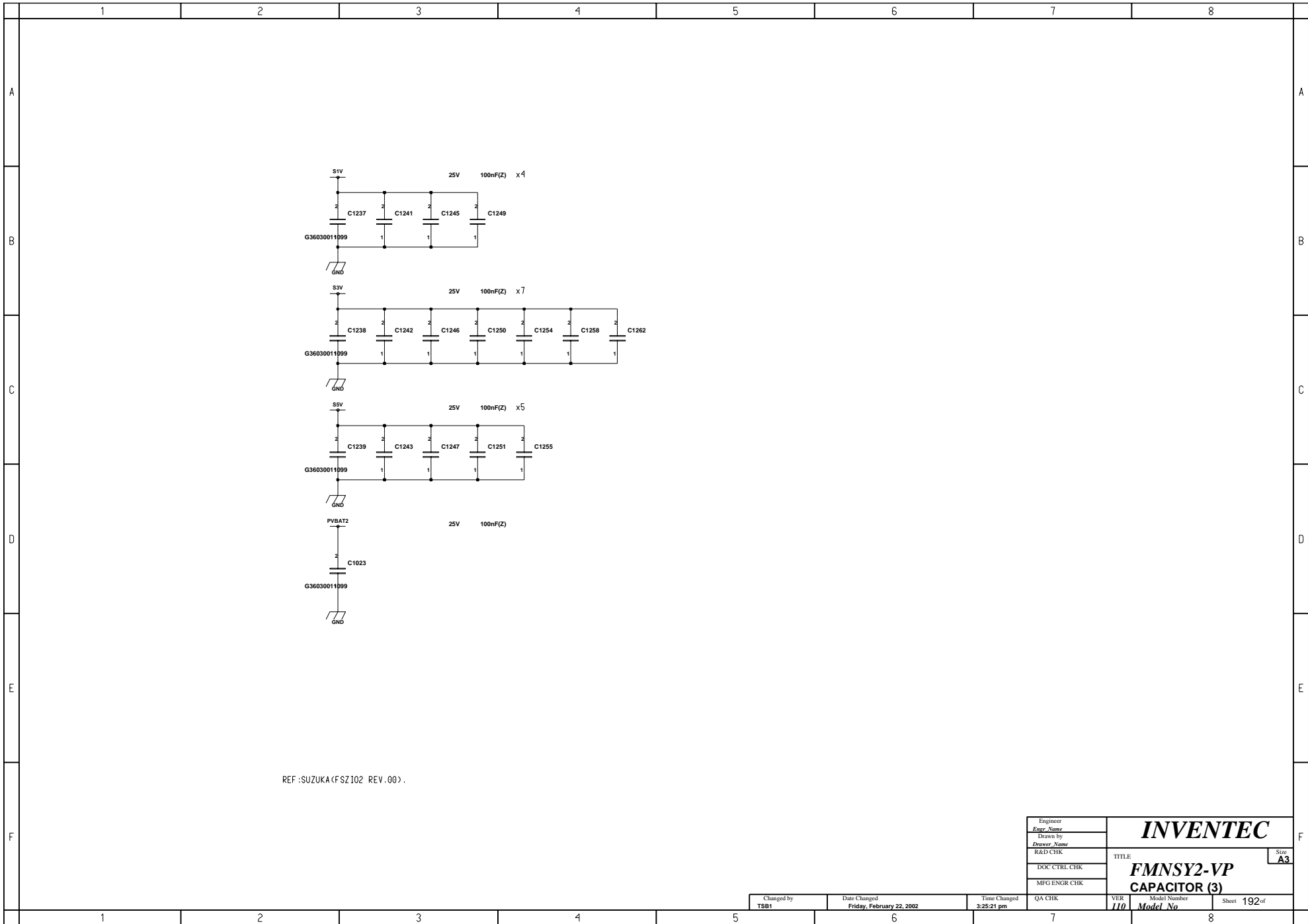
Engineer	
Engr Name	
Drawn by	
Drawn Name	
R&D CHK	
DOC CTRL CHK	
MFG ENGR CHK	
TITLE	<b>INVENTEC</b>
Model Number	<b>FMNSY2-VP</b>
Sheet	<b>190</b> of <b>190</b>
Size	<b>A3</b>

Changed by	Date Changed	Time Changed	QA CHK	VER	Model Number	Sheet
TSB1	Friday, February 22, 2002	3:25:12 pm		110	Model No	190 of



C1219,C1223,C1227,C1231,C1235pXRGNDSDNDX 11/23

Engineer <i>Engr Name</i>	<b>INVENTEC</b>	
Drawn by <i>Drawer Name</i>		
R&D CHK	TITLE	Size <b>A3</b>
DOC CTRL CHK	<b>FMNSY2-VP</b>	
MFG ENGR CHK	<b>CAPACITOR (2)</b>	
Changed by <b>TSB1</b>	Date Changed <b>Friday, February 22, 2002</b>	Time Changed <b>3:25:17 pm</b>
QA CHK	VER <b>110</b>	Model Number <i>Model No</i>
		Sheet <b>191</b> of



REF :SUZUKA <FSZ102 REV.00> .

Engineer <i>Engr Name</i>	<b>INVENTEC</b>
Drawn by <i>Drawer Name</i>	
R&D CHK	
DOC CTRL CHK	
MFG ENGR CHK	
TITLE	<b>FMNSY2-VP</b>
	<b>CAPACITOR (3)</b>

Size  
**A3**

Changed by <b>TSB1</b>	Date Changed <b>Friday, February 22, 2002</b>	Time Changed <b>3:25:21 pm</b>	QA CHK	VER <b>110</b>	Model Number <i>Model No</i>	Sheet <b>192</b> of
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SHT.	Power Signal	Voltage	I <sub>static</sub>	I <sub>Limit</sub>	Power on	SKIP Mode	Other
204	C3V	3.3V	4.5A		CVON-S5N	PVON-S5P	
205	E3V				EVON-S5N		
	B3V				BVONCP-EBP		
	P3V				PVONCP-EBP		
204	C5V	5.0V	7.5A	8.4A	CVON-S5N	PVON-S5P	
205	E5V				EVON-S5N		
	B5V				BVONCP-EBP		
	P5V				PVONCP-EBP		
	SND-P5V	PVON-S5P					
206	15-EBV	11.5V	1mA		E5V		
209	S1V	1.8V	100mA		SVON-S5N		
209	S3V	3.3V	100mA		SVON-S5N		
209	S5V	5.0V	50mA		SVON-S5N		
209	M5V	5.0V	100mA		PVD/PBV		
209	MCV	5.0V	25mA		PVD/PBV		
213	PPV	1.0	30.0A	40.0A	ONCPU-E3P	Disable	IMVP3
214		1.3V					
215	1R2-P1V	1.20V	300mA		ONVID-E5P		
216	2R5-E2V	2.51V	4.0A	4.0A	EVON-S5P	Enable	
218	2R5-B2V				BVONCP-EBP		
217	1R25-E1V	2R5V/2	3.0A	3.0A	EVON-S5P	PVON-S5P	
218	1R25-B1V				BVONCP-EBP		
217	1R25VR-E1V	2R5V/2			EVON-S5P		DDR t@Xpd
218	1R25VR-B1V				BVONCP-EBP		
219	1R5-E1V				EVON-S5P		
218	1R5-P1V	1.51V	2.0A	3.0A	PVONCP-EBP	Enable	
220	1R8-E1V	1.81V	2.0A	3.0A	EVON-S5P	Enable	
205	1R8-P1V				PVONCP-EBP		
221	FAN VCC	3.3V	400mA				
222	R3V	2.0~3.5V	0.1mA				
	VGA VCC(1)						VGA Core
	VGA VCC(2)						VGA VRAM

Engineer		<b>INVENTEC</b>
Engr. Name		
Drawn by		
Drawer Name		
R&D CHK		TITLE: <b>FMNSY2-VP</b>
DOC CTRL CHK		<b>[PS] SPEC SHEET</b>
MFG ENGR CHK		Model Number

Changed by	Date Changed	Time Changed	QA CHK	VER	Model Number	Sheet
TSB1	Friday, February 22, 2002	3:25:24 pm		110	Model No	200 of

A

B

C

D

E

F

A

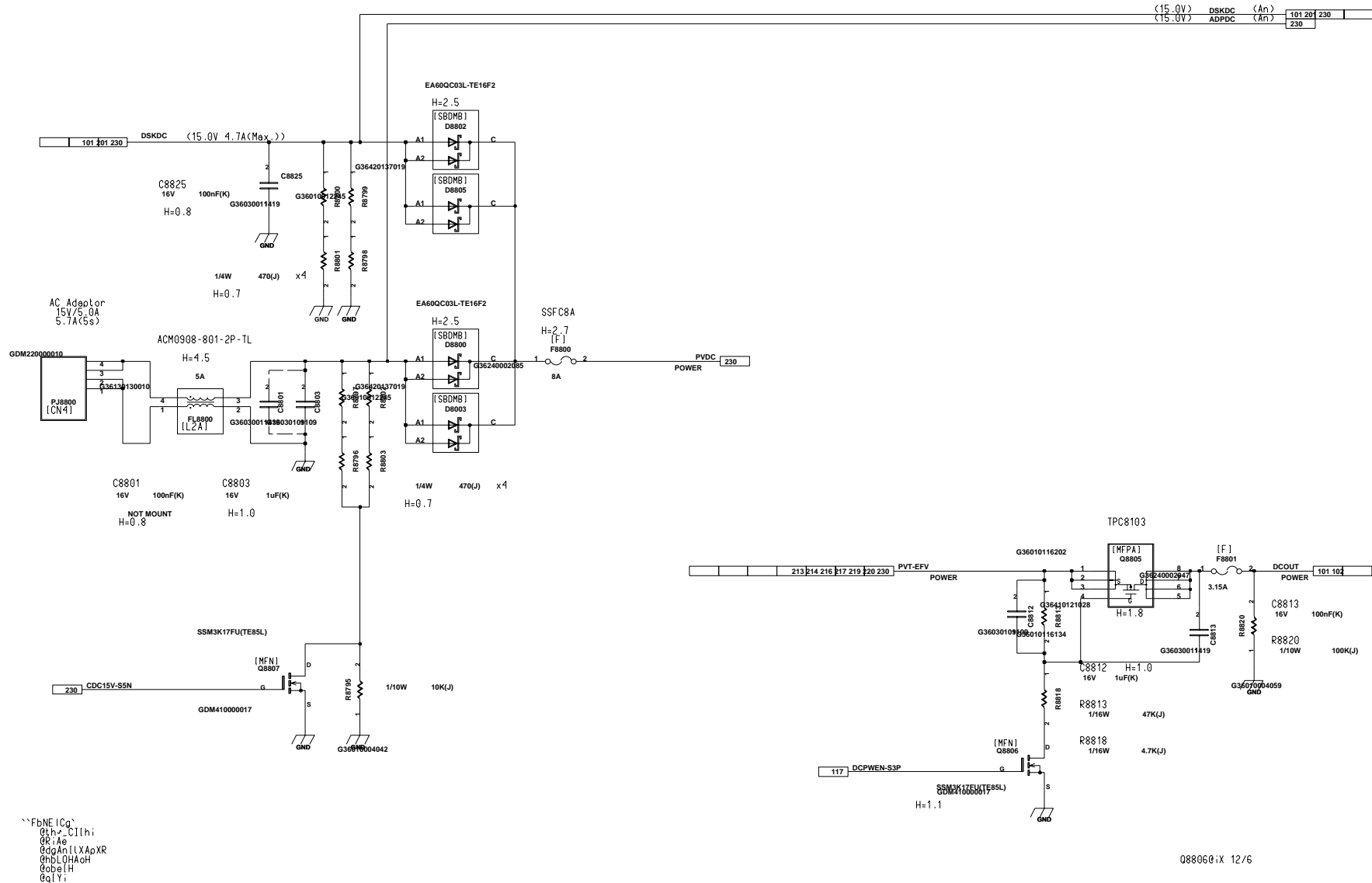
B

C

D

E

F



..FbNEICq  
 @h=CIIh;  
 OR Ae  
 @gAnlIXApXR  
 @hLOHAoh  
 @deIH  
 @qLY;

Engineer	
Drawn by	
R&D CHK	
DOC CTRL CHK	
MFG ENGR CHK	

**INVENTEC**

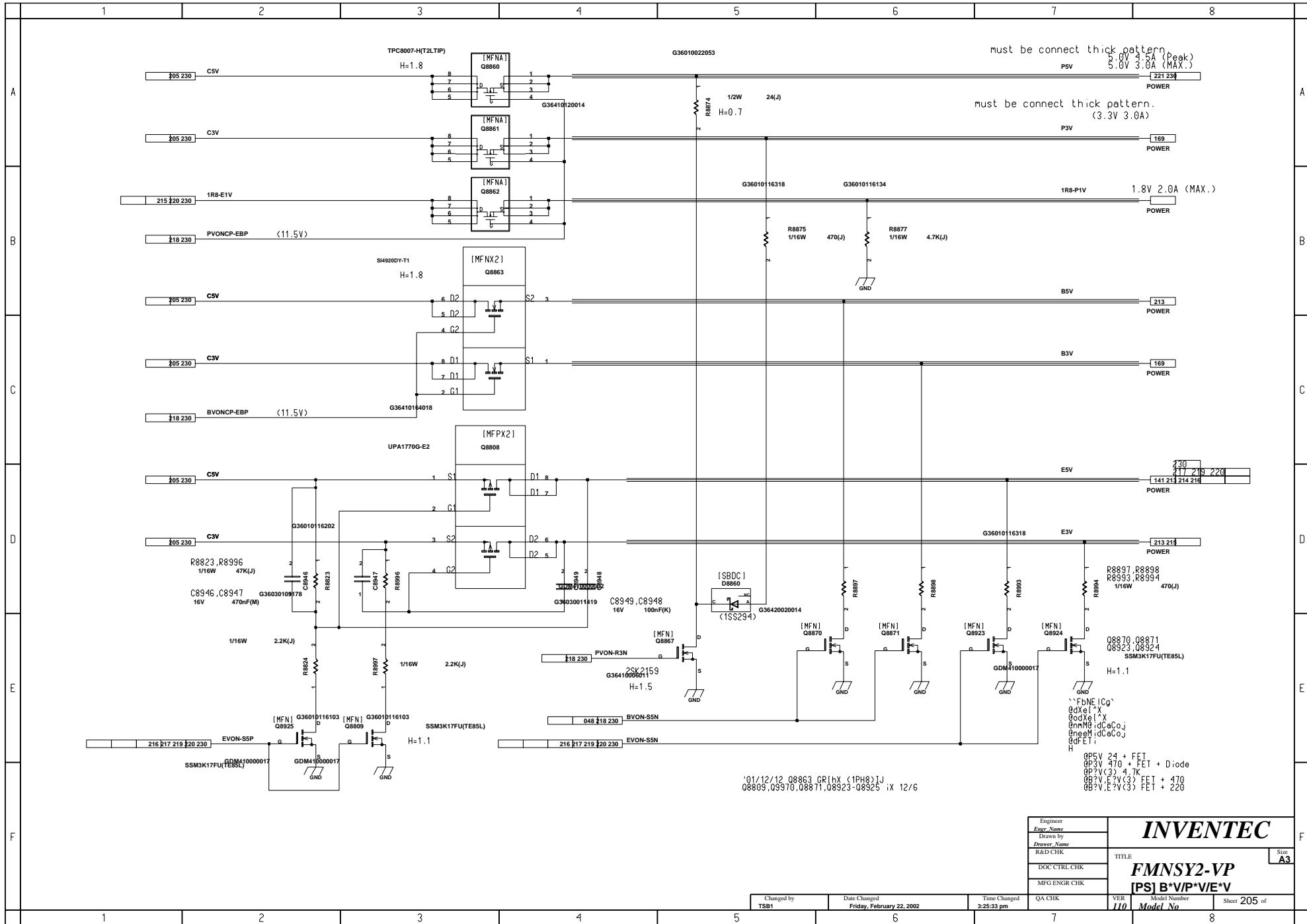
TITLE: **FMNSY2-VP**

**[PS] DC-IN**

Model No

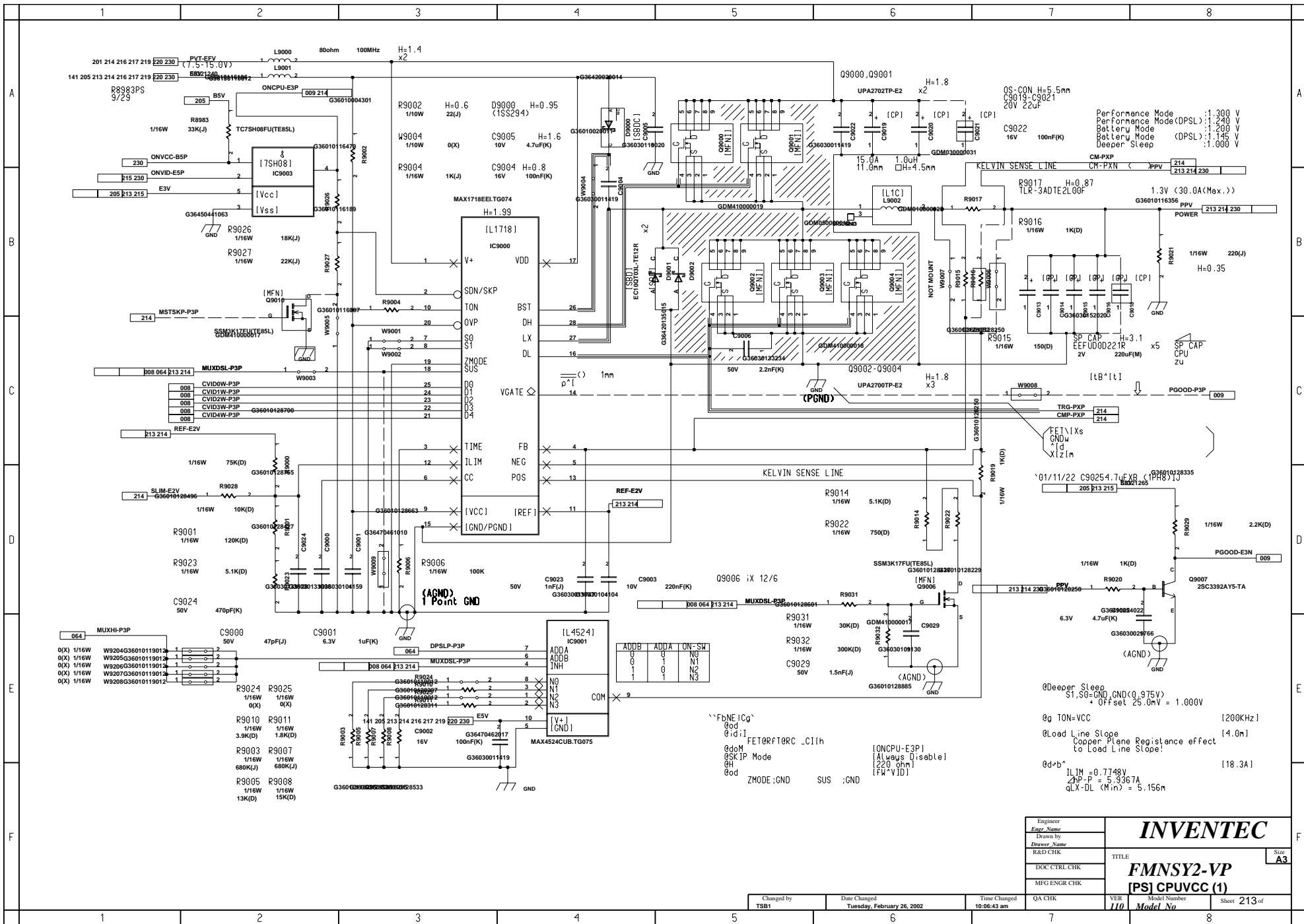
Sheet 201 of

Changed by	Date Changed	Time Changed	QA CHK	VER	Model Number	Sheet
TSB1	Friday, February 22, 2002	3:25:28 pm		110		201 of



Engineer	
Eng. Name	
Drawn by	
Drawer Name	
R&D CHK	
DOC CTRL. CHK	
MFG ENGR. CHK	
TITLE	<b>INVENTEC</b>
	<b>FMNSY2-VP</b>
	<b>[PS] B*V/P*V/E*V</b>
VER	110
Model Number	Model No
Sheet	205 of

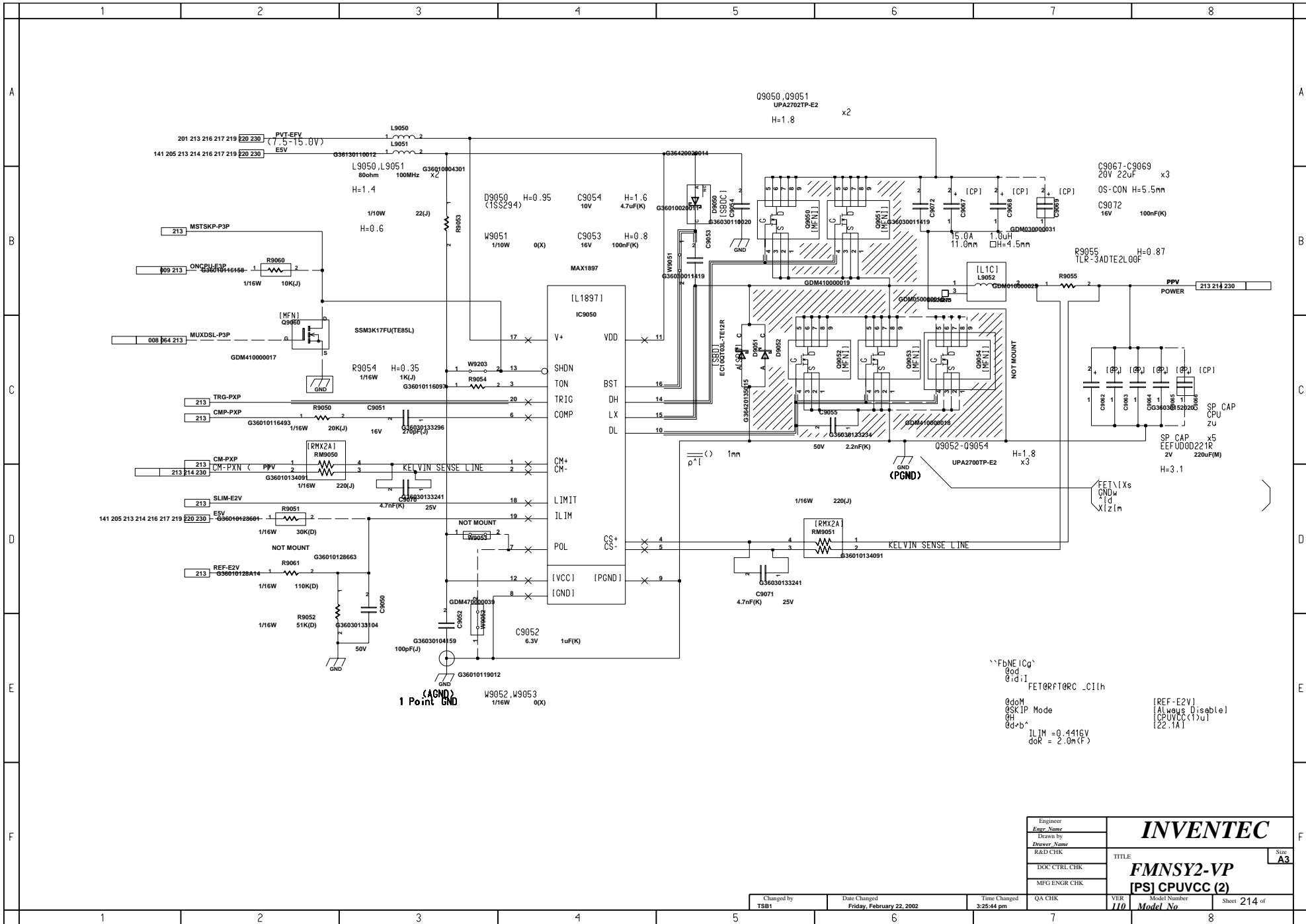
Changed by	Date Changed	Time Changed	QA CHK	VER	Model Number	Sheet
TSB1	Friday, February 22, 2002	3:25:33 pm		110	Model No	205 of



Engineer  
Eng Name  
Drawn by  
Drawer Name  
R&D CHK  
DOC CTRL CHK  
MFG ENGR CHK

TITLE: **FMNSY2-VP [PS] CPUVC (1)**

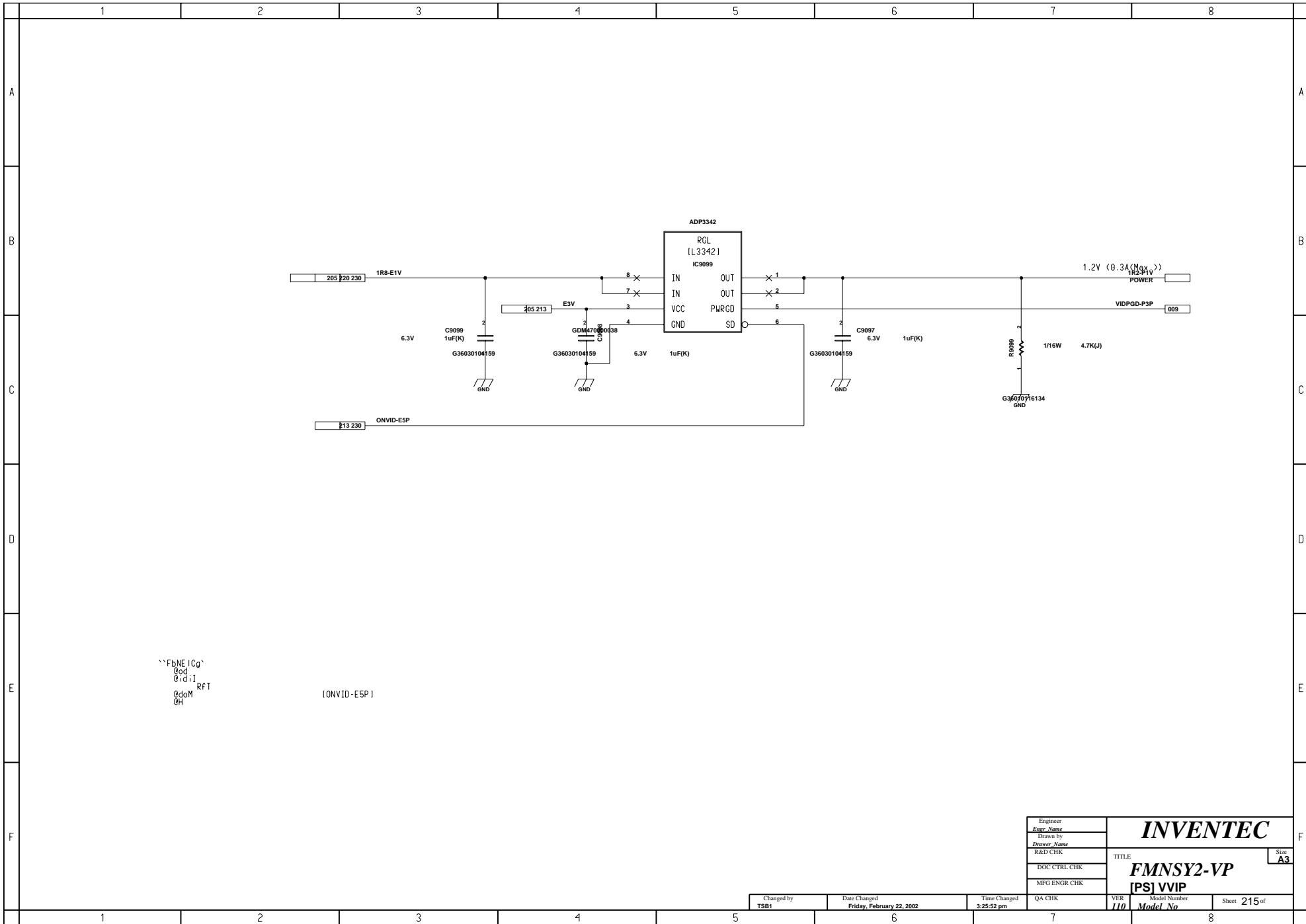
Size: **A3**



FbNE ICg  
 God  
 0:di  
 FET0RFT0RC\_C11h  
 0doM  
 0SKIP Mode  
 0d  
 0d+b  
 ILIM = 0.4416V  
 doR = 2.0n(F)

Engineer		<b>INVENTEC</b>	Size <b>A3</b>
Engr Name			
Drawn by			
Drawn Name			
R&D CHK			
DOC CTRL CHK	TITLE	<b>FMNSY2-VP</b>	
MFG ENGR CHK		<b>[PS] CPUVC (2)</b>	
Changed by	Date Changed	Time Changed	QA CHK
TSB1	Friday, February 22, 2002	3:25:44 pm	
VER	Model Number	Sheet 214 of	
110	Model No		



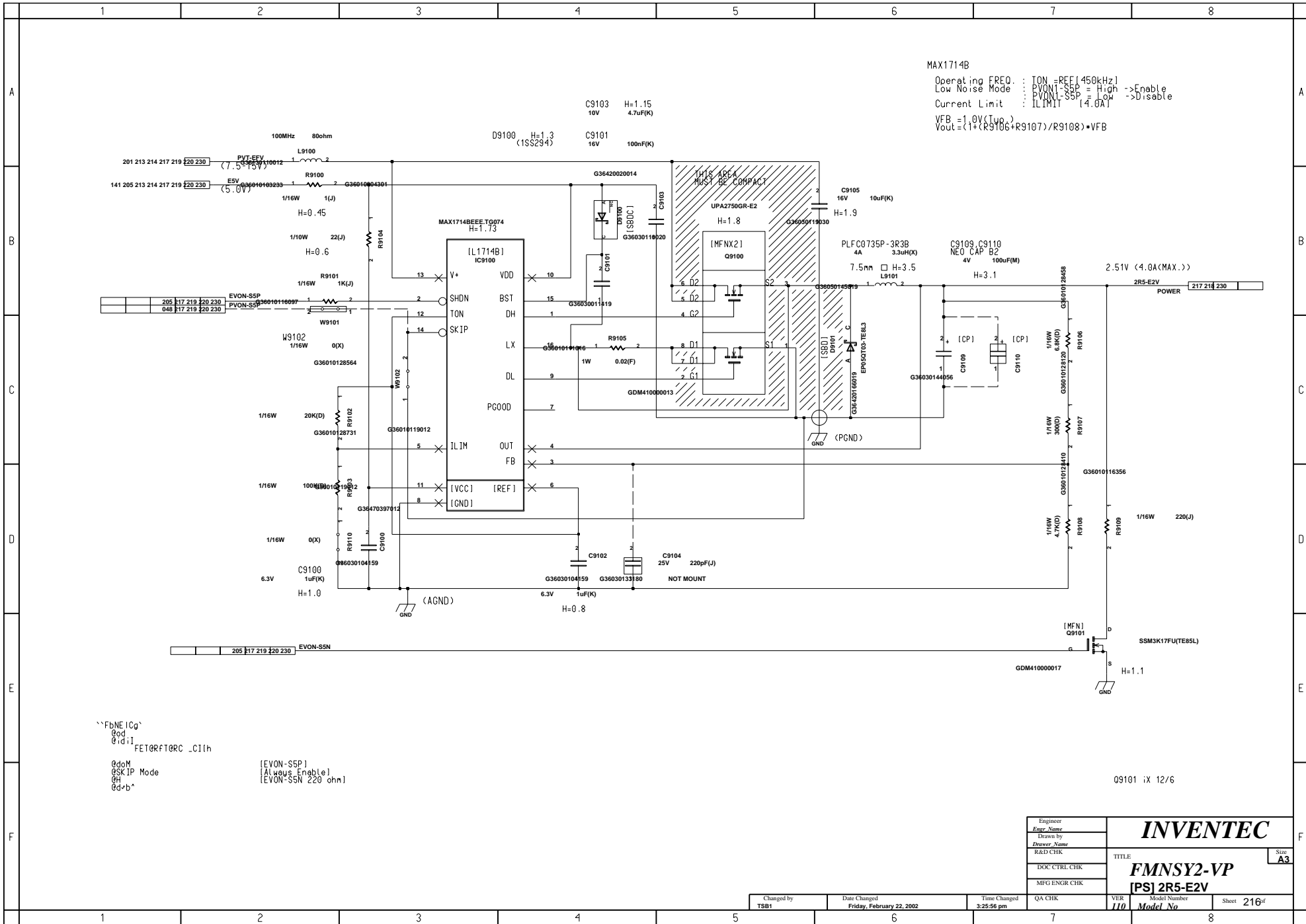


``FbNE |Cq`  
 Rod  
 0 d i l  
 0 do M RFT  
 0H

IONVID-ESP I

Engineer	
Engr Name	
Drawn by	
Drawer Name	
R&D CHK	
DOC CTRL CHK	
MFG ENGR CHK	
TITLE	INVENTEC
	FMNSY2-VP
	[PS] VVIP
Size	A3

Changed by	Date Changed	Time Changed	QA CHK	VER	Model Number	Sheet
TSB1	Friday, February 22, 2002	3:25:52 pm		110	Model No	215 of



MAX1714B  
 Operating FREQ. : TON = REF(450kHz)  
 Low Noise Mode : EVON1-SSP = High -> Enable  
 Current Limit : ILIMIT [4.0A]  
 VFB = 1.0V(Typ)  
 Vout = (1 + (R9106 + R9107) / R9108) \* VFB

201	213	214	217	219	220	230	
PVT-EV1							
(7.5 Ohm)							
141	205	213	214	217	219	220	230
ESV							
(5.0V)							

205	17	219	220	230
EVON-SSP				
048	17	219	220	230
EVON-SSP				

205	17	219	220	230
EVON-SSP				

205	17	219	220	230
EVON-SSN				

``FbNE ICg`  
 @od  
 @idi1  
 FET@RFT@RC\_C11h  
 @doM  
 @SKIP Mode  
 @h  
 @d+b`

[EVON-SSP]  
 [Always Enable]  
 [EVON-SSN 220 ohm]

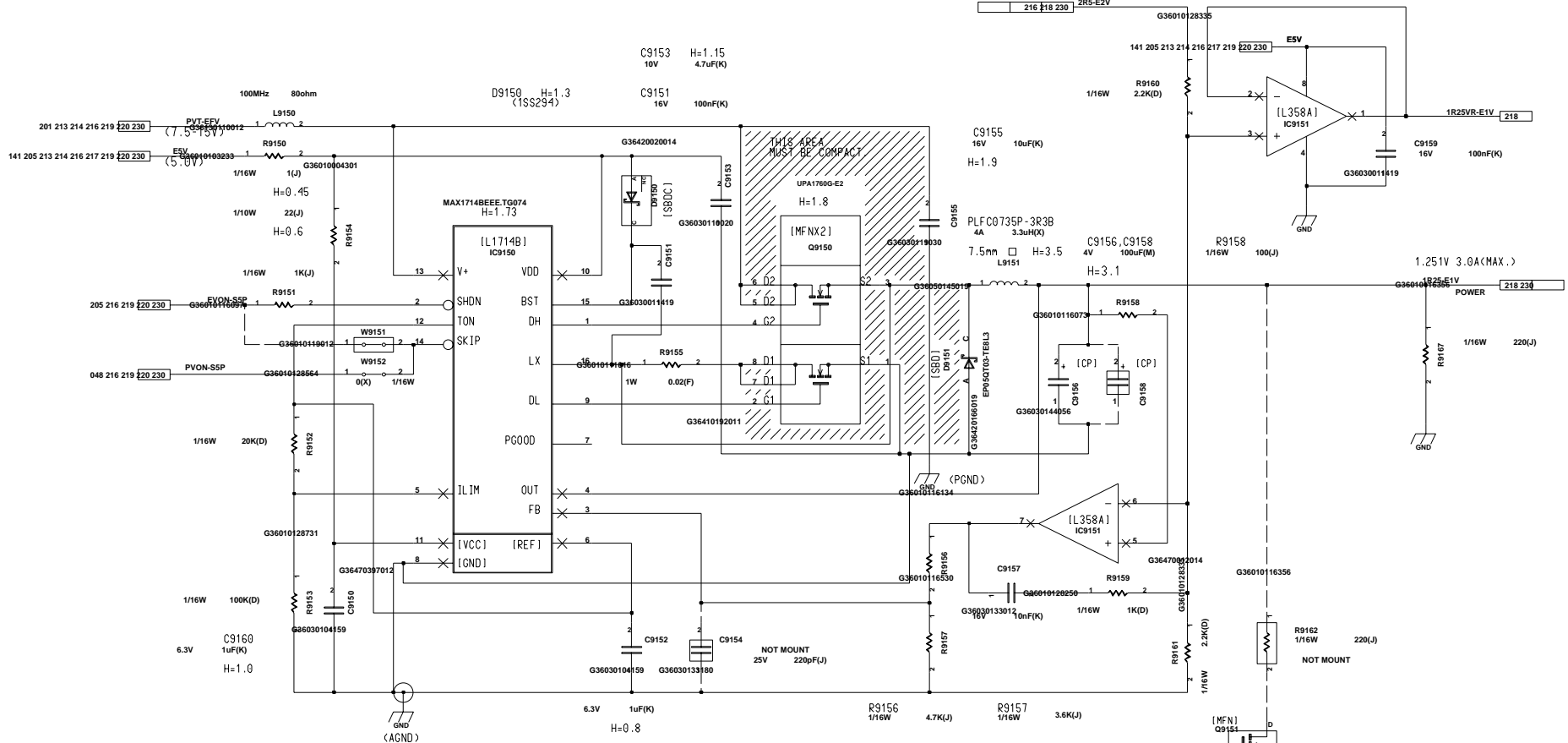
Q9101 ix 12/6

Engineer		<b>INVENTEC</b> TITLE: <b>FMNSY2-VP</b> <b>[PS] 2R5-E2V</b>	Sheet	216 of 4
Drawn by			Model Number	
Drawn Name			Model No	
R&D CHK			Size	A3
DOC CTRL CHK				
MFG ENGR CHK				
Changed by	Date Changed	Time Changed	QA CHK	VER
TSB1	Friday, February 22, 2002	3:25:56 pm		110

01/11/14 OP-AMPL358X (1PH8)JJ

MAX1714B  
Operating FREQ. : TON=REF(450kHz)  
Low Noise Mode : SKIP=ON(Disable)  
Current Limit : ILIMIT(3.0A)

[RF TLA  
UAT00V1YR]



EVON-SSN

```

FbNEICg
Gnd
0id1
FET0RF0RC_C11h

GdnM
GSKIP Mode
GH
Gdb*
  
```

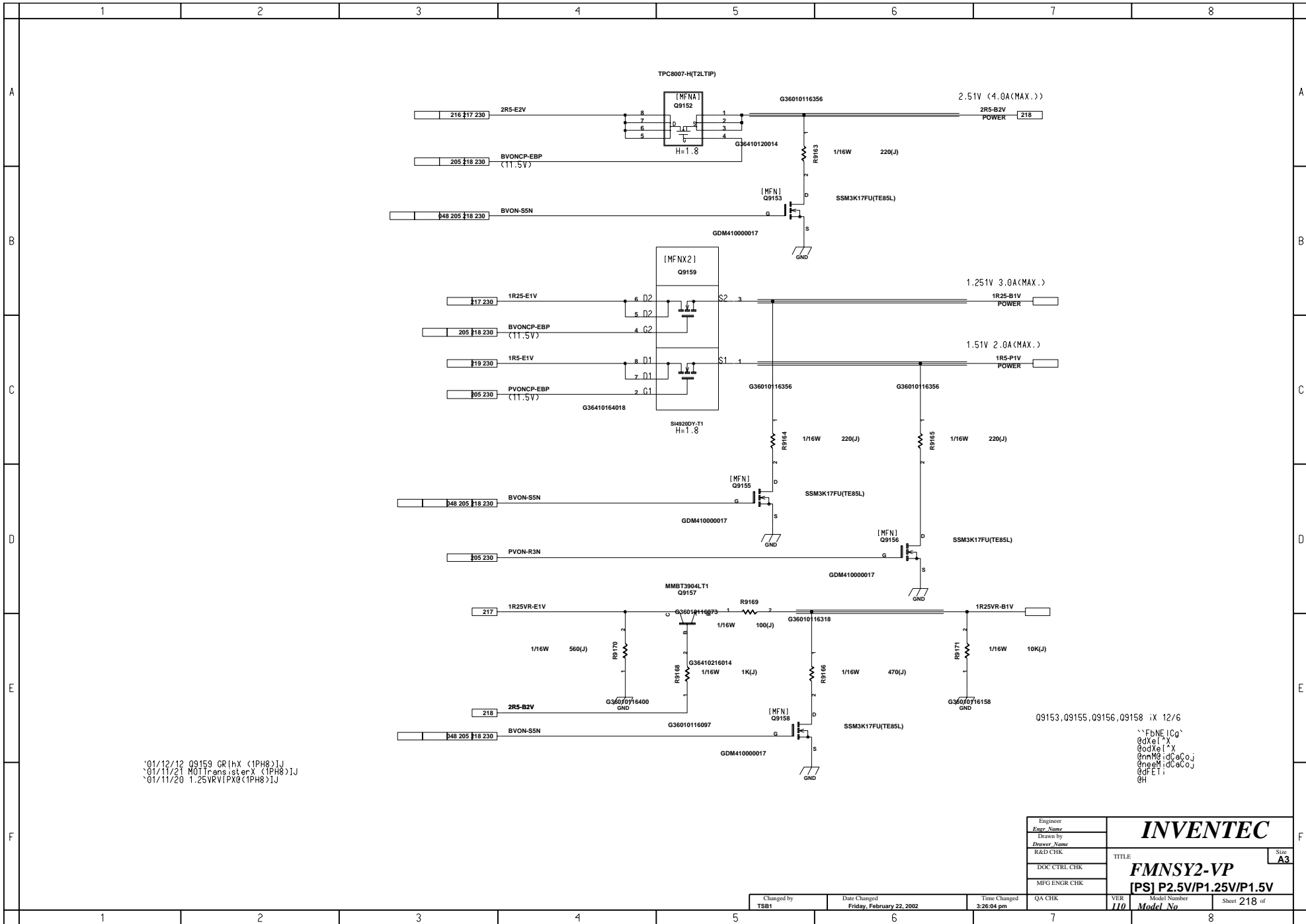
```

[EVON-SSP]
[PVON-SSP]
[EVON-SSN 220 ohm]
  
```

09151 :X 12/6

Engineer	
Engr Name	
Drawn by	
Drawer Name	
R&D CHK	
DOC CTRL CHK	
MFG ENGR CHK	
TITLE	<b>INVENTEC</b>
	<b>FMNSY2-VP</b>
	<b>[PS] 1R25V-E1V</b>
Model Number	Sheet 217 of
Model No	Size A3

Changed by	Date Changed	Time Changed	QA CHK	VER	Model Number	Sheet
TSB1	Friday, February 22, 2002	3:26:00 pm		110	Model No	217 of



01/12/12 Q9159 GR(hX (1PH8)1J  
 01/11/21 M01TransistorX (1PH8)1J  
 01/11/20 1.25VRV(PX0(1PH8)1J

09153,09155,09156,09158 :X 12/6  
 \FbNEICq`  
 qdXeI`X  
 @oXeI`X  
 @mM`dCaCoJ  
 @neM`dCaCoJ  
 qdFETi  
 @H

Engineer	
Engr Name	
Drawn by	
Drawer Name	
R&D CHK	
DOC CTRL CHK	
MFG ENGR CHK	
TITLE	Size
<b>FMNSY2-VP</b>	<b>A3</b>
<b>[PS] P2.5V/P1.25V/P1.5V</b>	

Changed by	Date Changed	Time Changed	QA CHK	VER	Model Number	Sheet
TSB1	Friday, February 22, 2002	3:26:04 pm		110	Model No	218 of

A

B

C

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E

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A

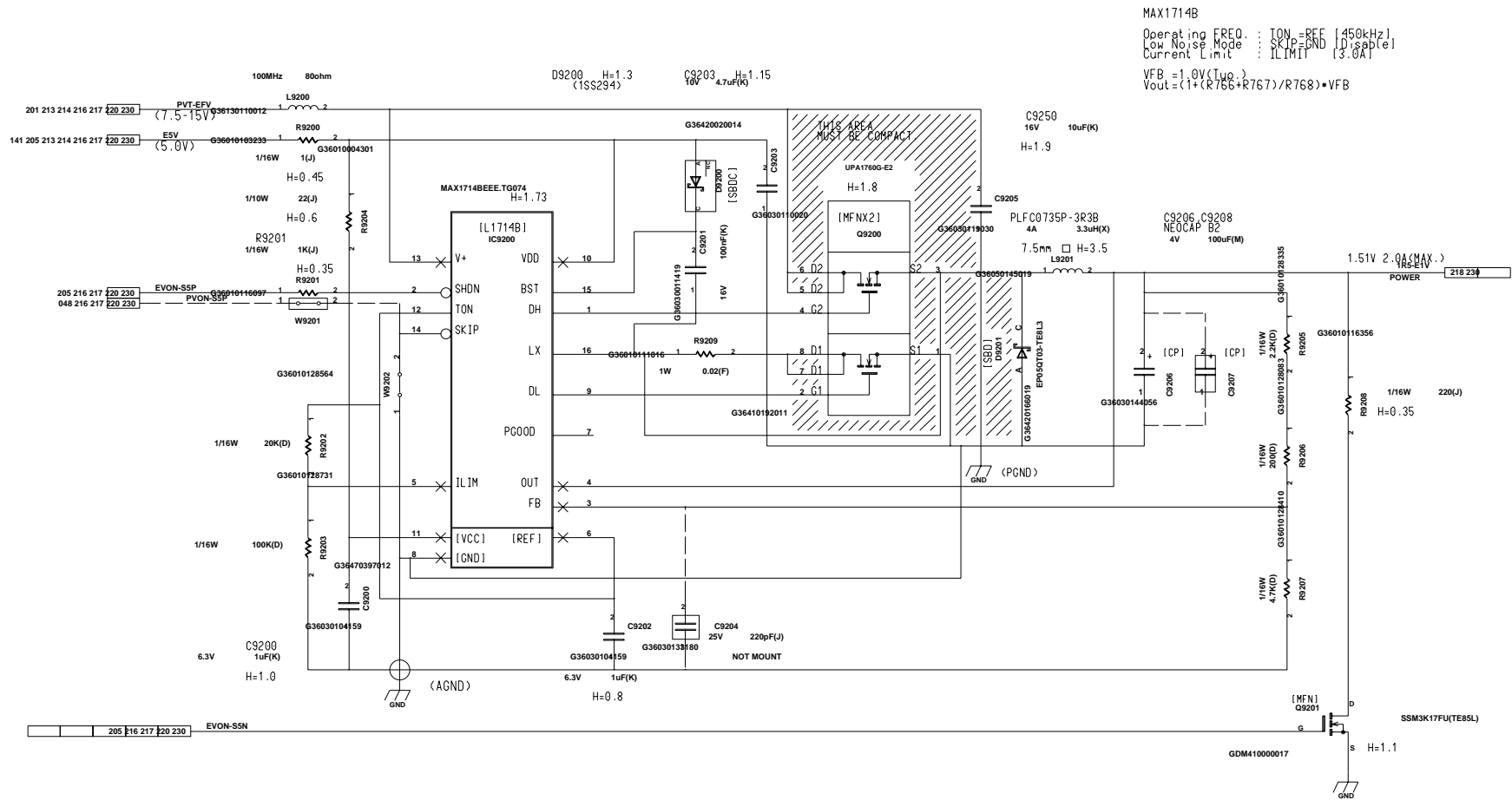
B

C

D

E

F



MAX1714B  
 Operating FREQ. : TON = REF [450kHz]  
 Low Noise Mode : SKIP GND [D] sab(e)  
 Current Limit : ILIMIT [3.0A]  
 VFB = 1.0V(LUG.)  
 Vout = (1+(R766+R767)/R768) \* VFB

```

FbNE[Cg]
@od
@id;1
FETORFTORC .C1ih

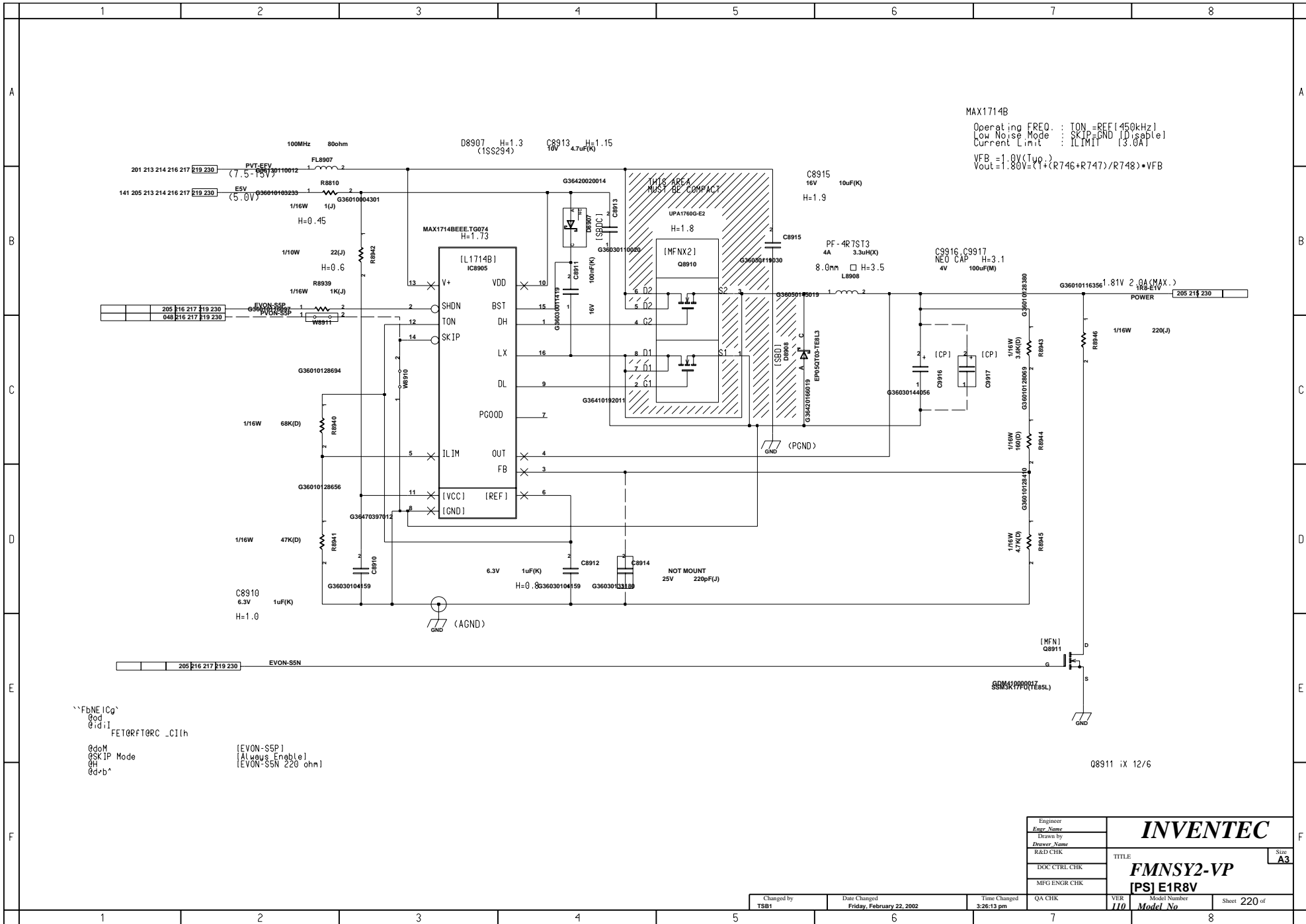
@odM
@SKIP Mode
@H
@db*

[EVON-SSP]
[Always Enable]
[EVON-SSN 220 ohm]
    
```

09201 iX 12/6

Engineer Eng Name	<b>INVENTEC</b> TITLE: <b>FMNSY2-VP</b> <b>[PS] E1R5V</b>
Drawn by Drawer Name	
R&D CHK	
DOC CTRL CHK	
MFG ENGR CHK	

Changed by TSB1	Date Changed Friday, February 22, 2002	Time Changed 3:26:09 pm	QA CHK	VER 110	Model Number Model No	Sheet 219 of
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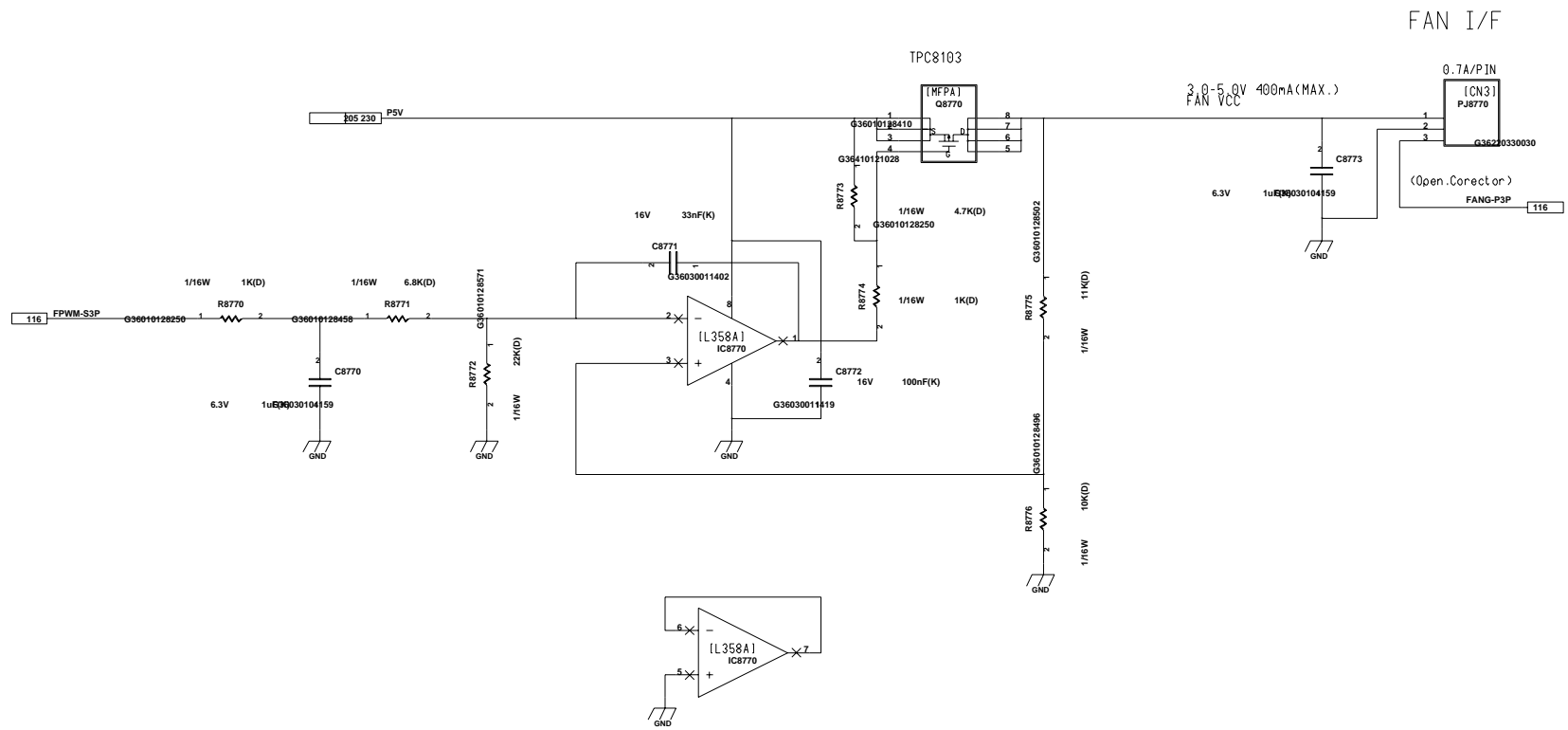
MAX1714B  
 Operating FREQ : TON=REF(450kHz)  
 Low Noise Mode : SKIP=NO (Disable)  
 Current Limit : ILIMIT (3.0A)  
 VFB = 1.0V(Typ.)  
 Vout=1.80V=V\*(R746+R747)/R748=VFB

``FbNE ICg`  
 0d`  
 0`d`i`  
 FET0RFT0RC \_CI1h  
 0daM  
 0SKIP Mode  
 0H  
 0d`b`  
 [EVON-SEP]  
 [Always Enable]  
 [EVON-SEN 220 ohm]

08911 iX 12/6

Engineer		<b>INVENTEC</b>
Drawn by		
R&D CHK		
DOC CTRL CHK		
MFG ENGR CHK		
TITLE	<b>FMNSY2-VP</b>	
	<b>[PS] E1R8V</b>	
Model Number		Size <b>A3</b>
Sheet 220 of		

Changed by	Date Changed	Time Changed	QA CHK	VER	Model Number	Sheet
TSB1	Friday, February 22, 2002	3:26:13 pm		110	Model No	220 of



``FbNE ICg`  
 @RLN`  
 @EC/KBC/JL

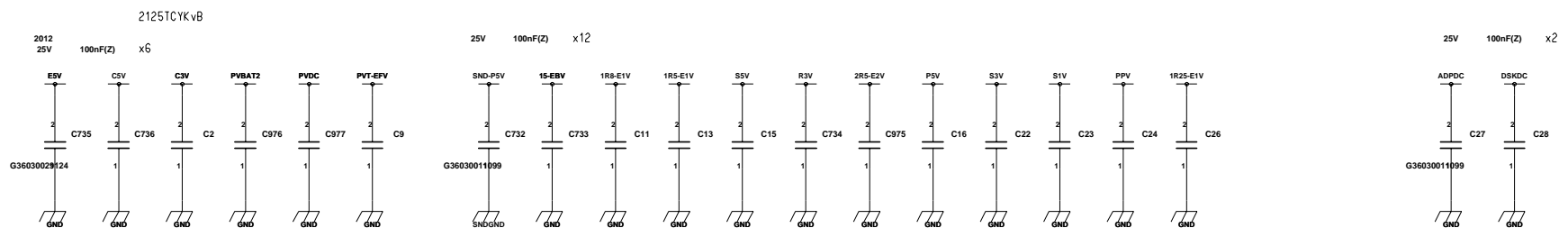
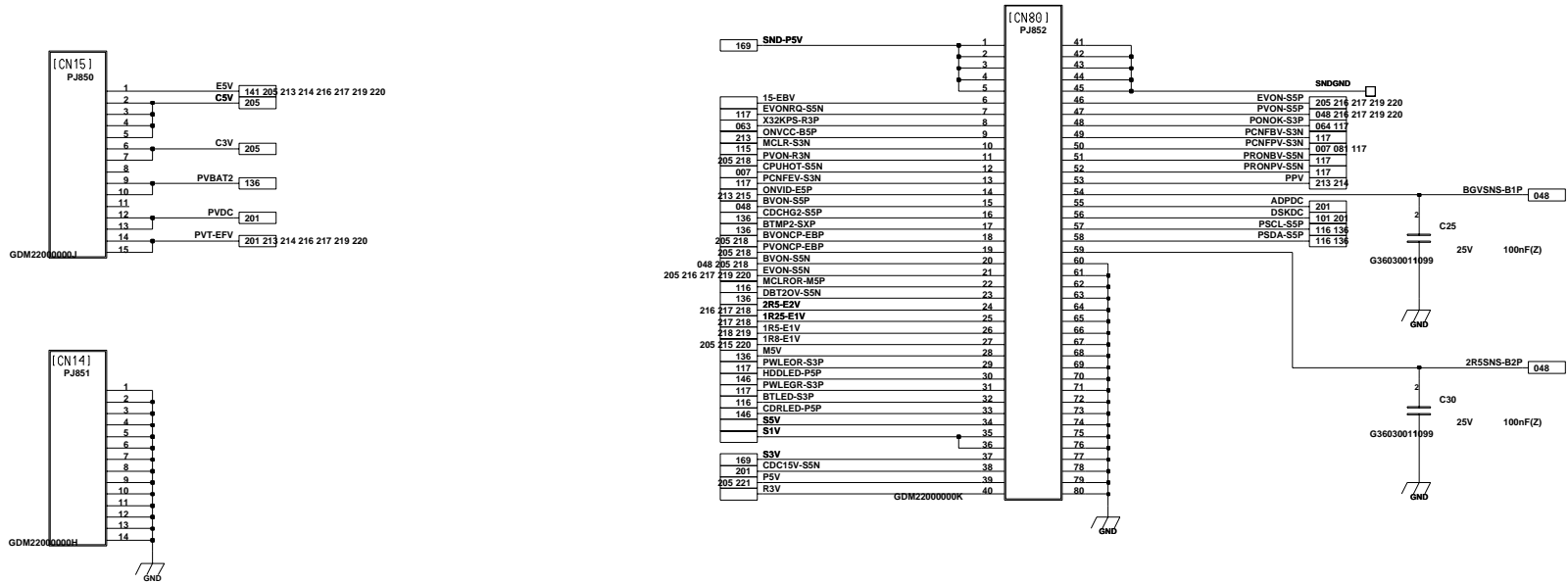
	OHEC	EC
VCC	3.3V	5.0V
FPWM-S3P	0-3.3V	0-5.0V
R8771	6.8K(D)	10K(D)
R8772	22K(D)	10K(D)

PJ8770 G36220330030X010/19

Engineer Enger Name	<b>INVENTEC</b> TITLE: <b>FMNSY2-VP</b> <b>[PS] FANVCC</b>	Size <b>A3</b>
Drawn by Drawer Name		
R&D CHK		
DOC CTRL CHK		
MFG ENGR CHK		

Changed by TSB1	Date Changed Friday, February 22, 2002	Time Changed 3:26:17 pm	QA CHK	VER 110	Model Number Model No	Sheet 221 of
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01/12/5 1PH8-CR-01B020 <CDC15V-S5N38P.inj <1PH8>1J



PJB52 BGVBGVSN5-B1PXA  
 53P IN2R5SNS-B2P  
 C30 11/16  
 REF:Montreal10(FMOP2 REV.00)

FbNICg  
 KvMAtC'FbN  
 0h'e'FbN

Engineer Engy Name	<b>INVENTEC</b> TITLE: <b>FMNSY2-VP</b> <b>[PS] PS(FMNP5*) I/F</b>	Size <b>A3</b>				
Drawn by Drawer Name						
R&D CHK						
DOC CTRL CHK						
MFG ENGR CHK						
Changed by TSB1	Date Changed Friday, February 22, 2002	Time Changed 3:26:21 pm	QA CHK	VER 110	Model Number Model No	Sheet 230 of